



0.5A SBR SURFACE-MOUNT SUPER BARRIER RECTIFIER

Product Summary

VRRM (V)	lo (A)	V _{F (MAX)} (V) @ +25°C	I _{R (MAX)} (μA) @ +25°C	
100	0.5	1.0	20	

Features and Benefits

- Patented Trench Super Barrier Rectifier SBR[®] Technology
- With Visible and Solderable Side Pads
- Ultra-Low-Forward Voltage Drop
- Superior Reverse Avalanche Capability
- · Soft, Fast Switching Capability
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The SBR05100LPWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Description and Applications

Packaged in X1-DFN1006-2 (SWP) (Type C) package, provides very low V_F and excellent reverse-leakage stability at high temperatures. It is ideal for use as a rectifier, freewheel diode or blocking diode in:

- DC-DC converters
- AC-DC adaptors

Mechanical Data

- Package: X1-DFN1006-2
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.001g (Approximate)

X1-DFN1006-2 (SWP) (Type C)



Top View



Bottom View

Ordering Information (Note 4)

Orderable Part Number	Package	Packing		
Orderable Part Number	Package	Qty.	Carrier	
SBR05100LPWQ-7B	X1-DFN1006-2 (SWP) (Type C)	10,000	Tape & Reel	

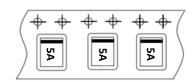
Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



5A = Product Type Marking Code Bar Denotes Cathode





Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	Vrrm Vrwm Vrm	100	>
Average Rectified Output Current	lo	500	mA
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine Wave Superimposed on Rated Load	IFSM	5	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Power Dissipation, T _A = +25°C (Note 5)	P _D	500	mW
Typical Power Dissipation, T _A = +25°C (Note 6)	PD	950	mW
Typical Thermal Resistance, Junction to Ambient, T _A = +25°C (Note 5)	Reja	250	°C/W
Typical Thermal Resistance, Junction to Ambient, T _A = +25°C (Note 6)	$R_{\theta JA}$	125	°C/W
Typical Thermal Resistance, Junction to Case, T _A = +25°C (Note 5)	Rejc	25	°C/W
Typical Thermal Resistance, Junction to Case, T _A = +25°C (Note 6)	Rejc	15	°C/W
Operating and Storage Temperature Range (Note 7)	TJ, TSTG	-55 to +150	°C

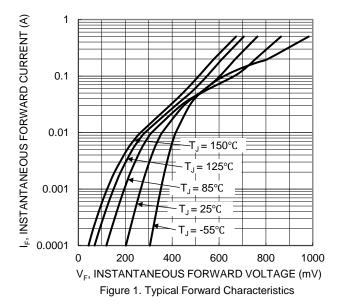
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

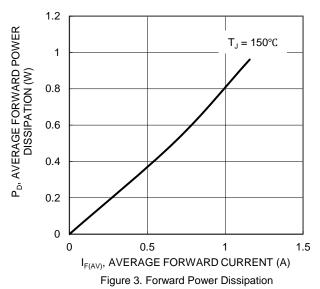
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop	VF		0.87 0.71	1.0 0.8	V	I _F = 500mA, T _J = +25°C I _F = 500mA, T _J = +125°C
Leakage Current (Note 8)	IR		0.1 0.03	20 2	μA mA	V _R = 100V, T _J = +25°C V _R = 100V, T _J = +125°C
Total Capacitance	Ст	-	5	_	pF	$V_R = 5V, f = 1MHz$
Reverse-Recovery Time	t _{RR}		11	_	ns	$I_F = 10 \text{mA}, I_{RR} = 0.1 I_R$ $T_A = +25 ^{\circ}\text{C}$

Notes:

- 5. 1*MRP FR-4 PC board 2oz. copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.
- 6. One-inch square copper pad 2oz.
- 7. The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D / dT_J < 1 / R_{\theta JA}$. 8. Short duration pulse test used to minimize self-heating effect.







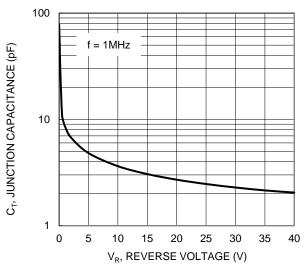


Figure 5. Typical Junction Capacitance

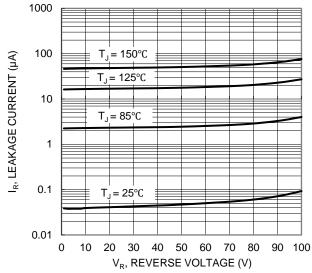


Figure 2. Typical Reverse Characteristics

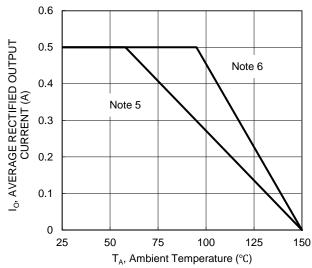


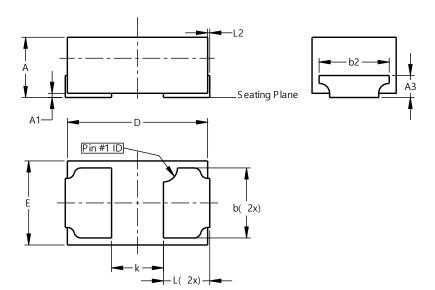
Figure 4. DC Forward Current Derating



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1006-2 (SWP) (Type C)

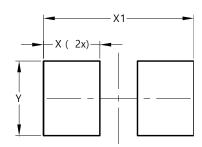


X1-DFN1006-2 (SWP)				
	(Ty	pe C)		
Dim	Min	Max	Тур	
Α	0.37	0.47	0.42	
A1	0.00	0.05	0.03	
А3	0.17 REF			
b	0.47	0.57	0.52	
b2	0.55 REF			
D	0.95	1.05	1.00	
Е	0.55	0.65	0.60	
k	0.37 REF			
L	0.28	0.38	0.33	
L2	L2 0.15 REF			
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X1-DFN1006-2 (SWP) (Type C)



Dimensions	Value (in mm)	
Х	0.45	
X1	1.20	
Υ	0.60	



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