Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max	I_D T_C = +25°C
1700V	0.85Ω @V_{GS} = 20V	6.58A

Features and Benefits

- High Blocking Voltage with Low On-Resistance
- High BV_{DSS} Rating for Power Application
- Low Input Capacitance
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The DMWSH170H850HM4Q is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**
  https://www.diodes.com/quality/product-definitions/

Description and Applications

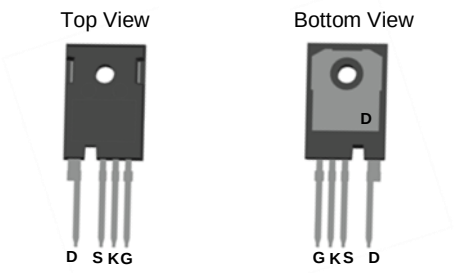
This SiC MOSFET is designed to minimize the on-state resistance yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- SMPS (switching mode power supplies)
- UPS (uninterruptable power supplies)
- DC-DC converters for EV/HEV
- Auxiliary power supplies
- On-board chargers (OBC)

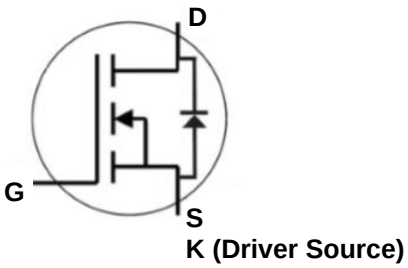
Mechanical Data

- Package: TO247-4
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 e3
- Weight: 6.6 grams (Approximate)

TO247-4 Standard



Pin Configuration



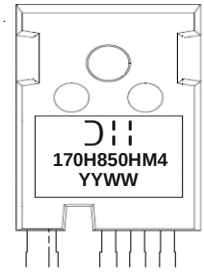
Internal Schematic

Ordering Information (Note 4)

Orderable Part Number	Package	Packing	
		Qty.	Carrier
DMWSH170H850HM4Q	TO247-4 Standard	30 Pieces	Tube

Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



YH = Manufacturer's Marking
170H850HM4 = Product Type Marking Code
YYWW or YYWW = Date Code Marking
YY or YY = Last Two Digits of Year (ex: 25 = 2025)
WW or WW = Week Code (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	1700	V
Gate-Source Voltage		$V_{GS(max)}$	-10/+25	V
Gate-Source Voltage (Recommended Operating Values)		$V_{GS(op)}$	-5/+20	V
Continuous Drain Current (Notes 5, 9)	T_C = +25°C T_C = +100°C	I_D	6.58 4.65	A
Continuous Diode Forward Current (Note 5)		I_S	11	A
Pulsed Source Current (10µs Pulse, Duty Cycle = 1%) (Note 5)		I_{SM}	11	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) (Note 5)		I_{DM}	19	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T_C = +25°C	P_D	73.5	W
	T_C = +100°C		36.6	
Thermal Resistance, Junction to Ambient (Note 6)		$R_{\theta JA}$	30.7	°C/W
Thermal Resistance, Junction to Case (Note 5)		$R_{\theta JC}$	2.04	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV_{DSS}	1700	—	—	V	V_{GS} = 0V, I_D = 100µA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	100	µA	V_{DS} = 1700V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±250	nA	V_{GS} = +20/-10V, V_{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(TH)}$	2.0	2.8	4.0	V	V_{DS} = V_{GS}, I_D = 0.5mA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	0.63	0.85	Ω	V_{GS} = 20V, I_D = 2A
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	0.68	—	Ω	V_{GS} = 18V, I_D = 2A
Diode Forward Voltage	V_{SD}	—	3.8	—	V	V_{GS} = -5V, I_S = 1A
Transconductance	gfs	—	60	—	S	V_{DS} = 20V, I_D = 2A
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C_{iss}	—	178	—	pF	V_{GS} = 0V, V_{DS} = 1000V, V_{AC} = 25mV, f = 1MHz
Output Capacitance	C_{oss}	—	11	—		
Reverse Transfer Capacitance	C_{rss}	—	1.1	—		
C_{oss} Stored Energy	E_{oss}	—	7.1	—	µJ	
Turn-On Switching Energy (Body Diode Forward)	E_{ON}	—	56	—	µJ	V_{GS} = -5V/+20V, V_{DS} = 1.2kV, Rg = 2.5Ω, I_D = 2A, L = 1.4mH
Turn-Off Switching Energy (Body Diode Forward)	E_{OFF}	—	31	—		
Gate Resistance	R_g	—	5.1	—	Ω	V_{AC} = 25mV, f = 1MHz, I_D = 0A
Total Gate Charge	Q_g	—	14.8	—	nC	V_{GS} = -5V/+20V, V_{DS} = 1.2kV, I_D = 2A
Gate-Source Charge	Q_{gs}	—	3.0	—		
Gate-Drain Charge	Q_{gd}	—	6.3	—		
Turn-On Delay Time	$t_{D(ON)}$	—	6.4	—	ns	V_{GS} = -5V/+20V, V_{DD} = 1.2kV, Rg = 2.5Ω, I_D = 2A, R_L = 600Ω
Turn-On Rise Time	t_R	—	11.4	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	9.2	—		
Turn-Off Fall Time	t_F	—	68	—		
Body Diode Reverse-Recovery Time	t_{RR}	—	40	—	ns	V_{GS} = -5V, V_{DS} = 1000V, I_D = 20A, di/dt = 1000A/µs
Body Diode Reverse-Recovery Charge	Q_{RR}	—	59	—	nC	
Body Diode Reverse-Recovery Current	I_{RRM}	—	2.2	—	A	

Notes:
5. Device mounted on an infinite heatsink.
6. Device mounted on FR-4 substrate PC board, 2oz. copper, with minimum recommended pad layout.
7. Guaranteed by design. Not subject to production testing.
8. Short duration pulse test used to minimize self-heating effect.
9. Drain current limited by maximum junction temperature.

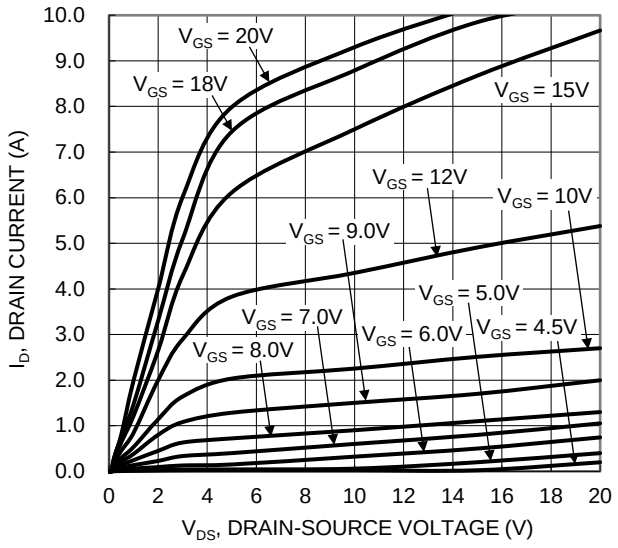


Figure 1. Typical Output Characteristic

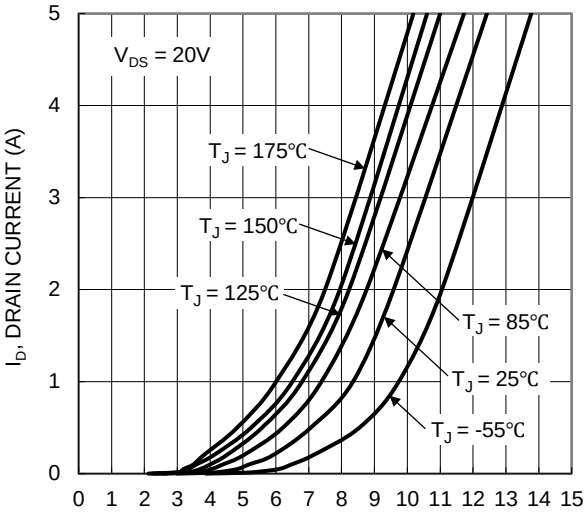


Figure 2. Typical Transfer Characteristic

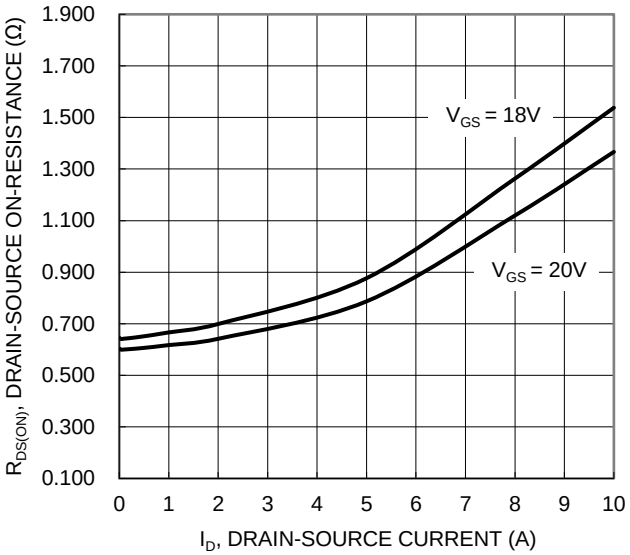


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

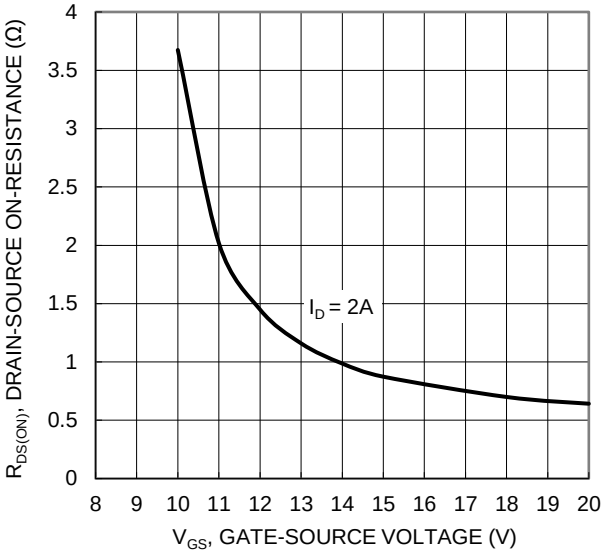


Figure 4. Typical Transfer Characteristic

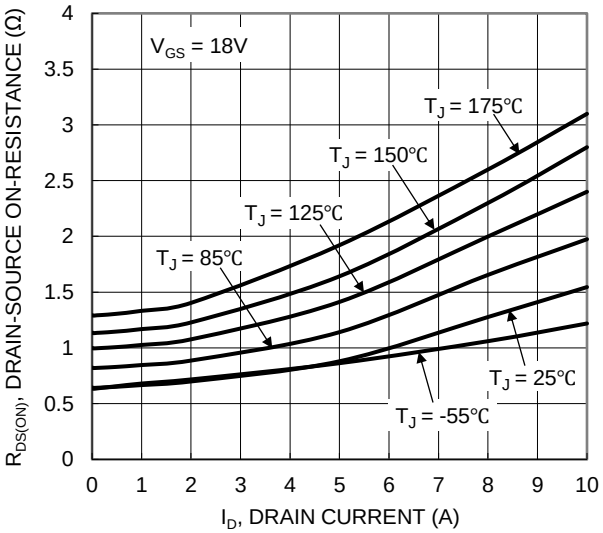


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

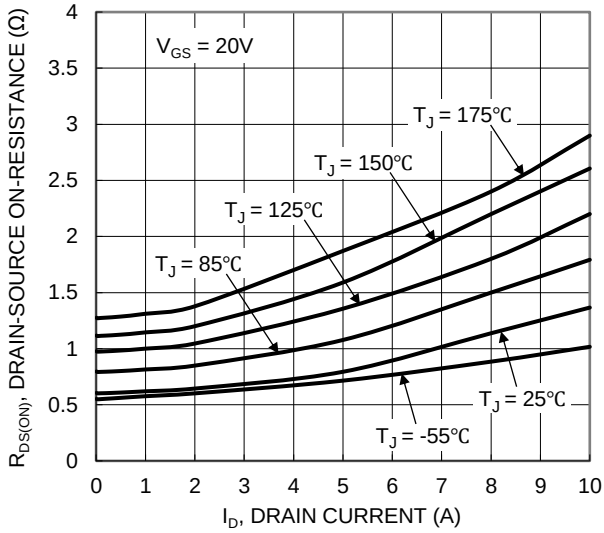


Figure 6. Typical On-Resistance vs. Drain Current and Temperature

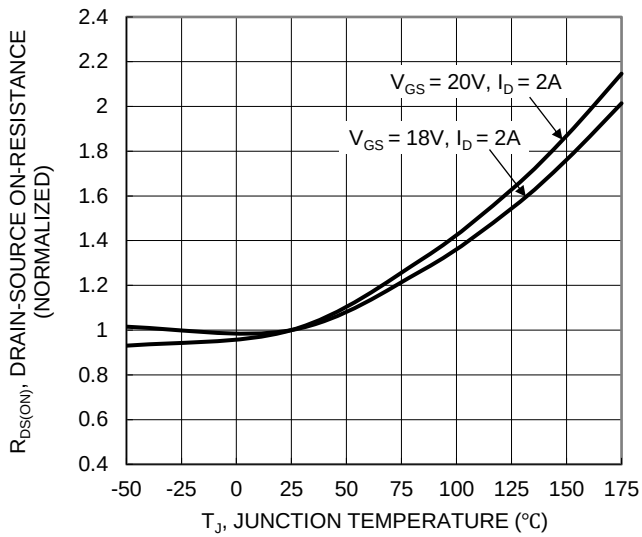


Figure 7. On-Resistance Variation with Temperature

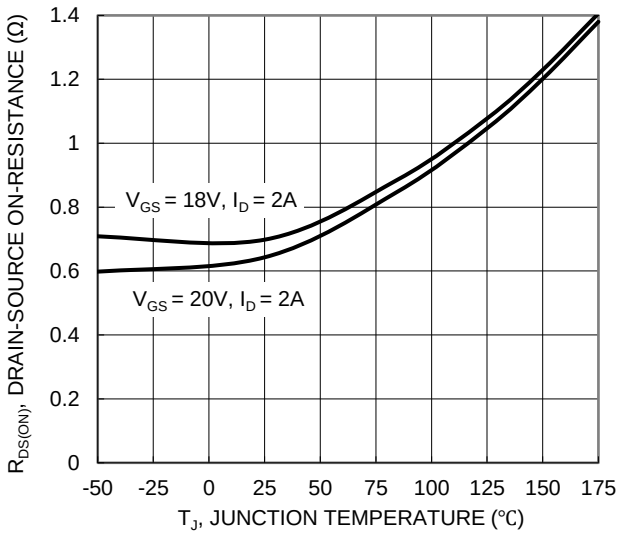


Figure 8. On-Resistance Variation with Temperature

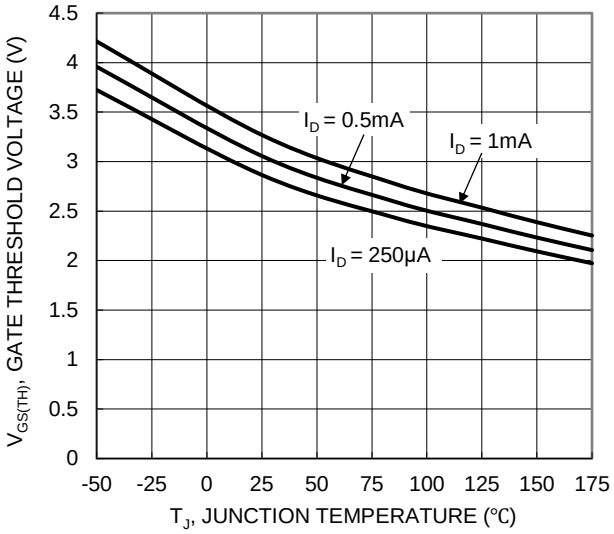


Figure 9. Gate Threshold Variation vs. Junction Temperature

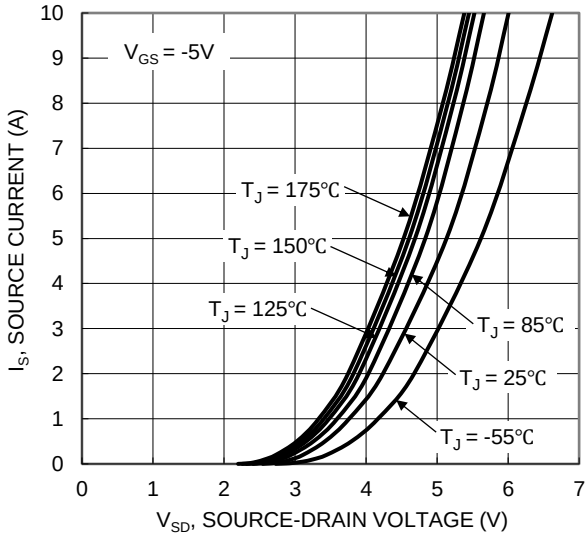


Figure 10. Diode Forward Voltage vs. Current

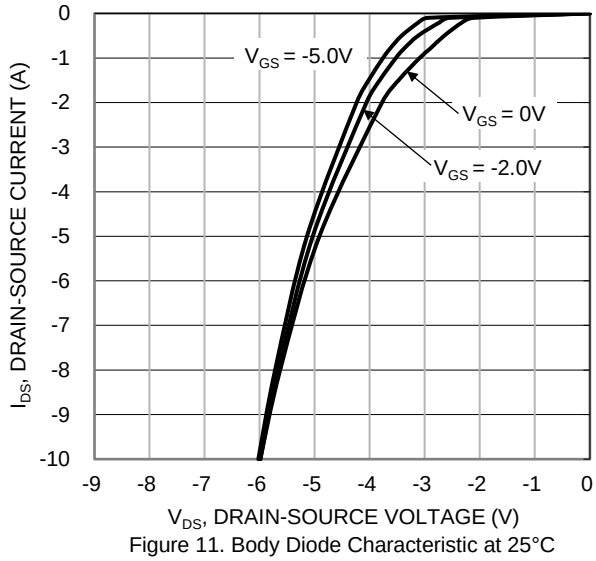


Figure 11. Body Diode Characteristic at 25°C

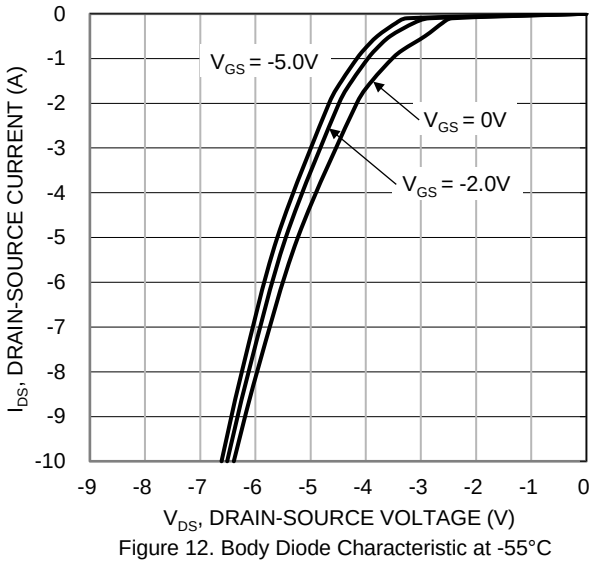


Figure 12. Body Diode Characteristic at -55°C

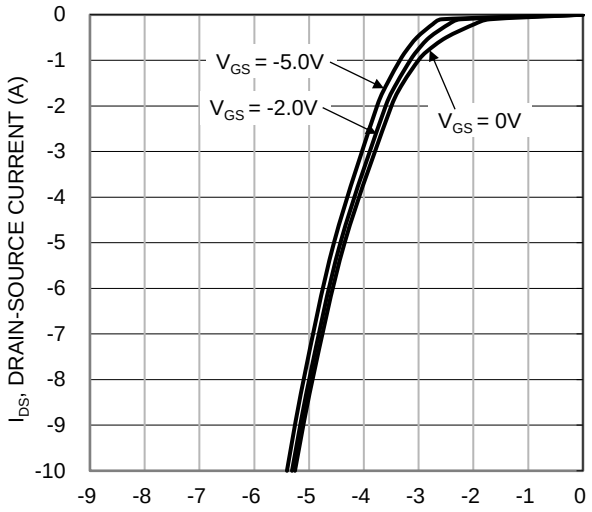


Figure 13. Body Diode Characteristic at 175°C

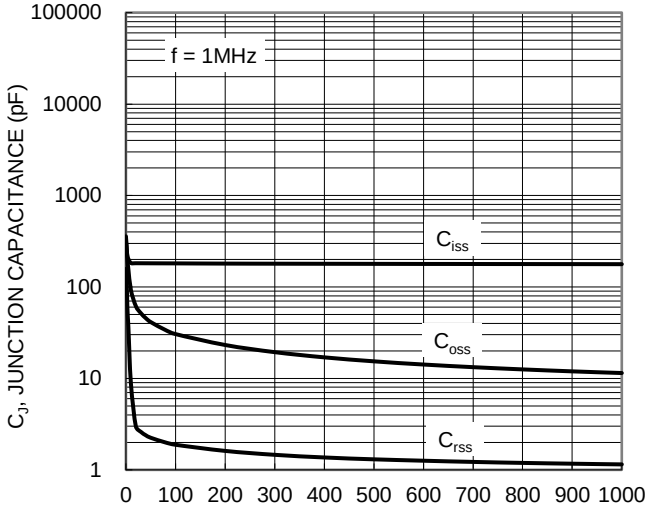


Figure 14. Typical Junction Capacitance

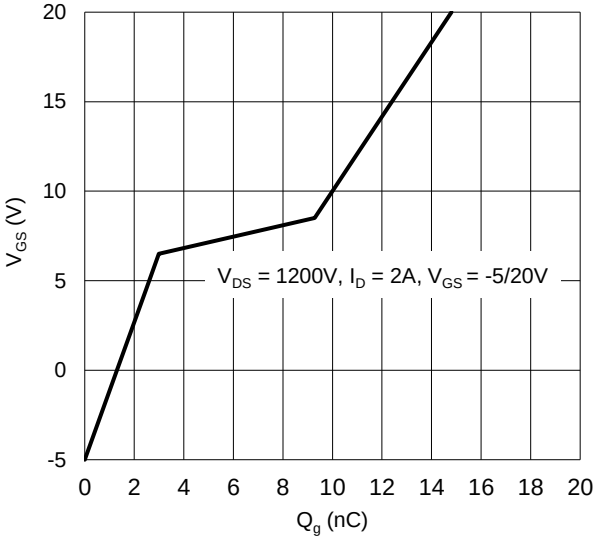


Figure 15. Gate Charge

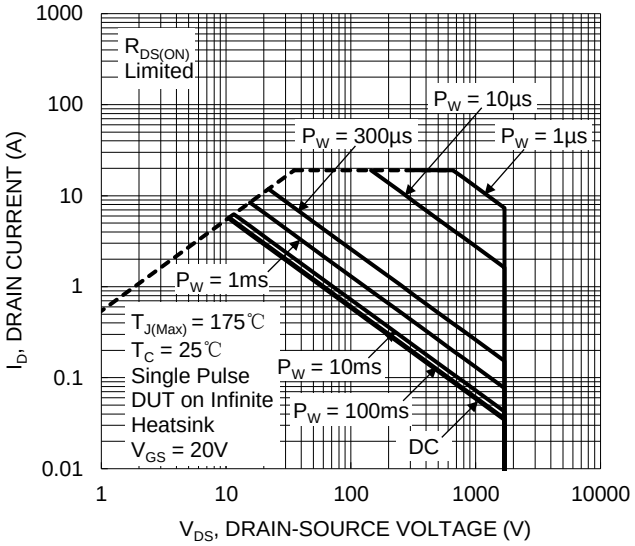


Figure 16. SOA, Safe Operation Area

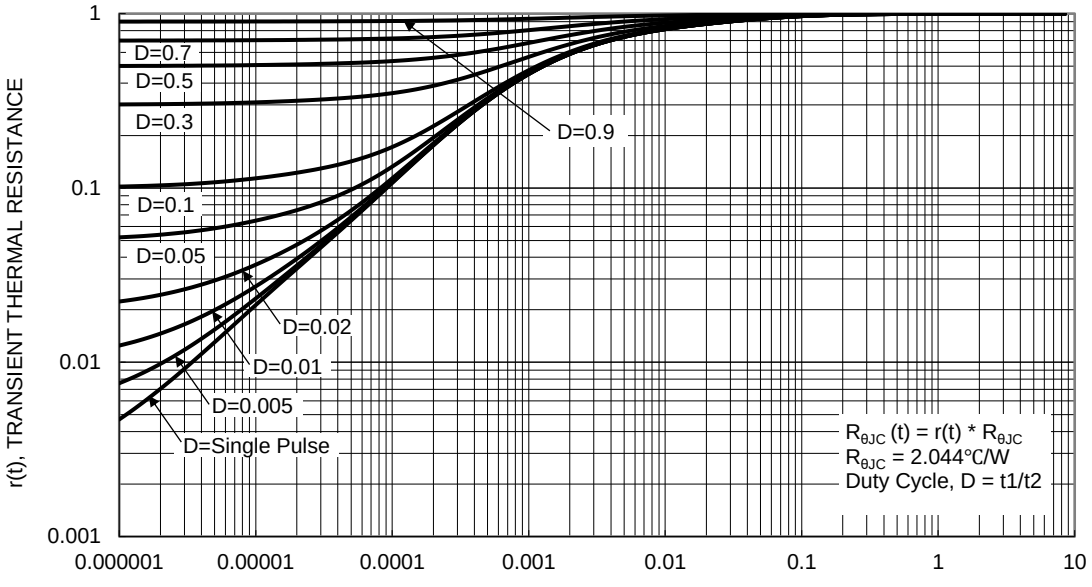
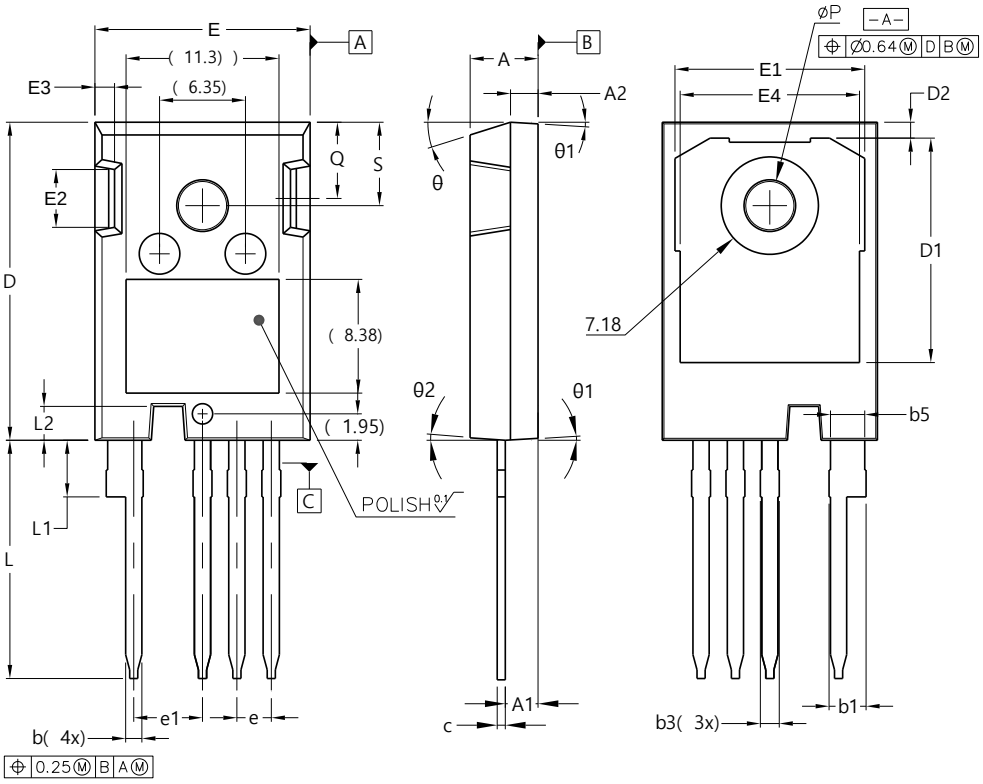


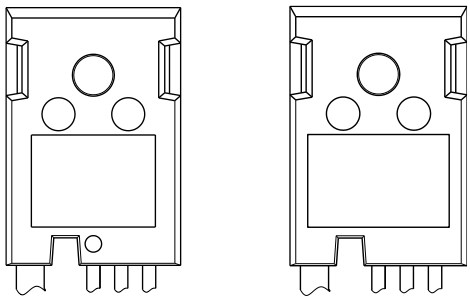
Figure 17. Transient Thermal Resistance

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO247-4 Standard





OPTION A
(TOP VIEW)

OPTION B
(TOP VIEW)

TO247-4 Standard		
Dim	**Min**	**Max**
A	4.83	5.21
A1	2.29	2.54
A2	1.91	2.16
b	1.07	1.33
b1	2.39	2.94
b3	1.07	1.60
b5	2.39	2.69
c	0.55	0.68
D	23.30	23.60
D1	16.25	17.65
D2	0.95	1.25
E	15.75	16.30
E1	13.10	14.15
E2	3.68	5.10
E3	1.00	1.90
E4	12.38	13.43
e	2.54 BSC	
e1	5.08 BSC	
L	17.31	17.82
L1	3.97	4.37
L2	2.35	2.65
ØP	3.51	3.65
Q	5.49	6.00
S	6.04	6.30
θ	17.5°- 20° REF	
θ1	3.5°- 5° REF	
θ2	4°- 5° REF	
All Dimensions in mm		

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