

40V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI8080-5

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _C = +25°C
40V	0.4mΩ @ V _{GS} = 10V	700A
400	0.64mΩ @ V _{GS} = 4.5V	580A

Features and Benefits

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production— Ensures More Reliable and Robust End Application
- High Conversion Efficiency
- Low R_{DS(ON)}—Minimizes Power Losses
- Wettable Flank for Improved Optical Inspection
- Fast Switching Speed
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMTH4M40LPGWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

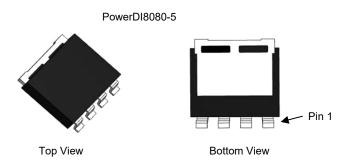
Description and Applications

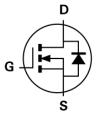
This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

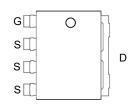
- Engine-management systems
- Body control electronics
- DC-DC converters

Mechanical Data

- Package: PowerDI[®]8080-5
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 ©3
- Weight: 0.36 grams (Approximate)







Internal Schematic

Top View Pin Configuration

Ordering Information (Note 4)

Orderable Part Number	Dookono	Packing		
Orderable Part Number	Package	Qty.	Carrier	
DMTH4M40LPGWQ-13	PowerDI8080-5	2000	Tape & Reel	

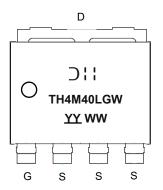
Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

PowerDI is a registered trademark of Diodes Incorporated in the United States and other countries.



Marking Information



] | |= Manufacturer's Marking TH4M40LGW = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 25 = 2025) WW = Week (01 to 53)

Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage		V_{DSS}	40	V	
Gate-Source Voltage		Vgss	±20	V	
Continuous Drain Current (Note 5)	T _C = +25°C	ID	700	_	
Continuous Drain Current (Note 5)	T _C = +100°C		495	A	
Maximum Continuous Body Diode Forward Current (Note 5)	Is	700	Α		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	3200	Α	
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		Ism	3200	Α	
Avalanche Current, L = 1mH		las	66	Α	
Avalanche Energy, L = 1mH		Eas	2178	mJ	

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)	T _A = +25°C	P _D	4.6	W
Thermal Resistance, Junction to Ambient (Note 6)		Reja	32.3	°C/W
Total Power Dissipation (Note 5)	T _C = +25°C	PD	341	W
Thermal Resistance, Junction to Case (Note 5)		R ₀ JC	0.44	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +175	°C

Notes: 5. Thermal resistance from junction to soldering point (on the exposed drain pad).

^{6.} Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.



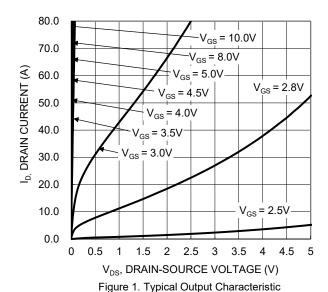
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	40	_	_	V	V _{GS} = 0, I _D = 1mA	
Zero Gate Voltage Drain Current	IDSS		_	1	μA	V _{DS} = 32V, V _{GS} = 0	
Gate-Source Leakage	Igss		_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(th)	1	_	2.5	٧	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	D-s/s/	1	0.28	0.4	mΩ	$V_{GS} = 10V, I_D = 25A$	
Static Drain-Source On-Resistance	R _{DS(ON)}	1	0.44	0.64	mΩ	$V_{GS} = 4.5V, I_D = 25A$	
Diode Forward Voltage	VsD	1	0.7	1.2	V	V _{GS} = 0, I _S = 25A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	_	16698	_		V _{DS} = 20V, V _{GS} = 0 f = 1MHz	
Output Capacitance	Coss	_	10257	_	pF		
Reverse Transfer Capacitance	Crss		452	_			
Gate Resistance	Rg	_	0.7	_	Ω	V _{DS} = 0, V _{GS} = 0, f = 1MHz	
Total Gate Charge	Qg	1	241	_		V 00V 1 05A	
Gate-Source Charge	Qgs		49.9		nC	V _{DD} = 20V, I _D = 25A V _{GS} = 10V	
Gate-Drain Charge	Q_{gd}		28.7				
Turn-On Delay Time	t _{D(ON)}	_	29.3	_		V_{DD} = 20V, V_{GS} = 10V I_{D} = 25A, R_{g} = 6 Ω	
Turn-On Rise Time	t _R	_	45.8	_	20		
Turn-Off Delay Time	t _{D(OFF)}	_	199.6	_	ns		
Turn-Off Fall Time	t _F	_	108.7	_			
Body Diode Reverse-Recovery Time	t _{RR}		118.9	_	ns	I _F = 25A, di/dt = 100A/µs	
Body Diode Reverse-Recovery Charge	Qrr	_	363.4	_	nC	IF - 23A, αΙ/αι - 100A/μS	

Notes:

^{7.} Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.





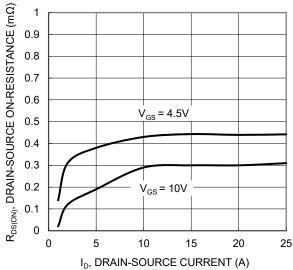


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

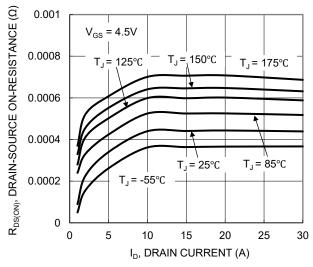


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

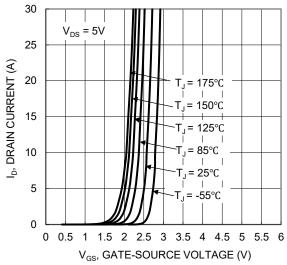
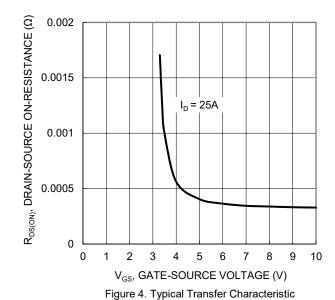


Figure 2. Typical Transfer Characteristic



 $R_{DS(ON)}$, DRAIN-SOURCE ON-RESISTANCE (Ω) 0.001 $V_{GS} = 10V$ 0.0008 $T_J = 150^{\circ}C$ 0.0006 $T_1 = 175^{\circ}C$ 0.0004 0.0002 T_J = 85°C T. = 25°C $T_{.1} = -55^{\circ}C$

10

ID, DRAIN CURRENT (A) Figure 6. Typical On-Resistance vs. Drain Current and Junction Temperature

15

0

0

5

25

30

20



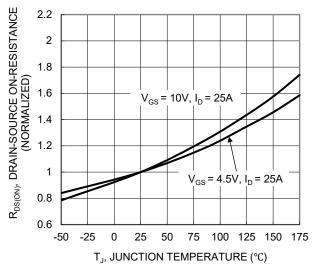


Figure 7. On-Resistance Variation with Junction Temperature

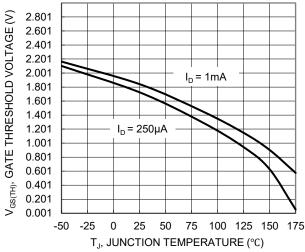


Figure 9. Gate Threshold Variation vs. Junction Temperature

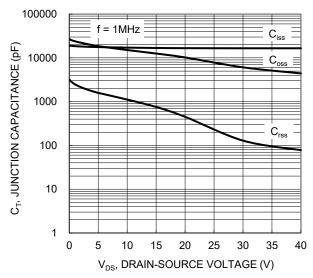


Figure 11. Typical Junction Capacitance

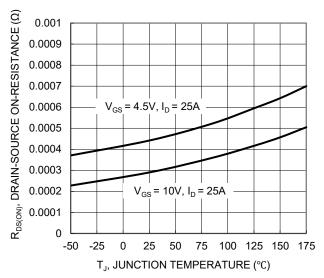


Figure 8. On-Resistance Variation with Junction Temperature

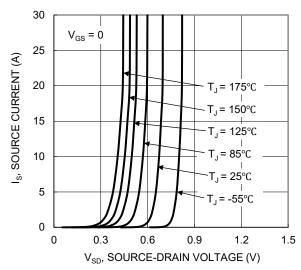


Figure 10. Diode Forward Voltage vs. Current

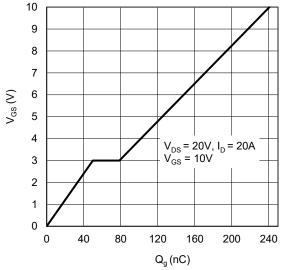


Figure 12. Gate Charge



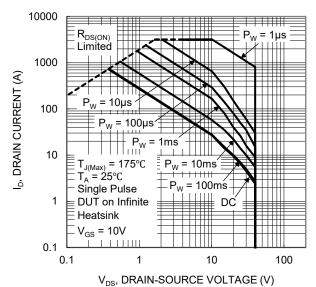


Figure 13. SOA, Safe Operation Area

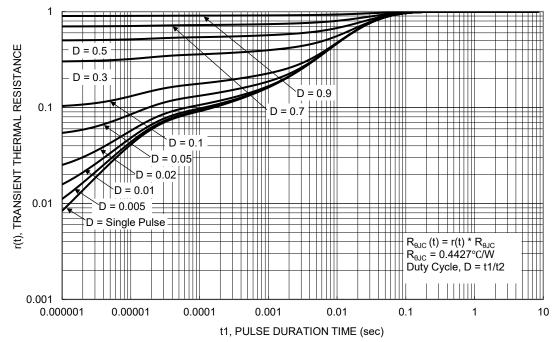


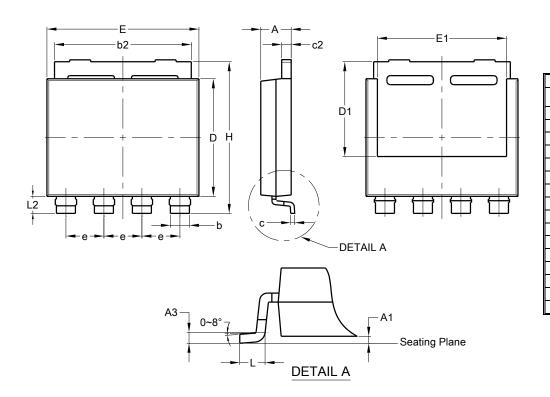
Figure 14. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI8080-5

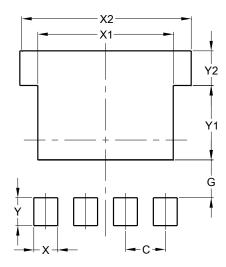


	PowerDI8080-5					
Dim	Min	Max	Тур			
Α	1.50	1.70				
A1	0.00	0.15				
А3			0.25			
b	0.90	1.10				
b2	7.10	7.30				
С	0.18	0.24				
c2	0.47	0.57				
D	6.10	6.30				
D1	4.90	5.10				
Е	7.90	8.10				
E1	6.70	6.90				
е			2.00			
Н	7.80	8.10				
L	0.60	0.80				
L2	0.90	1.30				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI8080-5



Dimensions	Value (in mm)		
С	2.00		
G	1.90		
X	1.20		
X1	6.80		
X2	8.60		
Υ	1.40		
Y1	3.74		
Y2	1.76		



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