

60V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	Rds(ON) Max	I _D Max T _C = +25°C
60V	3.1mΩ @ V _{GS} = 10V	100A

Features

- 100% Unclamped Inductive Switching (UIS) Test in Production –
 Ensures More Reliable and Robust End Application
- Low RDS(ON) Minimizes Power Losses
- Low Qg Minimizes Switching Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Description and Applications

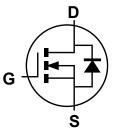
This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- Switching
- Synchronous rectification
- DC-DC converters

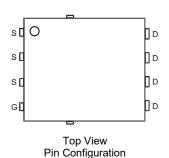
Mechanical Data

- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, "Green" Molding Compound;
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe;
 Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.097 grams (Approximate)





Internal Schematic



Ordering Information (Note 4)

Orderable Part Number	Paskage	Packing		
Orderable Part Number	Package	Qty.	Carrier	
DMT6004SPSW-13	PowerDI5060-8/SWP (Type UX)	2,500	Tape & Reel	

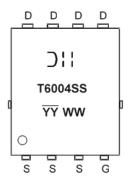
Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

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Marking Information



);; = Manufacturer's Marking
T6004SS = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 25 = 2025)
WW = Week (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			Vgss	±20	V
Continuous Drain Current (Note 5)		T _A = +25°C T _A = +70°C	lo	23 18	А
Continuous Drain Current (Note 6) $ T_C = +25^{\circ}C $ $ T_C = +70^{\circ}C $			l _D	100 100	А
Maximum Continuous Body Diode Forward Current (Note 6)			Is	100	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	400	А
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)			I _{SM}	400	А
Avalanche Current, L = 0.2mH			las	45	А
Avalanche Energy, L = 0.2mH			Eas	200	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T _A = +25°C	P _D	2.6	W
Thermal Resistance, Junction to Ambient	Steady State	Reja	47	°C/W
Total Power Dissipation (Note 6)	T _C = +25°C	PD	139	W
Thermal Resistance, Junction to Case		Rejc	0.9	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

6. Thermal resistance from junction to soldering point (on the exposed drain pad).



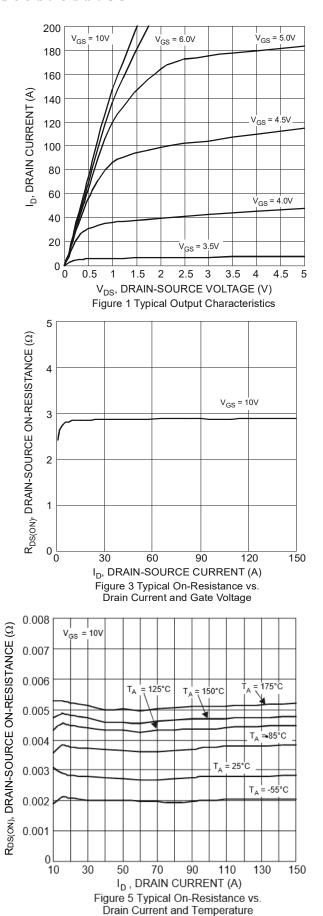
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

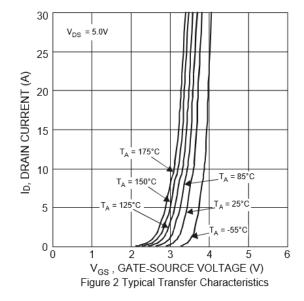
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	V _{GS} = 0, I _D = 1mA	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μA	V _{DS} = 48V, V _{GS} = 0	
Gate-Source Leakage	Igss	_	_	±100	nA	V _{GS} = ±20V, V _{DS} = 0	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _G S(TH)	2	2.5	4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}		_	3.1	mΩ	V _{GS} = 10V, I _D = 50A	
Diode Forward Voltage	V _{SD}	_	0.9	1.2	V	V _{GS} = 0, I _S = 20A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	1	4,556	_	pF	V _{DS} = 30V, V _{GS} = 0 f = 1MHz	
Output Capacitance	Coss		1,383	_			
Reverse Transfer Capacitance	Crss	_	105.2	_			
Gate Resistance	Rg		0.7	_	Ω	V _{DS} = 0, V _{GS} = 0, f = 1MHz	
Total Gate Charge	Qg	_	95.4	_		V _{DD} = 30V, I _D = 90A V _{GS} = 10V	
Gate-Source Charge	Q _{gs}	_	21.6	_	nC		
Gate-Drain Charge	Q _{gd}	-	20.4	_			
Turn-On Delay Time	t _{D(ON)}	_	13.2	_		$V_{DD} = 30V, V_{GS} = 10V$ $I_{D} = 90A, R_{g} = 3.5\Omega$	
Turn-On Rise Time	t _R		11.7	_	20		
Turn-Off Delay Time	t _{D(OFF)}		31	_	ns		
Turn-Off Fall Time	tF		12	_			
Body Diode Reverse-Recovery Time	t _{RR}	_	50.5		ns	L = 504 di/dt = 1004/up	
Body Diode Reverse-Recovery Charge	Q _{RR}		80.8	_	nC	I _F = 50A, di/dt = 100A/μs	

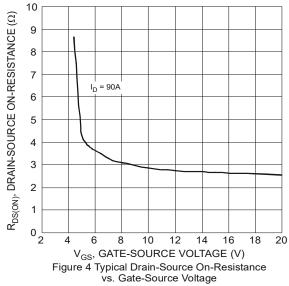
Notes:

^{7.} Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to production testing.









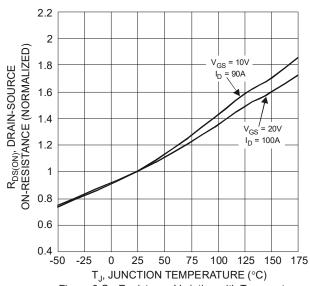
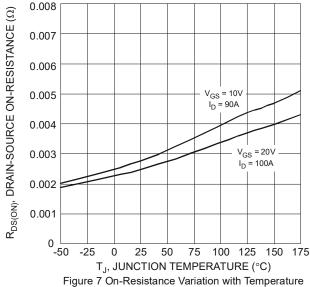
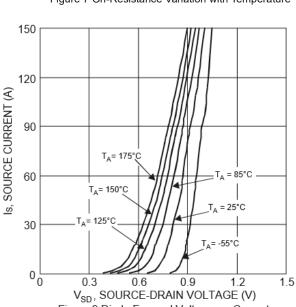


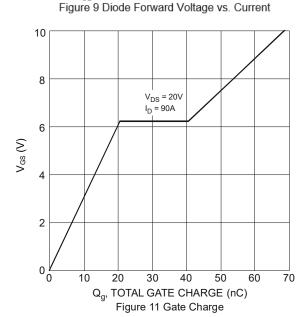
Figure 6 On-Resistance Variation with Temperature











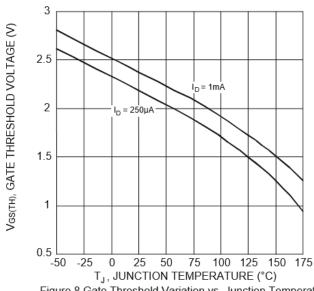
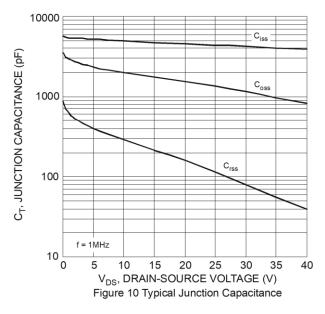
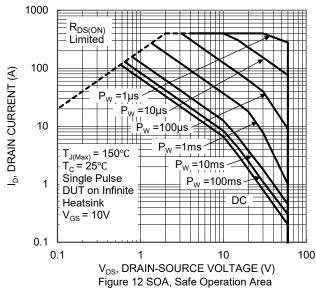


Figure 8 Gate Threshold Variation vs. Junction Temperature







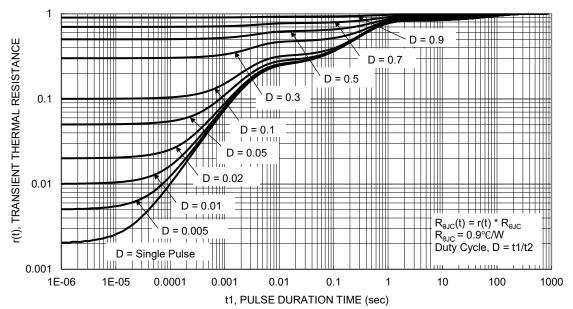


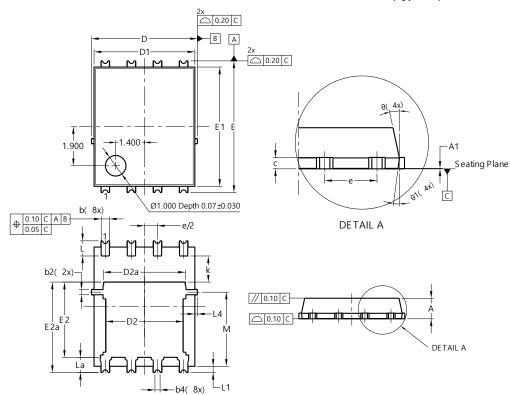
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8/SWP (Type UX)

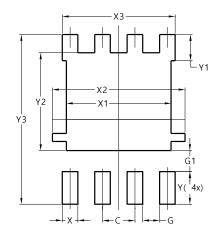


PowerDI5060-8/SWP				
(Type UX)				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0	0.05	-	
b	0.30	0.50	0.41	
b2	0.20	0.35	0.25	
b4	C).25REF		
С	0.230	0.330	0.277	
D	5.15 BSC			
D1	4.70	5.10	4.90	
D2	3.56	3.96	3.76	
D2a	3.78	4.18	3.98	
Е	6	.40 BS0		
E1	5.60	6.00	5.80	
E2	3.46	3.86	3.66	
E2a	4.195	4.595	4.395	
е	1	.27BSC		
k	1.05	-	-	
┙	0.635	0.835	0.735	
La	0.635	0.835	0.735	
L1	0.200	0.400	0.300	
L4	0.025	0.225	0.125	
M	3.205	4.005	3.605	
θ	10°	12°	11°	
θ1	6°	8°	7°	
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8/SWP (Type UX)



Dimensions	value (in mm)		
С	1.270		
G	0.660		
G1	0.820		
X	0.610		
X1	4.100		
X2	5.190		
Х3	4.420		
Y	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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