

#### 60V N-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> Max T <sub>A</sub> = +25°C
60V	2Ω @ V <sub>GS</sub> = 10V	384mA
007	3Ω @ V <sub>G</sub> S = 5V	319mA

### **Description and Applications**

This MOSFET has been designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

- Motor controls
- Power-management functions
- Backlighting

### **Features and Benefits**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **ESD Protected**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotiveproducts/.

This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. https://www.diodes.com/quality/product-definitions/

#### **Mechanical Data**

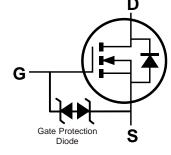
- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.008 grams (Approximate)

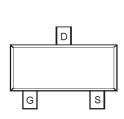




SOT23







Top View

**Equivalent Circuit** 

Top View

### **Ordering Information** (Note 4)

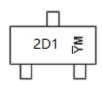
Orderable Part Number	Paakaga	Packing			
Orderable Part Number	Package	Qty.	Carrier		
DMN62D1L-7	SOT23	3,000	Tape & Reel		
DMN62D1L-13	SOT23	10,000	Tape & Reel		

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



# **Marking Information**



M = Month (ex: 9 = September)

### Date Code Key

Year	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034	2035	2036
Code	М	N	Р	R	S	Т	U	V	W	Х	Υ	Z
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code		_	_		-	•	-	_	_		N	,

## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage	VDSS	60	V		
Gate-Source Voltage	Vgss	±20	V		
Continuous Drain Current (Note 5) $V_{GS} = 10V$ Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$			lo	384 307	mA
Maximum Continuous Body Diode Forward Curren	ls	384	Α		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 19	%) (Note 5)	I <sub>DM</sub>	1.0	Α	

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.59	mW
Thermal Resistance, Junction to Ambient (Note 5)  Steady	y State	Reja	212	°C/W
Total Power Dissipation (Note 6)		PD	0.46	mW
Thermal Resistance, Junction to Ambient (Note 6)  Steady	y State	Reja	272	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

<sup>5.</sup> Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

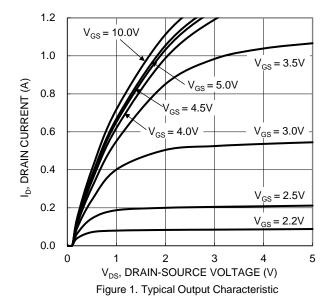


# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	60	_	_	V	$V_{GS} = 0$ , $I_{D} = 10\mu A$	
Zero Gate Voltage Drain Current	IDSS		1	1.0	μΑ	$V_{DS} = 60V, V_{GS} = 0$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	Vgs(th)	1.0		2.5	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	Dag(a)		1.3	2.0	Ω	$V_{GS} = 10V, I_{D} = 0.5A$	
Static Drain-Source Ori-Resistance	RDS(ON)		1.3	3.0	12	$V_{GS} = 5V, I_{D} = 0.05A$	
Diode Forward Voltage	$V_{SD}$	_	8.0	1.1	V	$V_{GS} = 0$ , $I_{S} = 115mA$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss		52	_	pF	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Output Capacitance	Coss		5.3	_	рF	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0 f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	3.9	_	pF	1 = 1.0WH IZ	
Gate Resistance	Rg		135	_	Ω	$f = 1MHz$ , $V_{GS} = 0$ , $V_{DS} = 0$	
Total Gate Charge	Qg		0.5	_	nC	V 45V V 40V	
Gate-Source Charge	Qgs	_	0.3	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$ $I_{D} = 250mA$	
Gate-Drain Charge	$Q_{gd}$	_	0.1	_	nC	ID = 250IIIA	
Turn-On Delay Time	td(on)	_	4.2	_	ns		
Turn-On Rise Time	t <sub>R</sub>	_	10	_	ns	$V_{DD} = 30V, V_{GS} = 10V,$	
Turn-Off Delay Time	tD(OFF)	_	26	_	ns	$R_G = 25\Omega$ , $I_D = 200mA$	
Turn-Off Fall Time	tF	_	19	_	ns		

7. Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing. Notes:





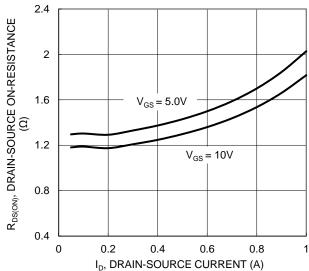


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

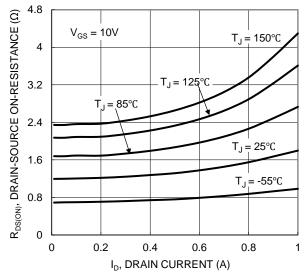


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

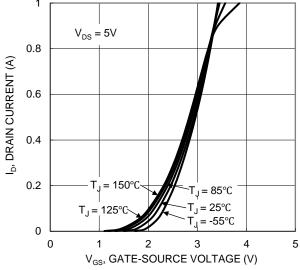
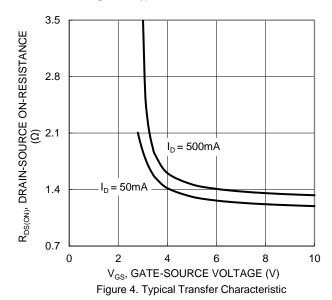


Figure 2. Typical Transfer Characteristic



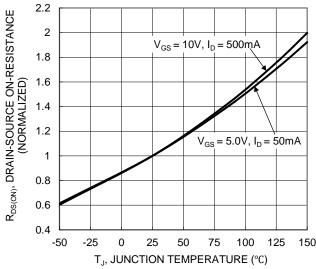


Figure 6. On-Resistance Variation with Temperature



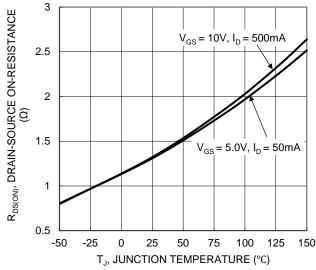


Figure 7. On-Resistance Variation with Temperature

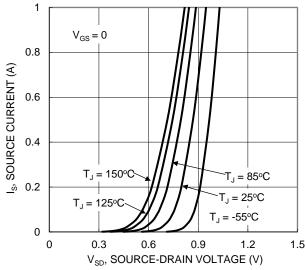


Figure 9. Diode Forward Voltage vs. Current

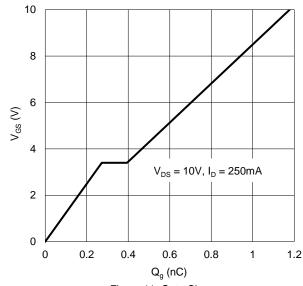


Figure 11. Gate Charge

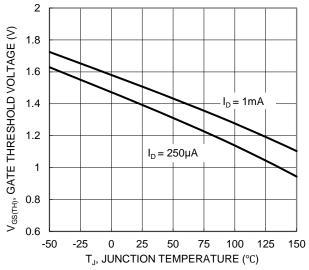
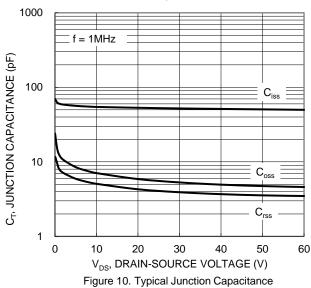


Figure 8. Gate Threshold Variation vs. Junction Temperature



10  $P_{W} = 100 \mu s$ ID, DRAIN CURRENT (A) 0.1  $T_{J(Max)} = 150^{\circ}C$ 0.01 T<sub>A</sub> = 25°C  $P_W = 10s$ Single Pulse DUT on 1\*MRP board  $V_{GS} = 10V$ 0.001 0.1 10 100 V<sub>DS</sub>, DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



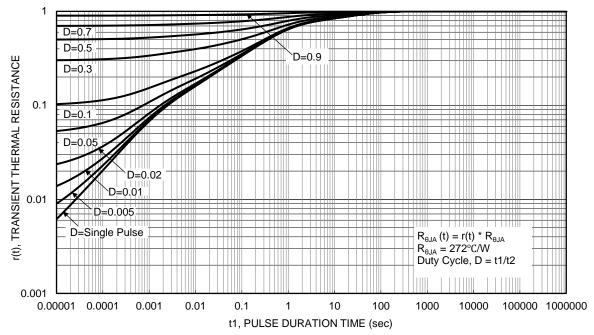


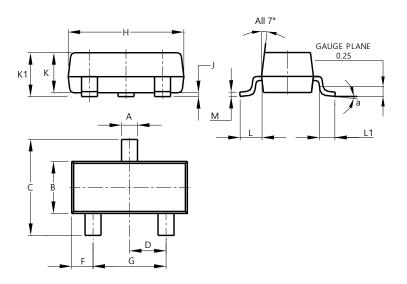
Figure 13. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### SOT23

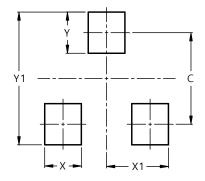


SOT23								
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
C	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78	2.05	1.83					
Н	2.80	3.00	2.90					
J	0.013	0.10	0.05					
K	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
L	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
М	0.085	0.150	0.110					
а	0°	8°						
All	All Dimensions in mm							

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23



Dimensions	Value (in mm)
С	2.0
X	0.8
X1	1.35
Y	0.9
V1	2.0



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