



New Product Announcement

PI6LC58S1101

Networking Clock Generator with Ultra-Low 80fs Phase Jitter Provides Flexible Design Solutions

The PI6LC58S1101 is a very low jitter clock generator for applications that demand extremely low phase noise.

It has four output banks that can be configured independently for different frequencies and different output signaling types based on control pins, which provide a simple way to configure the device at the hardware level.

With ultra-low jitter at around 80fs, the device meets tight jitter requirements and provides ample margin for the latest high-speed designs, such as 100G/200G/400G and PAM4 serial interfaces.

The PI6LC58S1101 is available in the 64-TQFN (9mm x 9mm) package.



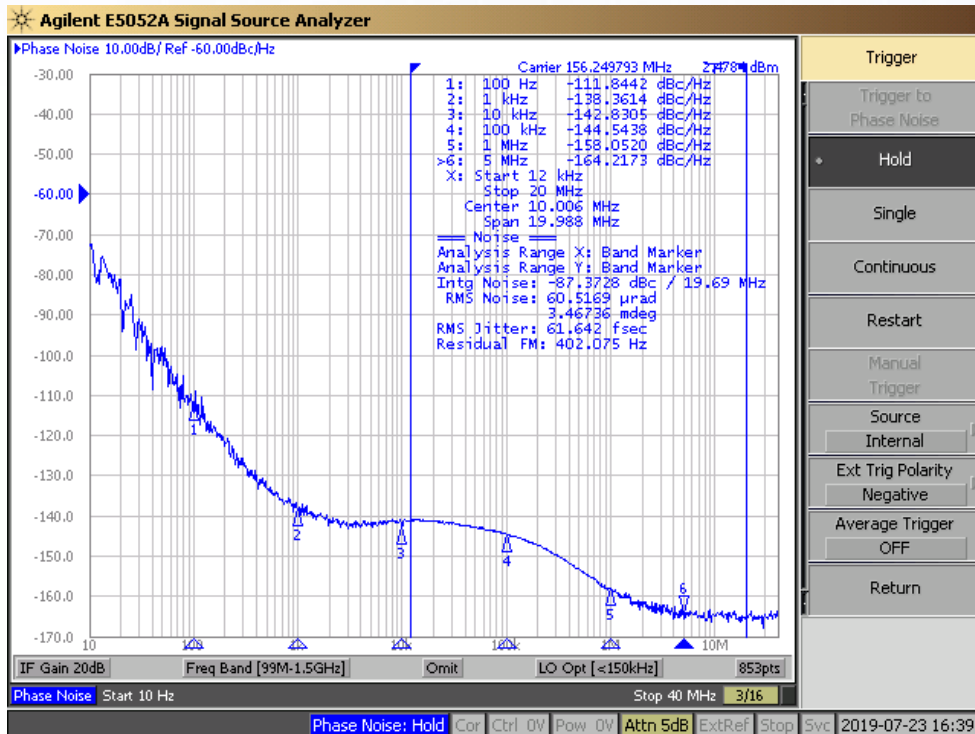
The Diodes Advantage

- **Four output banks with selectable output signaling: LVPECL and LVDS**
Provide flexibility and ease of design
- **Eleven differential outputs with selectable frequencies by banks**
Supports different frequencies for overall timing needs as well as simplifying the BOM
- **Ultra-low jitter at 80fs RMS (typ)**
Provides ample jitter margin for system design

Applications

- Switches & Routers
- Data Centers
- Embedded Systems

Phase Noise Plots (156.25MHz LVPECL)



Product Information

Description	Part Number	# of Outputs	Logical Type	RMS Jitter	Voltage Support	Package
11 Outputs HiFlex™ Ethernet Network Clock Generator	PI6LC58S1101	11	LVPECL/LVDS	80fs	3.3V	64-TQFN (9mm x 9mm)

Ordering Information

Description	Orderable Part Number	Package Code	MOQ	Operating Temp.
11 Outputs HiFlex™ Ethernet Network Clock Generator	PI6LC58S1101ZDIEX	ZD	3,000	-40°C to 85°C