



New Product Announcement

PI6C39X020401

PI6C39X0202

Low Jitter Low Skew Small Package CMOS Clock Buffers for High Density Networking Applications

The PI6C39X020401/0202 are Single-Ended four/two output Clock Buffers with extended -40°C to 105°C temperature operation.

As networking system clock trees get more complex with increasing density, multiple output clock buffers with low jitter, low skew and small package sizes are required.

The PI6C39X020401/0202 meet this need with their four/two CMOS outputs offering low 50ps skew, low 50fs additive jitter and up to 250MHz output frequency.

The PI6C39X020401/0202 are available in the 2mmx2mm DFN package.



The Diodes Advantage

- **Extended temperature range -40°C to 105°C**
Meets applications' ambient temperature requirements

- **Low additive Jitter (50fs_{RMS}) clocks**
Minimal phase noise increasing system timing margin

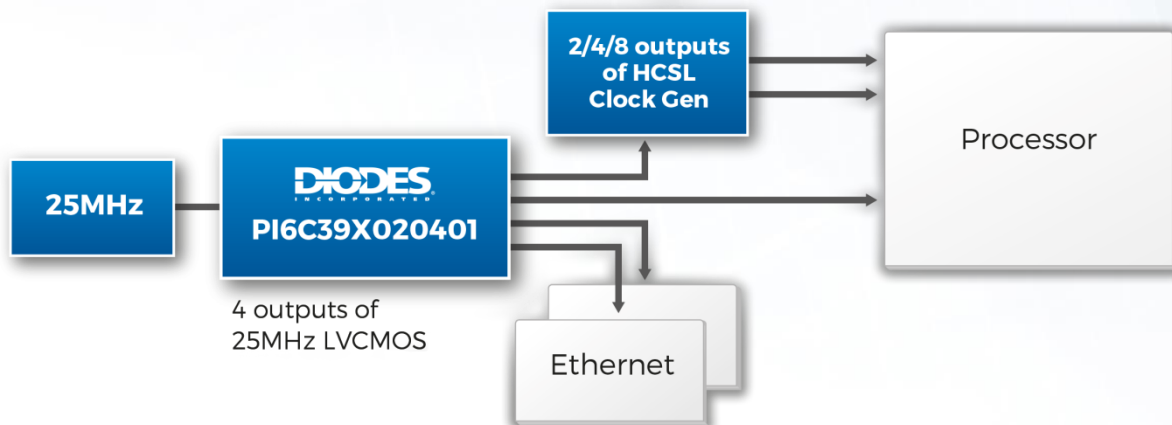
- **Low Skew (50ps) clocks**
All output clocks are aligned and synced

- **Small 8-pin 2mmx2mm DFN package**
Ideal for high density designs with small board space

Applications

- High Density Switches
- Gateways
- Server and Datacenters

Typical Application



CMOS Clock Buffers Portfolio

MPN	Outputs	Output Types	Input Types	Additive Jitter	Output Skew	Max Output Freq.	Vdd	Package
PI6C39X0202	2	LVCMOS LVTTTL	LVCMOS LVTTTL	0.05ps	50ps	250MHz	1.5V to 3.3V	DFN2020-8
PI6C39X020401	4			0.05ps	50ps	250MHz	1.5V to 3.3V	DFN2020-8

To find out more information:

<https://www.diodes.com/part/PI6C39X020401>

<https://www.diodes.com/part/PI6C39X0202>

Ordering Information

Part Number	Temp Range	Package	Tape Width	Quantity per reel
PI6C39X020401XECIEX	-40 to 105°C	DFN2020-8	12mm	2500
PI6C39X0202XECIEX	-40 to 105°C	DFN2020-8	12mm	2500