



## New Product Announcement

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### LV LOGIC

# Low Voltage CMOS (LV) Logic Family Available in TSSOP-14 and SO-14

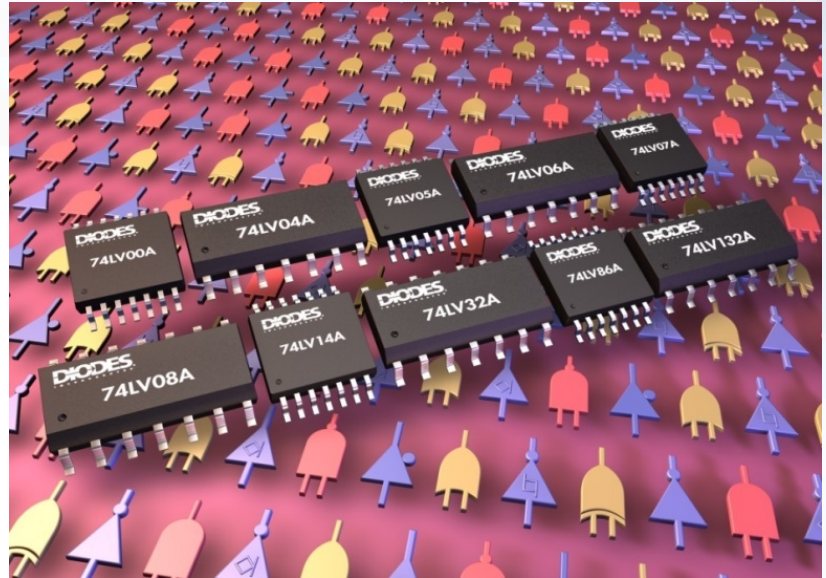
The 74LVxx is a popular “Low Voltage CMOS” logic family that is widely used in computer and consumer electronics applications.

LV is optimized for operation at 3.3V but operates over a supply voltage range of 2.0 to 5.5V

The inputs are tolerant to 5.5V even when  $V_{cc}$  is 3V allowing the parts to be used in mixed voltage applications.

Output pins feature Ioff circuitry which completely eliminate current paths when power is removed. This is useful for power down isolation.

The TSSOP-14 and SO-14 packages available.



### The Diodes Advantage

#### Wide Supply Voltage Range

The recommended operating voltage range is  $V_{cc}$  from 2.0 to 5.5V. This allows a wide range of applications including 5V legacy systems.

#### Upgrade Path

Diodes offers the designer the flexibility of a system upgrade from HC or AHC technologies.

The compatible voltage levels and faster switching speeds allow for faster clock rates in legacy systems.

#### Direct Replacement

The LVxx series offers a direct replacement for the industry standard LV products.



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## 74LVxx Logic Products / Cross Reference

Diodes Device	Package	Description	TI	NXP
74LV00AS14-13	SO-14	Quadruple 2-input NAND Gate	SN74LV00ADR	74LV00D,118
74LV00AT14-13	TSSOP-14	Quadruple 2-input NAND Gate	SN74LV00APWR	74LV00PW,118
74LV04AS14-13	SO-14	Hex Inverters	SN74LV04ADR	74LV04D,118
74LV04AT14-13	TSSOP-14	Hex Inverters	SN74LV04APWR	74LV04PW,118
74LV05AS14-13	SO-14	Hex Inverters with open drains	SN74LV05ADR	
74LV05AT14-13	TSSOP-14	Hex Inverters with open drains	SN74LV05APWR	
74LV06AS14-13	SO-14	Hex Inverters with open drains	SN74LV06ADR	
74LV06AT14-13	TSSOP-14	Hex Inverters with open drains	SN74LV06APWR	
74LV07AS14-13	SO-14	Hex Buffers with open drains	SN74LV07ADR	
74LV07AT14-13	TSSOP-14	Hex Buffers with open drains	SN74LV07APWR	
74LV08AS14-13	SO-14	Quadruple 2-input AND Gate	SN74LV08ADR	74LV08D,118
74LV08AT14-13	TSSOP-14	Quadruple 2-input AND Gate	SN74LV08APWR	74LV08PW,118
74LV14AS14-13	SO-14	Hex Inverters with Schmidt Trigger Inputs	SN74LV14ADR	74LV14D,118
74LV14AT14-13	TSSOP-14	Hex Inverters with Schmidt Trigger Inputs	SN74LV14APWR	74LV14PW,118
74LV32AS14-13	SO-14	Quadruple 2-input OR Gate	SN74LV32ADR	74LV32D,118
74LV32AT14-13	TSSOP-14	Quadruple 2-input OR Gate	SN74LV32APWR	74LV32PW,118
74LV86AS14-13	SO-14	Quadruple 2-input XOR Gate	SN74LV86ADR	74LV86D,118
74LV86AT14-13	TSSOP-14	Quadruple 2-input XOR Gate	SN74LV86APWR	74LV86PW,118
74LV132AS14-13	SO-14	Quadruple 2-input NAND Gate with Schmidt Trigger Inputs	SN74LV132ADR	74LV132D,118
74LV132AT14-13	TSSOP-14	Quadruple 2-input NAND Gate with Schmidt Trigger Inputs	SN74LV132APWR	74LV132PW,118

## Applications

74LVxx products are a suitable replacement for other logic families such as HC or AHC. The 74LV product offers more DC drive capability, favorable switching speed, and superior power dissipation at higher frequencies.

Family	Operating Voltage	Drive Current at 4.5 V	Maximum Switching Speed at 4.5 V and 50 pF Load	Power Dissipation Capacitance	Ioff Circuitry
LV	2.0 to 5.5 V	12 mA	7.5 ns	11 pF	yes
AHC	2.0 to 5.5 V	8 mA	7.5 ns	15pF	no
HC	2.0 to 6.0 V	4 mA	18 ns	20pF	no

## Ordering Information

Device	Packaging	Package Marking Info	Reel Size	Tape Width	Quantity
74LVxxxS14-13	SO -14	74LVxxx YY WW X	13"	12mm	2500
74LVxxxT14-13	TSSOP -14	74LVxxx YY WW X	13"	12mm	2500

All variants are in packages that are "Green" Molding Compound (No Br, Sb) with Lead Free Finish/RoHS Compliant (Note 1)

Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. No purposely added lead. Halogen and Antimony free.

2. Codes for date coding on part marks

xx x function number YY Year 0-9 WW week 1-52 52 represents 52 and 53 week X Internal Code