Introduction

Lighting class LEDs are now available that deliver the brightness, efficacy, lifetime, color temperature, and white point stability required for general illumination. As a result, these LEDs are being adopted into most general lighting applications including roadway, parking area and indoor directional lighting. LED-based luminaires reduce Total-Cost-of-Ownership (TCO) in these applications through maintenance avoidance and reduced energy costs.

MR16 lamps are one variety of Multifaceted Reflector (MR) lamps that have traditionally employed a halogen filament capsule as the light source. They are used in many retail and consumer lighting applications where their size, configurability, spot-lighting capability and aesthetics provide utility and creativity. Their low efficiency, heat generation (an issue for illuminating heat sensitive subjects and materials) and halogen capsule handling issues are typically cited among the disadvantages of the technology. They typically operate from 12V AC or 12V DC, though designs for 6V to 24V are also popular and as such require a step-down transformer to allow use from offline supplies. This is usually effected with conventional electromagnetic or electronic transformers.

With the advancement of HB (High Brightness) LED technologies, MR16 lamps can now be realized with an alternate light source. This hybrid solution can yield a cost effective, long-life, maintenance free, cooler operating unit which has not been previously possible.

Figure 1  MR16 Lamps (Incandescent A-lamp on far right shown for size comparison)
Description

This design note describes a driver solution developed using the Zetex ZXLD1350 LED driver IC to drive three CREE® XLamp XR-E High Brightness (HB) LEDs.

The **ZXLD1350** features can be summarized as:

- **Wide input voltage range**
- **7V to 30V; internal 30V NDMOS switch**
- **Up to 350mA output current (the ZXLD1360 can provide up to 1A output current)**
- **Capable of driving up to 8 series connected 1 Watt LEDs**
- **High efficiency (see datasheet - but >90% with 8 LEDs)**
- **Low quiescent current: (100\(\mu\)A typical)**
- **1A max shutdown current**
- **Brightness control using DC voltage or PWM (low or high frequency)**
- **Internal PWM filter for high frequency PWM signals**
- **Optional soft-start; up to 1MHz switching frequency**

The Cree XR-E LED is a lighting class device that provides energy savings for many traditional technologies such as the MR16 halogen lamp. The XR-E LED is capable of operating at forward currents of up to 1A without any noticeable shifts in chromaticity. The XR-E is ideally suited for direct replacement of MR16 when used in clusters of three at a forward current of 300mA—1000mA. They are specified at 80 lumens and 70 lumens per watt at 350mA (136 lumens at 700mA). These lighting class LEDs offer efficient, directional light that offers a lumen maintenance of 70% at 50,000 hours, in addition to significantly reducing power consumption.

The circuit diagram of the ZXLD1350 effected MR16 lamp solution is shown in **Figure 2**. **Table 1** provides the bill of materials. A full bridge (D1—D4) is employed using 1A DC rated, low leakage Schottky diodes to allow AC or DC input supplies. A thermistor circuit is incorporated to reduce the output current of the circuit to provide thermal feedback control, which allows the circuit to a) match the thermal de-rating requirements of the LEDs to ensure lumen maintenance expectations are achieved and b) prevent overheating. The thermistor must be thermally coupled to the LEDs to ensure accurate and responsive tracking. Adjustment of the thermal feedback circuit can be accomplished by the choice of the thermistor R3 - which sets the slope of the current vs. temperature response, and resistor R2 - which determines the temperature threshold point for the control circuit. R1 and D5 provide a reference voltage for the thermal control circuit. Q1 is a low \(V_{CE(sat)}\) PNP transistor. Schottky diode D9 is again a low leakage 1A rated device in a SOT23 package - its low forward voltage and low reverse current ensure high efficiency and thermal stability in the main switching circuit. C3 may be added to reduce the amplitude of the current ramp waveform experienced by the LED string but in many applications this isn't required as the integrating nature of human sight cannot perceive quickly changing light levels. Depending on layout intricacies and EMC dictates, it may be necessary to exchange the positions of the inductor and LED string - this isn't always possible mechanically but does give a lower EMI signature.
Figure 3 shows the measured response of the LED current drive with respect to temperature, with the values given in Table 2. The selection of components for the thermal feedback circuit is not only dependent on the choice of R2 and R3, but also on the amount of heat sink area required to extract heat from the LEDs. To maximize the light output at high ambient or operating temperature conditions, the LEDs must have a sufficient thermal extraction path, otherwise the thermal control circuit will effect current drive reduction in non-optimal conditions. The thermal control threshold point is set by adjusting R2. For this design, three values (33k, 22k and 10k) were evaluated. These values were chosen to give break points at approximately 25°C, 40°C and 60°C. Note that the light output will not continually dim to zero - the thermal control is applying DC control to the ADJ pin and therefore has a dimming ratio from maximum current of approximately 5:1. Once the reduced DC level goes below the shutdown threshold of around 200mV, the LED drive current will fall to zero and the LEDs will be extinguished. The slope of the current reduction is determined by the beta value of the thermistor. The larger the beta value, the sharper will be the resultant current control response. The slope of the current reduction is also affected by Q1’s base emitter voltage (V_{\text{BE}}) variation with temperature. Figure 3 shows the slope starts to level off at higher temperatures due to the increasing influence of the approximately -2.2mV/°C change in the V_{\text{BE}} of the transistor.

![Figure 2 Circuit diagram of ZXLD1350 MR16 lamp solution](image-url)
Figure 3  Measured response of thermal feedback control showing threshold point

Figure 3  Measured results for circuit of Figure 2 using 10k thermistor with beta of 3900

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Table 1  Bill of materials
For three series-connected LEDs, the voltage can be from 12V minimum to 30V DC maximum. For AC supplies, remember to include the 1.414 factor for RMS specified values - so for 20V AC (RMS), this will provide a DC rail after the Schottky bridge of 28.3V. The nominal current is set at 350mA with a 0.283Ω sense resistor. The sense resistor is a combination component using 4 low cost, commonly available values and allows current set point flexibility if the circuit is used as a platform design for a series of products. For three series-connected LEDs, with a nominal supply of 24V and a 100µH inductor, the ZXLD1350 runs in continuous mode at approximately 500kHz.

The ZXLD1350 datasheet displays this information graphically, as shown in Figure 4 (for a sense resistor of 330mΩ in this case), which allows a fast assessment to be made of operating conditions. The switching frequency will increase as the voltage on the ADJ pin decreases. As the ZXLD1350 (and ZXLD1360) series of LED drivers use a hysteretic switching topology, the switching frequency is dependent on several factors - input voltage, target current (including any effect by voltage on the ADJ pin to reduce the current) and number of LEDs. An Excel based calculator is available which allows "what-if" initial evaluation and is a useful tool for assessing component and condition changes. Final designs should, of course, be verified by reality.

### Table 2: Thermal feedback control threshold point (resistor R2)

<table>
<thead>
<tr>
<th>Temp.</th>
<th>R2 = 33k</th>
<th>R2 = 22k</th>
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Note: Please see the ZXLD1350 datasheet for complete characterization including efficiency, duty cycle and current variation charts for various values of inductor.

**Figure 4** Example of operating frequency chart for the ZXLD1350
Higher current designs

The ZXLD1350 is designed for LED current drive applications of up to 350mA. The monolithic N-MOSFET is sized appropriately to provide a cost-effective die size and is rated to 400mA, which with the hysteretic mode of operation (the current waveform will ramp ±15% about the nominal current set point) provides sufficient margin. For higher current operation, the 1A rated ZXLD1360 offers similar design procedures and has the following features:

- Up to 1A output current
- Wide input voltage range: 7V to 30V
- Internal 30V 400mΩ NDMOS switch
- Can drive up to 7 series connected 3W LEDs (with due attention to thermal path design)
- High efficiency (>90% for 7 LEDs)
- Brightness control using DC voltage or PWM
- Internal PWM filter
- Optional soft-start
- Up to 1MHz switching frequency
Board design

The Printed Circuit Board (PCB) design and circuit employed make it particularly suitable for use in MR16 halogen lamp replacement units. The supply voltage range is nominally 12V AC or DC, making it compatible and interchangeable with existing MR16 lamps. The printed circuit tracking has been designed using only one side of the board, to facilitate the use of an aluminum or other heat-conductive substrate where through-hole technology cannot be employed. A central hole is provided to enable connection of the supply leads from the rear and for connection to a dimming circuit, where this is required. Mounting holes are also provided. Gerber-format layout files for this PCB are available from Zetex upon request. Please quote PCB number ZDB335.
Appendix A - ZXLD1350 operation

In normal operation, when voltage is applied at $+V_{CC}$, the ZXLD1350 internal NDMOS switch is turned on. Current starts to flow through the sense resistor, inductor L1, and the LEDs. The current ramps up linearly, and the ramp rate is determined by the input voltage $+V_{CC}$ and the inductor L1. This rising current produces a voltage ramp across the sense resistor. The internal circuit of the ZXLD1350 senses the voltage across the sense resistor, and applies a proportional voltage to the input of the internal comparator. When this voltage reaches an internally set upper threshold, the NDMOS switch is turned off. The inductor current continues to flow through the sense resistor, L1, the LEDs, the Schottky diode SD9, and back to the supply rail, but it decays, with the rate of decay determined by the forward voltage drop of the LEDs and the Schottky diode. This decaying current produces a falling voltage at the sense resistor, which, in turn, is sensed by the ZXLD1350. A voltage proportional to the sense voltage across the sense resistor is applied at the input of the internal comparator. When this voltage falls to the internally set lower threshold, the NDMOS switch is turned on again. This switch-on-and-off cycle continues to provide the average LED current set by the sense resistor.

Both DC and PWM dimming can be achieved by driving the ADJ pin through W3. For DC dimming, the ADJ pin may be driven between 300mV and 1.25V. Driving the ADJ pin below 200mV will shutdown the output current. For PWM dimming, an external open-collector NPN transistor or open-drain N-channel MOSFET can be used to drive the ADJ pin. The PWM frequency can be low, around 100Hz to 300Hz, or high between 10kHz to 50kHz. For the latter case, an on-chip filter derives the DC content and so for high frequency PWM input, the device will operate essentially as for DC control input dimming. Generally, low frequency PWM control is preferred as in this mode, the converter is shut down during PWM low signals and drives the LEDs at the defined nominal current during PWM high signals - this ensures that the LEDs can are always driven at the nominal current and therefore color temperature (CCT) shifts are minimized. The capacitor C2 should be around 10nF to decouple high frequency noise at the ADJ pin for DC dimming. Note - C2 should not be fitted when using the PWM dimming feature. The soft-start time will be nominally 0.5ms without capacitor C2. Adding C2 will increase the soft start time by approximately 0.5ms/nF.

Please refer to the datasheets for the threshold limits, ZXLD1350 internal circuits, electrical characteristics and parameters.
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