**Verification of PT7C4337 IBIS model**

1. **Introduction:**

To verify the correlation between the ibis model and hspice model, we need to do some simulations:

1. IO: Add a **1KΩ** pull-up resistor to the IO pad.

PT7C4337

**VOUT**

**SCL\_C**

**SDA\_C**

**Input Signals**

**SCL\_C**

**SDA\_C**

**VIN**

**SCL\_C**

**SDA\_C**

SDA

input

**…..**

**SCL\_C**

**SDA\_C**

VCC

R=1kOhm

**SCL\_C**

**SDA\_C**

 The frequency of signal is 500KHz:

vin input pulse (0 power 0 1n 1n 1u 2u)

1. without package;
2. with package;
3. OUTPUT: Add a **1KΩ** pull-up resistor to the OUTPUT pad.

PT7C4337

**VOUT**

**SCL\_C**

**SDA\_C**

**Input Signals**

**SCL\_C**

**SDA\_C**

**VIN**

**SCL\_C**

**SDA\_C**

\_INTA

input

**…..**

**SCL\_C**

**SDA\_C**

VCC

R=1kOhm

**SCL\_C**

**SDA\_C**

 The frequency of signal is 500KHz:

vin input pulse (0 power 0 1n 1n 1u 2u)

1. without package;
2. with package;
3. **Conclusion:**

For IO and OUTPUT, the simulation results of IBIS model can match quite well with the HSPICE model at above load conditions.

1. **IO Simulation Result:**
2. without package;



1. with package;



1. **OUTPUT Simulation Result:**
2. without package;



1. with package;

