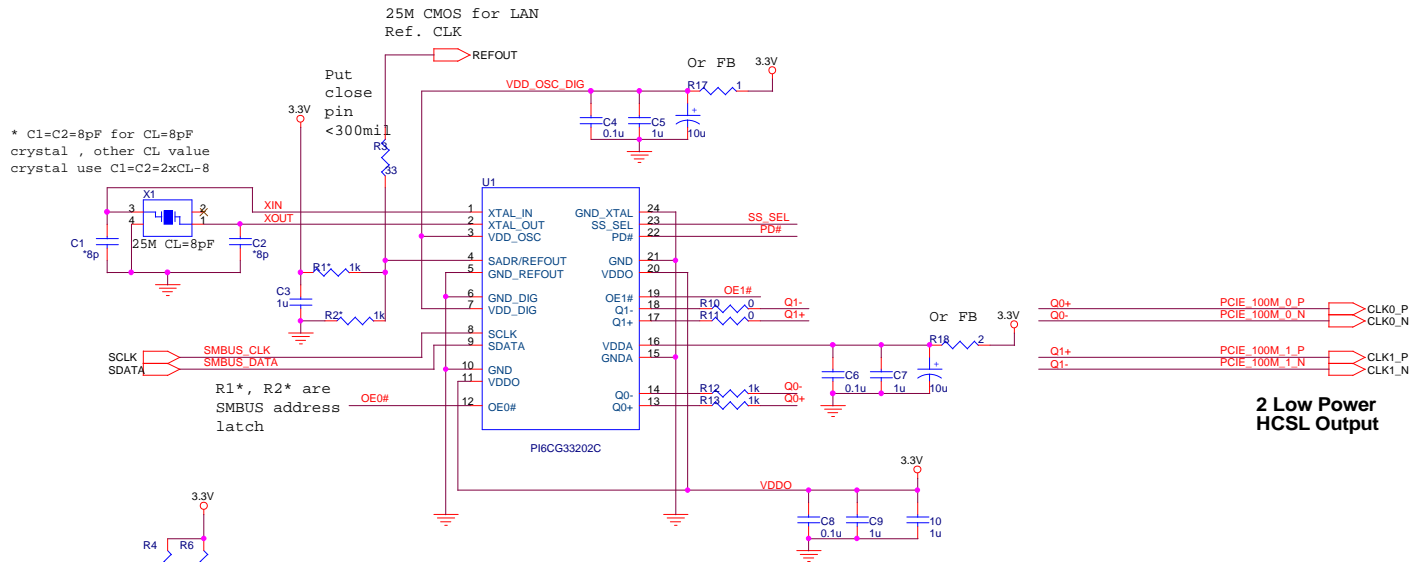


\* C1=C2=8pF for CL=8pF crystal , other CL value crystal use C1=C2=2xCL-8



None for SSC on

SS\_SEL : "H" => -0.5% SSC on  
 SS\_SEL : "M" (default) or "L" => SSC off  
 Use SMBUS to set -0.25% SSC

#### App Note:

1. All VDD pin needs 0.1u +1uF decoupling cloase to pin
2. VDDA, VDDOSC use small R+C filtering for DC/DC ripple noise filtering
3. This is LP\_HCSL type output: serial 0 ohm is optional, but it can be replace in 2 to 5ohm for fine tune board RX skew for different trace length if needed
4. Since OSC pin cap.=5pF so select CL=8pF crystal can C1=C2=8pF, other CL value crystal C1=C2=2xCL-5-3, 3pF is PCB C\_stray
5. Note SSC\_SEL and SMBUS address pins are power on latch select only
6. Make LVDS clock, it needs AC coupling and then RX side use pull-up/down to bias LVDS DC level, refer to datasheet;
7. OE# pins have internal pull-down

Title		
Pi6CG33202C Application schematic		
Size	Document Number	Rev
B	Diodes Inc. Clock IC Application Engineering	1.0
Date:	Monday, May 04, 2020	Sheet 1 of 1