**Verification of PI6C557-05 IBIS model**

1. **Introduction:**

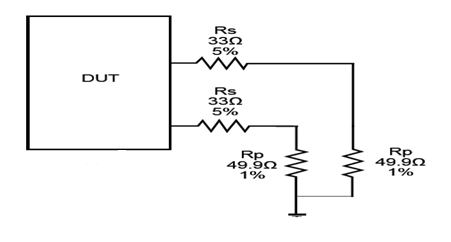
To verify the correlation between the ibis model and hspice model, we need to do some simulations:

1. For HCSL:
2. Add each 33ohm and 50ohm at the differential output;
3. Add each 33ohm resistor, 10inch trace and 2pF capacitor at the differential output, and add each 50ohm resistor to ground after 33ohm resistor;
4. For LVDS:
5. Add 100ohm resistor between the differential output, then add 150ohm resistor to the ground of each port, finally add another 100ohm resistor between differential output;
6. Add 100ohm resistor and 10inch trace between the differential output;
7. **Conclusion:**

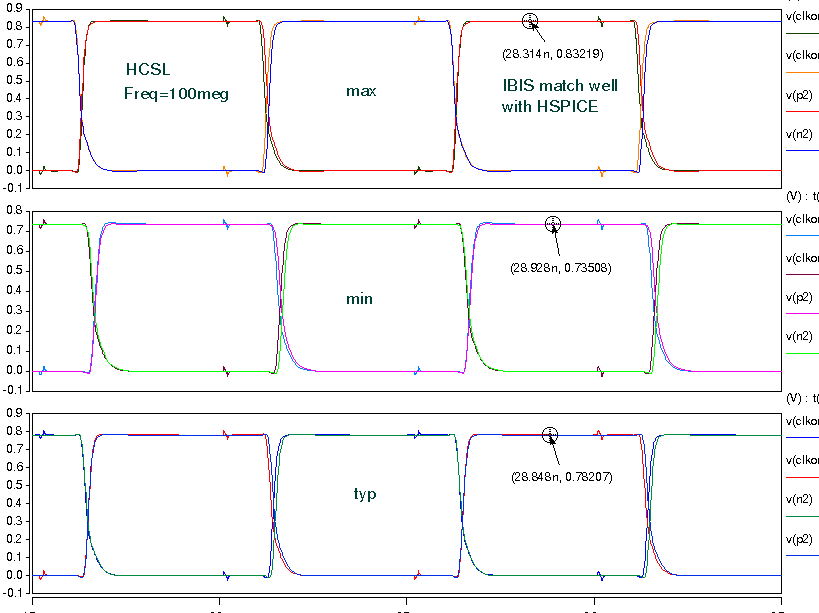
The simulated results show that the generated IBIS model can match well with the HSPICE model at different load conditions.

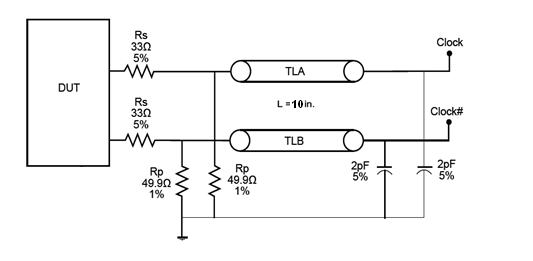
The DC swing can also match the datasheet for both HCSL and LVDS.

1. **HCSL simulation:**

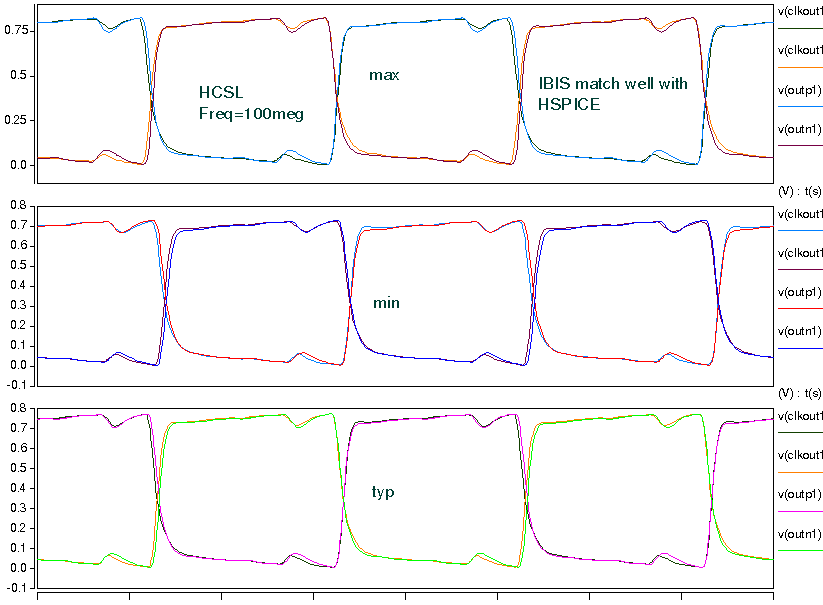


VDD=3.3V

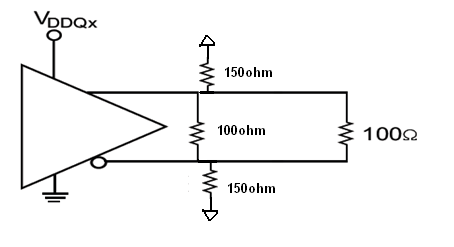




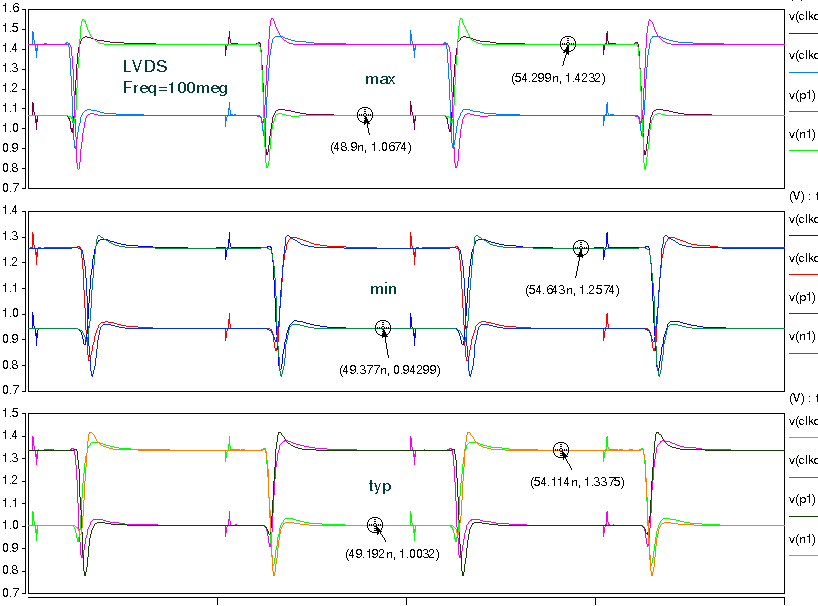
VDD=3.3V

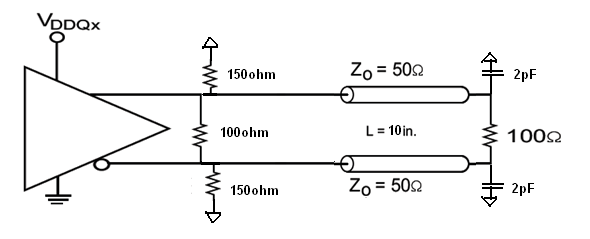


1. **LVDS simulation:**



VDD=3.3V





VDD=3.3V

