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PI6C557-03A EVB Use Manual

Pericom Application Engineering

1.1 Purpose

The PI6C557-03A is a spread spectrum clock generator compliant to PCI Express .Here provide test example of the EVB output frequency at 25M 100M 125M and 200M

1.2 Reference document

PI6C557-03A datasheet.

1.3 DUT sample

Device Parts No.: PI6C557-03A Demo board Package: 16pin TSSOP

1.4 Test theory

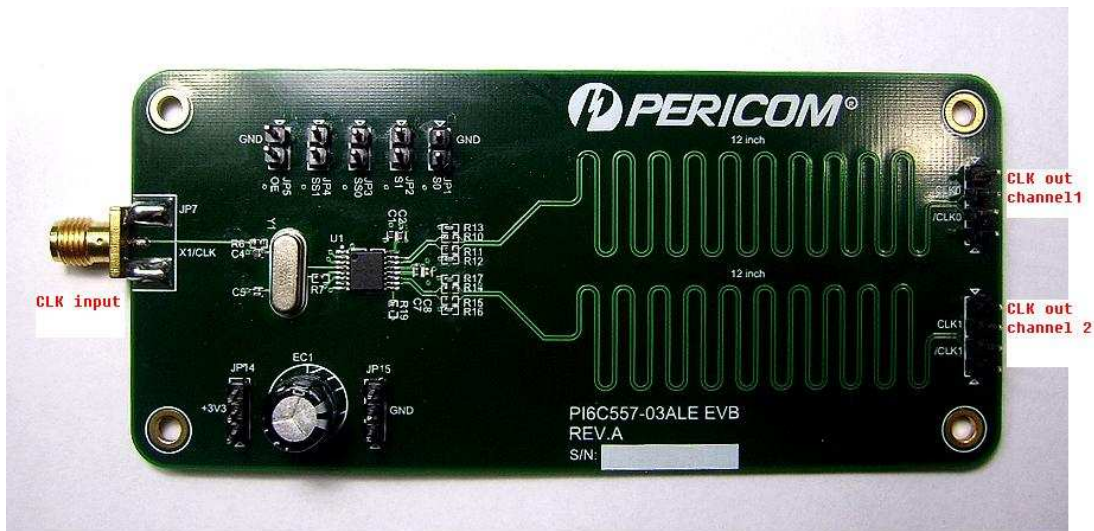
Use oscilloscope to test the output waveform of the chip

2.1 Equipment

Agilent 54855A oscilloscope
 Agilent E3631A Triple output DC power supply

2.2 Test demo board

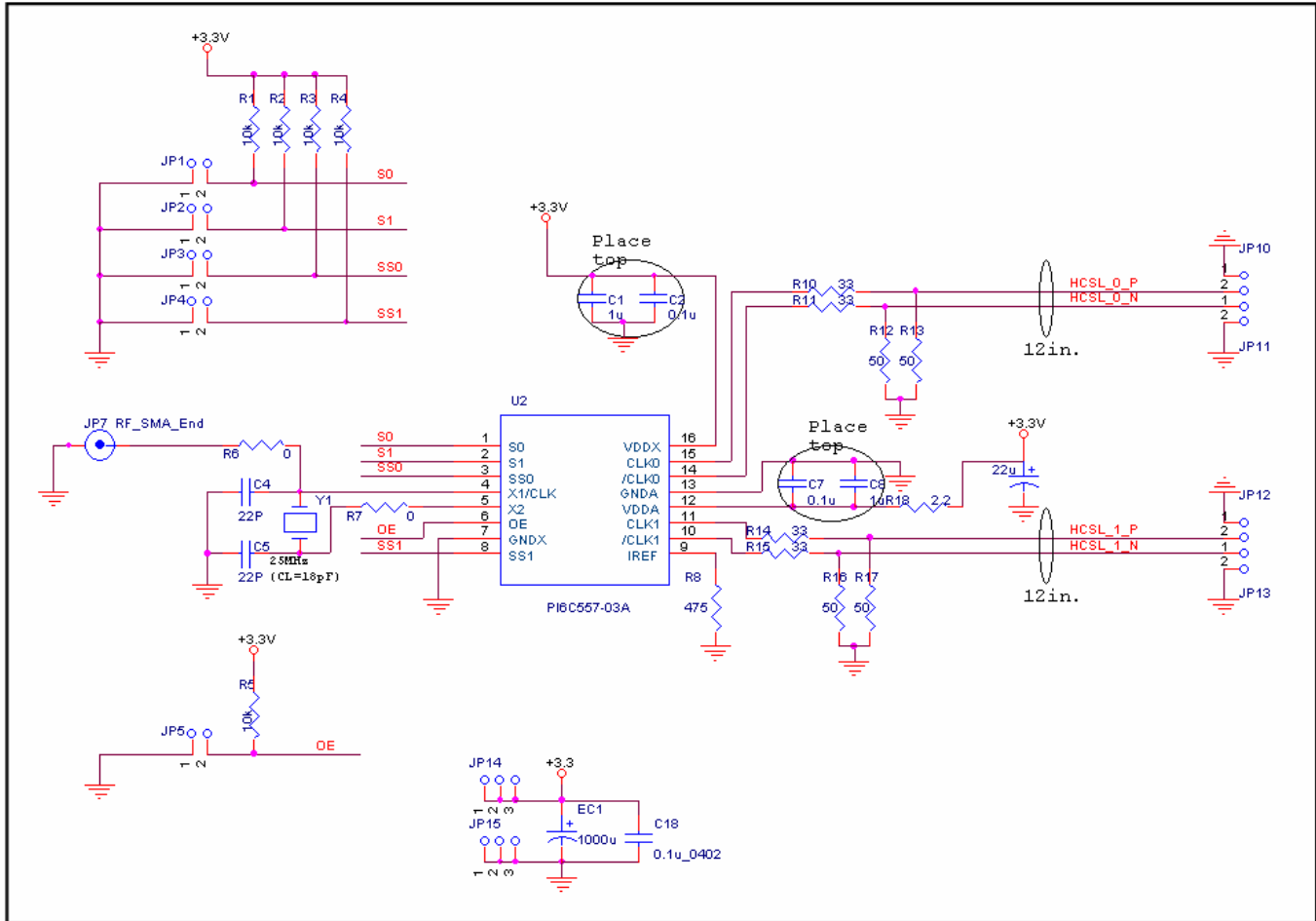
PI6C557-03A



2.3 EVB test schematic

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PI6C557-03A EVB Schematic



PI6C557-03A general use guide

Note:

- 1) Put all Vdd decoupling cap. in comp. side, all GNDs on one solid GND plane
- 2) Leave CLK0 and /CLK0 open if not used
- 3) For small size crystal, suggest R1=510 ohm
- 4) For output frequency and SSCG setting, please refer to the datasheet

Table 1: Output Select Table

S1	S0	CLK(MHz)
0	0	25
0	1	100
1	0	125
1	1	200

Table 2: Spread Selection Table

SS1	SS0	Spread
0	0	No Spread
0	1	Down -0.5
1	0	Down -0.75
1	1	No Spread

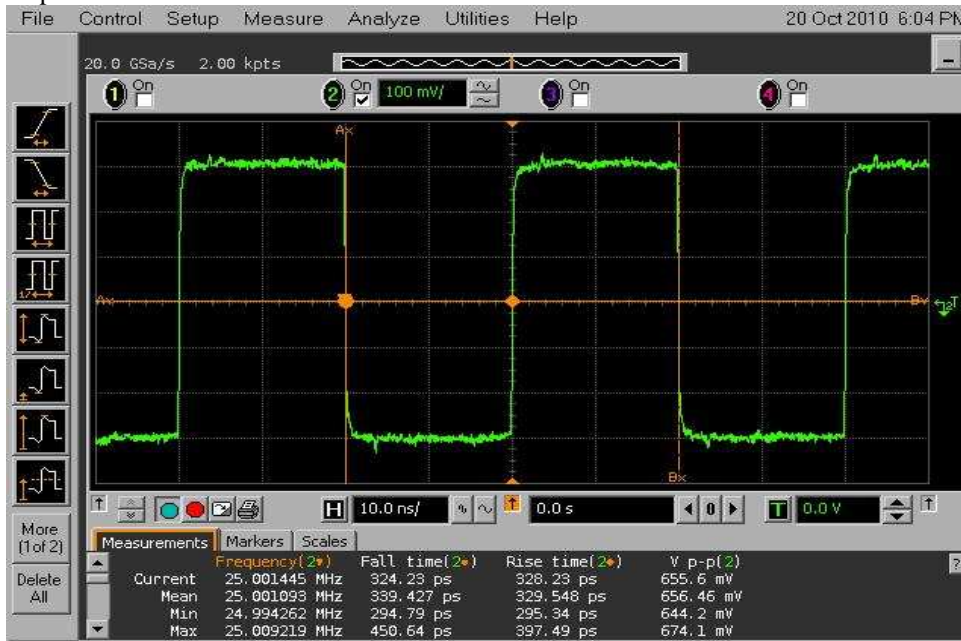
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3. EVB test summary

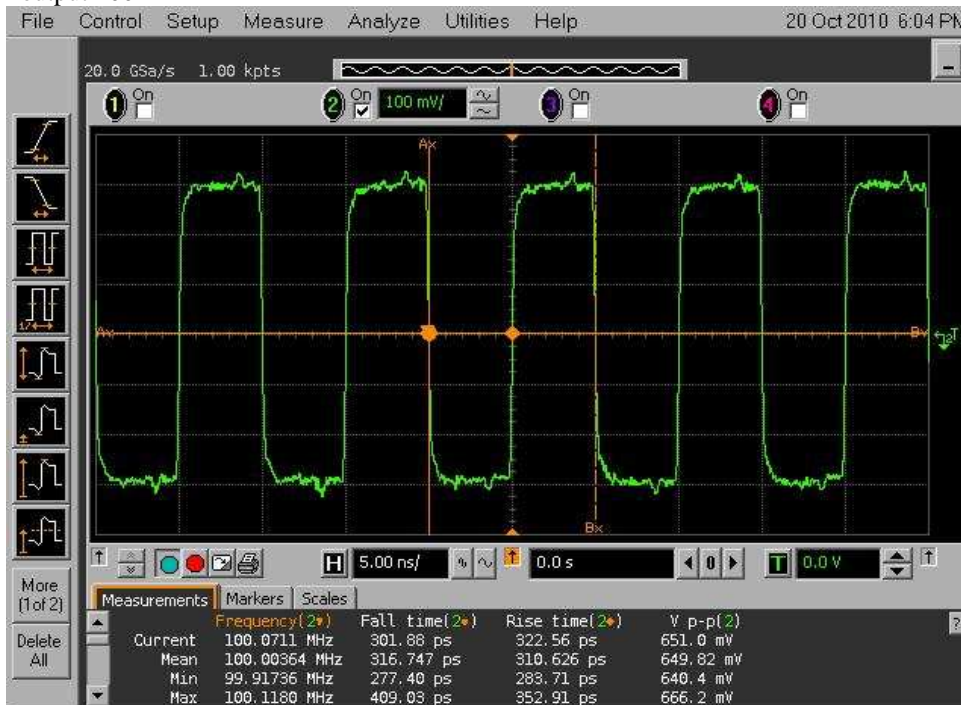
	25M	100M	125M	200M	Unit
Tr	328	322	308	307	Ps
Tf	324	301	304	312	Ps
V _{swing} (HCSL)	655	651	651	634	mV

3.1 HCSL output waveforms

i) CLK1 output 25M



ii) CLK 1 output 100M



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iii) CLK 1 output 125M



iv) CLK 1 output 200M

