



Note:

1. Each VDD poin needs 0.1uF decoupling cap.
2. Put 33 ohm souce termination close to pin <200mil. before do any via;
3. Leave un-used CLKx just open;
4. VDD supply should be = or > VDDOs' which need to use same supply voltage, for example VDD=3.3V; VDDO=3.3V or 2.5V;

Title		
PIC6C49X0210 Appli. circuit		
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