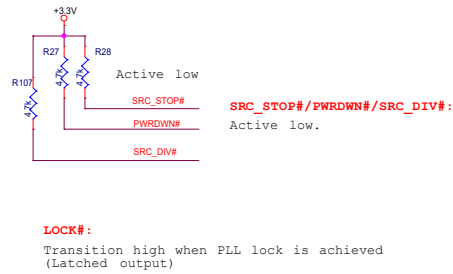
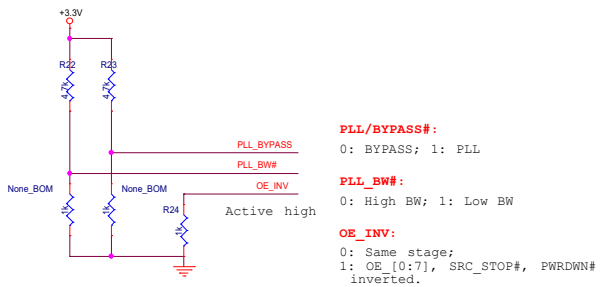


OE\_0  
Make individual OE\_x pull-up/down to enable/disable  
.  
.  
Active high  
OE\_7



**App Note:**

1. Each VDD pin needs 0.1u +1uF decoupling close to pin. (e.g.: VDD,VDD\_A...etc)
2. VDDA use small R=1-2 ohm or FB(ferrite bead)+C=10uF filtering for better DC/DC ripple noise rejection
3. Place serial 33 ohm and 50 ohm pull-down in comp. side close to pin <=300mil.
4. Put 475 ohm resistor closed to IREF Pin

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