**Check Report of PI6C10810 IBIS model**

**Introduction:**

To verify the correlation between the ibis model and hspice model, we need to do some simulations:

**The frequency of signal is 100MHz:**

Vclk in 0 pulse ( 0 pwr 0 0.1n 0.1n 4.9n 10n)

1. Without trace to the OUTPUT:

PI6C10810

**VOUT**

**SCL\_C**

**SDA\_C**

in

**SCL\_C**

**SDA\_C**

**Input Signals**

**SCL\_C**

**SDA\_C**

**VIN**

**SCL\_C**

**SDA\_C**

out

**SCL\_C**

**SDA\_C**

…..

**SCL\_C**

**SDA\_C**

1. With 33Ωresistor and 2-inch trace to the OUTPUT:

PI6C10810

**VOUT**

**SCL\_C**

**SDA\_C**

in

**SCL\_C**

**SDA\_C**

**Input Signals**

**SCL\_C**

**SDA\_C**

**VIN**

**SCL\_C**

**SDA\_C**

out

**SCL\_C**

**SDA\_C**

…..

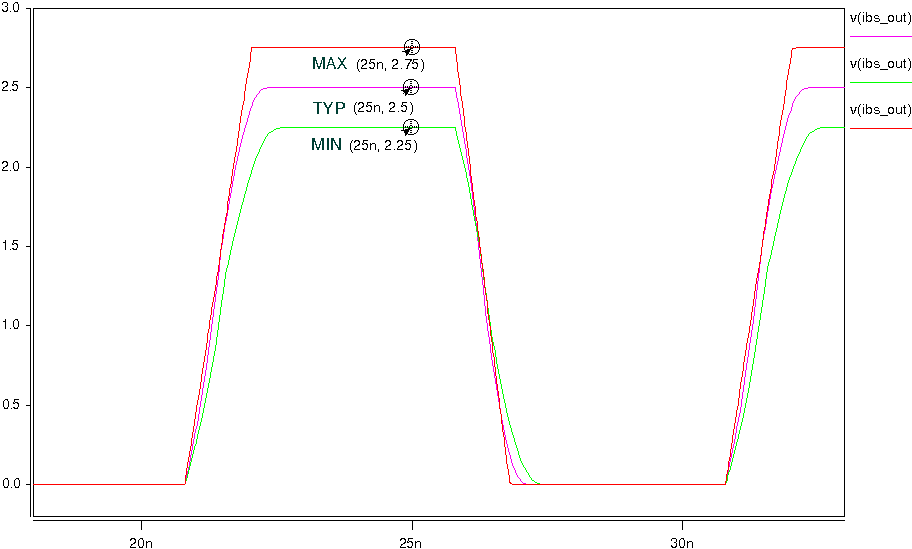
**SCL\_C**

**SDA\_C**

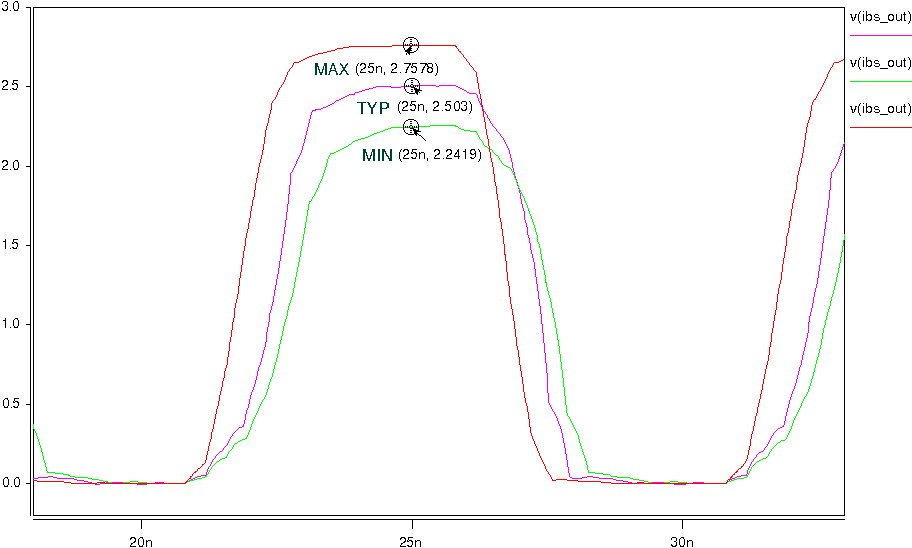
33Ω

2-inch

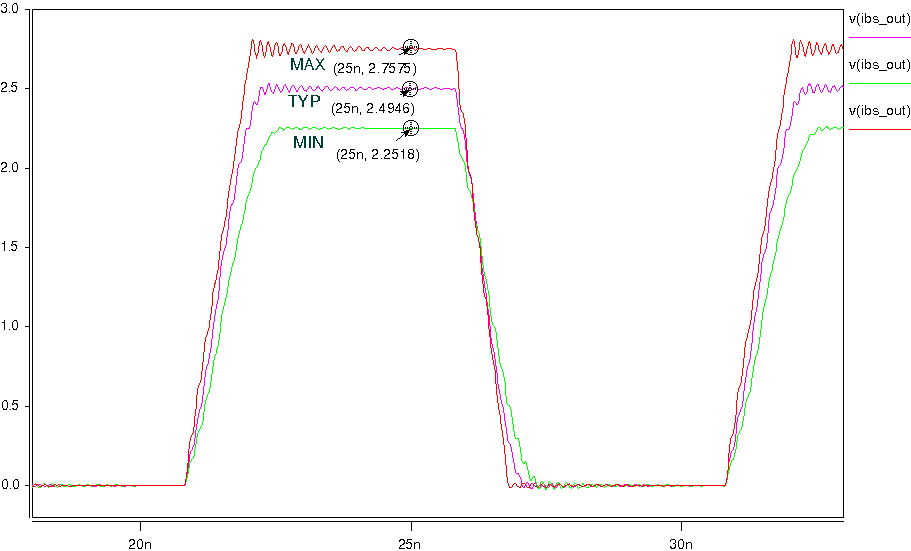
1. **Simulation without package data:**
2. Without trace to the OUTPUT:



1. With 33Ωresistor and 2-inch trace to the OUTPUT:



1. **Simulation with package data:**
2. Without trace to the OUTPUT:



1. With 33Ωresistor and 2-inch trace to the OUTPUT:

