**Verification of PI6C10806B IBIS model**

1. **Introduction: to verify the ibis models, we need to do some simulations:**

**The frequency of signal is 25MHz:** Vin in 0 pulse (0 pwr 0 0.1n 0.1n 19.9n 40n)

1. With **50Ω** resistor load to **VDD/2**

PI6C10806B

**VOUT**

**SCL\_C**

**SDA\_C**

**R**

**SCL\_C**

**SDA\_C**

**Input Signals**

**SCL\_C**

**SDA\_C**

**VIN**

**SCL\_C**

**SDA\_C**

CLK0

**SCL\_C**

**SDA\_C**

IN

**SCL\_C**

**SDA\_C**

**VDD/2**

**SCL\_C**

**SDA\_C**

1. PI6C10806B\_L\_33V\_10p.ibs;
2. PI6C10806B\_L\_33V\_15p.ibs.
3. Add **33Ω** resistor, 5 inches trace and 5pf pull-down capacitance

**VOUT**

**SCL\_C**

**SDA\_C**

IN

**SCL\_C**

**SDA\_C**

**Input Signals**

**SCL\_C**

**SDA\_C**

**VIN**

**SCL\_C**

**SDA\_C**

CLK0

**SCL\_C**

**SDA\_C**

5-inch

PI6C10806B

-06

**C**

**SCL\_C**

**SDA\_C**

**R**

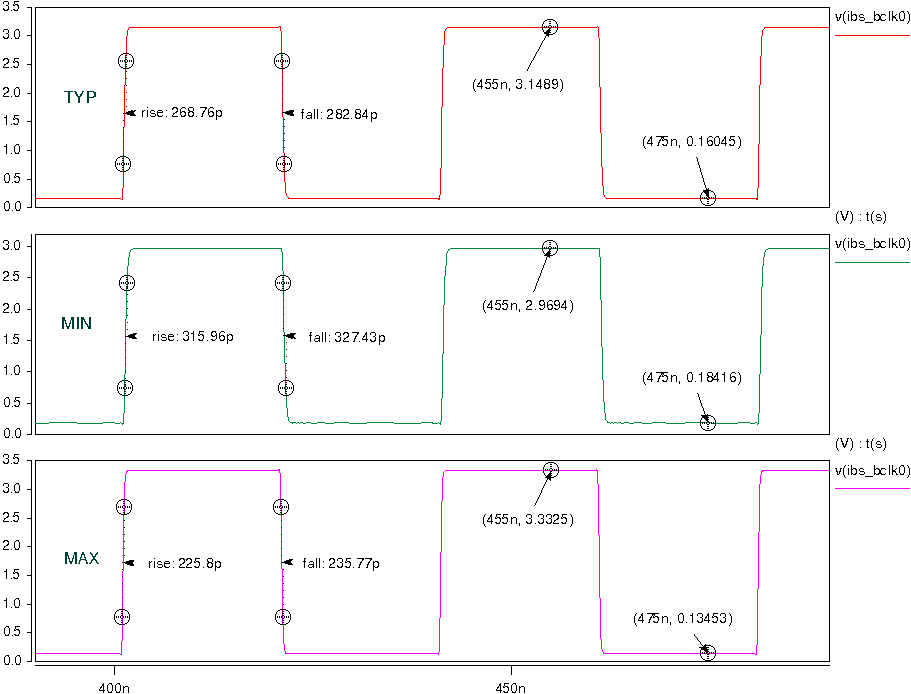
**SCL\_C**

**SDA\_C**

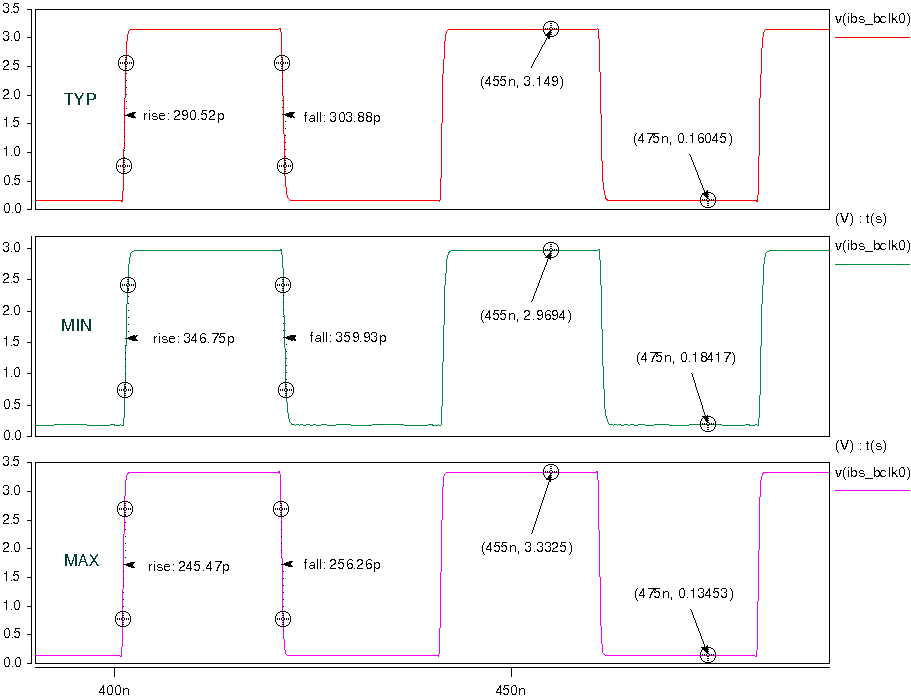
1. PI6C10806B\_L\_33V\_10p.ibs;
2. PI6C10806B\_L\_33V\_15p.ibs.
3. **Conclusion:**

Choose 10pF created ibis, considering mini. and max. normally we like to give a little faster rate ibis to customer.

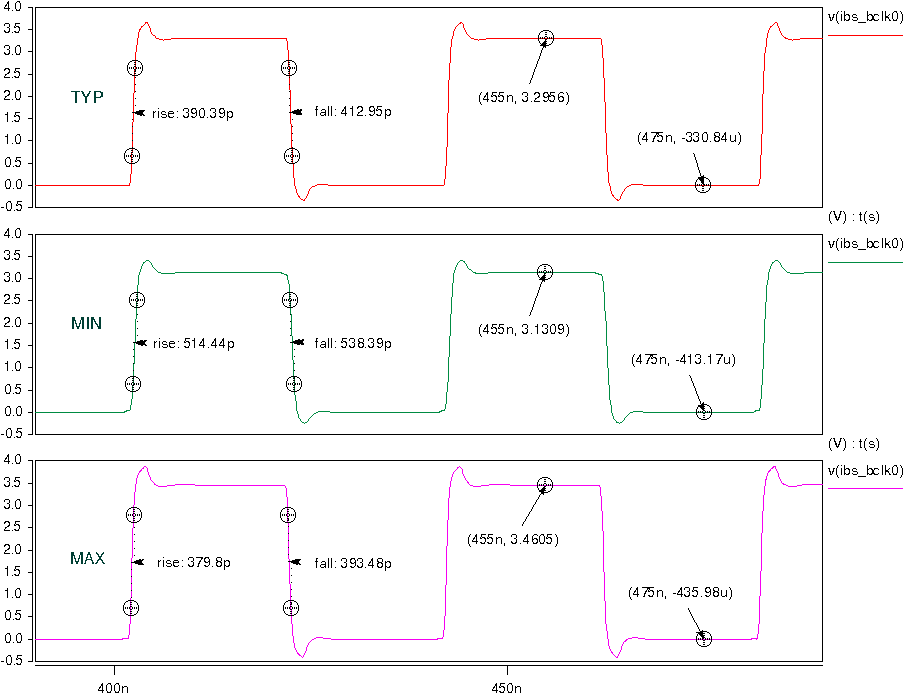
1. **Simulation Result:**
2. With **50Ω** resistor load to **VDD/2**
3. PI6C10806B\_L\_33V\_10p.ibs;



1. PI6C10806B\_L\_33V\_15p.ibs.



1. Add **33Ω** resistor, 5 inches trace and 5pf pull-down capacitance
2. PI6C10806B\_L\_33V\_10p.ibs;



1. PI6C10806B\_L\_33V\_15p.ibs.

