**Verification of PI4MSD5V9542A IBIS model**

1. **Introduction:**

To verify the correlation between the ibis model and hspice model, we need to do some simulations:

**The frequency of signal is 1MHz:**

vin sc0 0 pulse 0 **high-level** 0 2n2n 498n 1000n

Add **500Ω** pull-down resistor to the OUTPUT：

 **high-level=1.8V**

PI4MSD5V9542A

**OUT**

**SCL\_C**

**SDA\_C**

R

**SCL\_C**

**SDA\_C**

sc0

**SCL\_C**

**SDA\_C**

**Input Signals**

**SCL\_C**

**SDA\_C**

**Output**

**SCL\_C**

**SDA\_C**

scl

**SCL\_C**

**SDA\_C**

**VIN**

**SCL\_C**

**SDA\_C**

1. without package;
2. with package.
3. **Conclusion:**
4. For SWITCH1, the simulation results of IBIS model can match quite well with the HSPICE model at different load conditions.

1. **Simulation Result:**

**high-level=1.8V**

1. without package;



1. with package.

