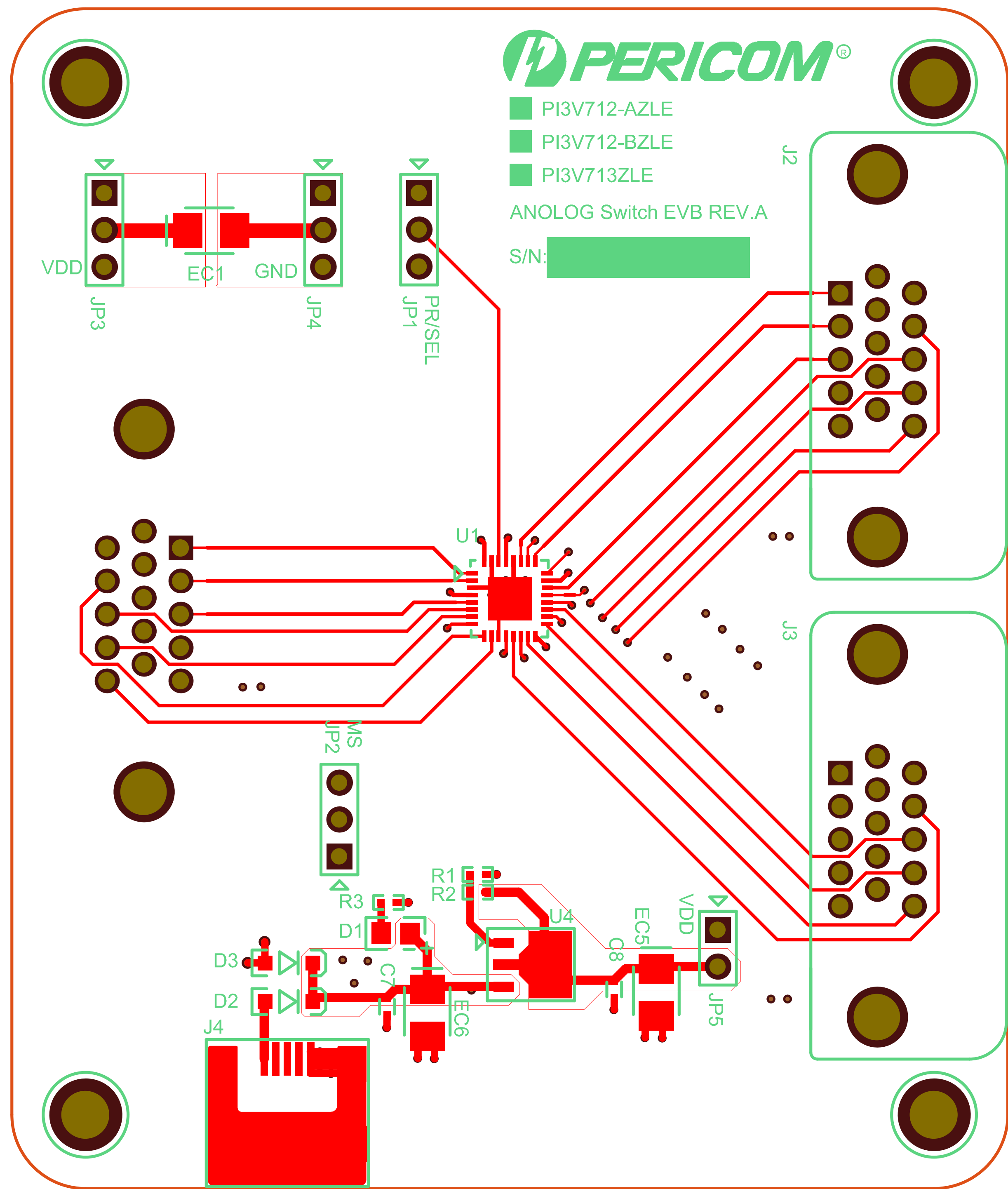


IMPEDANCE CHART

LAYER(S)	TRACE TYPE	SINGLE-ENDED IMPEDANCE REQ'D	DIFFERENTIAL-ENDED IMPEDANCE REQ'D	TOLERANCE
1	--	--	--	+/- 10%
1	10mil	75 ohm	--	+/- 10%




<div><div></div><div>Pericom</div><div></div></div>				
PCB NAME	PI3V71X-A&BZLE ANOLOG SW EVB			REV.A
DATE	12/08/2009	Drawn by	Haojie Ci	
TITLE	3 IN 1 USB 2.0 HS IC WITH 1 LAYER			
SHEET NUMBER	1		OF	10



Pericom			
PCB NAME	PI3V71X-A&BZLE ANOLOG SW EVB		REV.A
DATE	12/08/2009	Drawn by	Haojie Ci
TITLE	TOP LAYER		
SHEET NUMBER	5	OF	10



			
PCB NAME	PI3V71X-A&BZLE ANOLOG SW EVB		REV.A
DATE	12/08/2009	Drawn by	Haojie Ci
TITLE	BOTTOM LAYER GREEN LAYER		
SHEET NUMBER	4 OF 10		