**Verification of PI3USB42 33V IBIS model**

**Introduction:**

To verify the correlation between the ibis model and hspice model, we need to do some simulations:

PI3USB42

**VOUT**

**SCL\_C**

**SDA\_C**

**R**

**SCL\_C**

**SDA\_C**

D-

**SCL\_C**

**SDA\_C**

**C**

**SCL\_C**

**SDA\_C**

**Input Signals**

**SCL\_C**

**SDA\_C**

**VIN**

**SCL\_C**

**SDA\_C**

D-A

**SCL\_C**

**SDA\_C**

…..

**SCL\_C**

**SDA\_C**

D+

**SCL\_C**

**SDA\_C**

**VIN**

**SCL\_C**

**SDA\_C**

**VOUT**

**SCL\_C**

**SDA\_C**

**R**

**SCL\_C**

**SDA\_C**

**C**

**SCL\_C**

**SDA\_C**

D+A

**SCL\_C**

**SDA\_C**

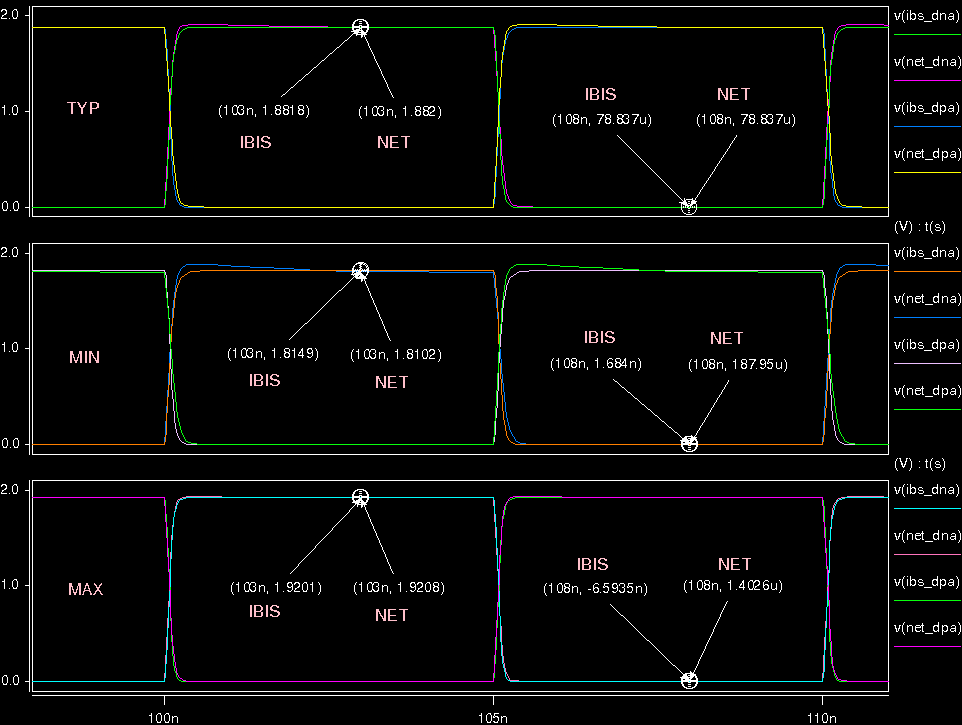
**The frequency of signal is 100MHz:**

vinn dn 0 pulse ( 0 2 0 0.1n 0.1n 4.9n 10n )

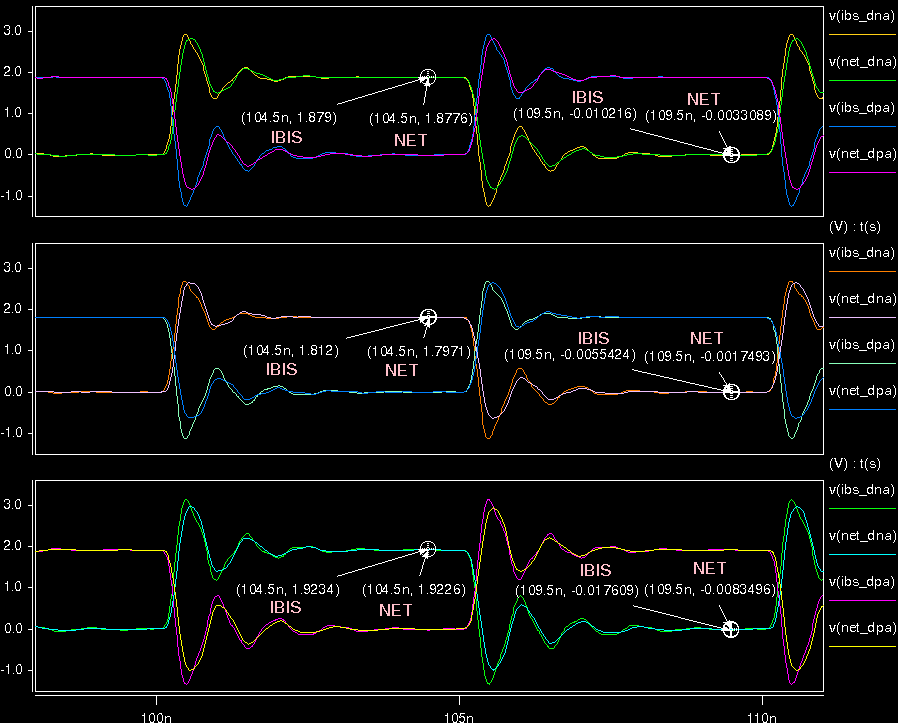
vinp dp 0 pulse ( 2 0 0 0.1n 0.1n 4.9n 10n )

Add **100Ω** pull-down resistor and **10pF** pull-down capacitance to the output;

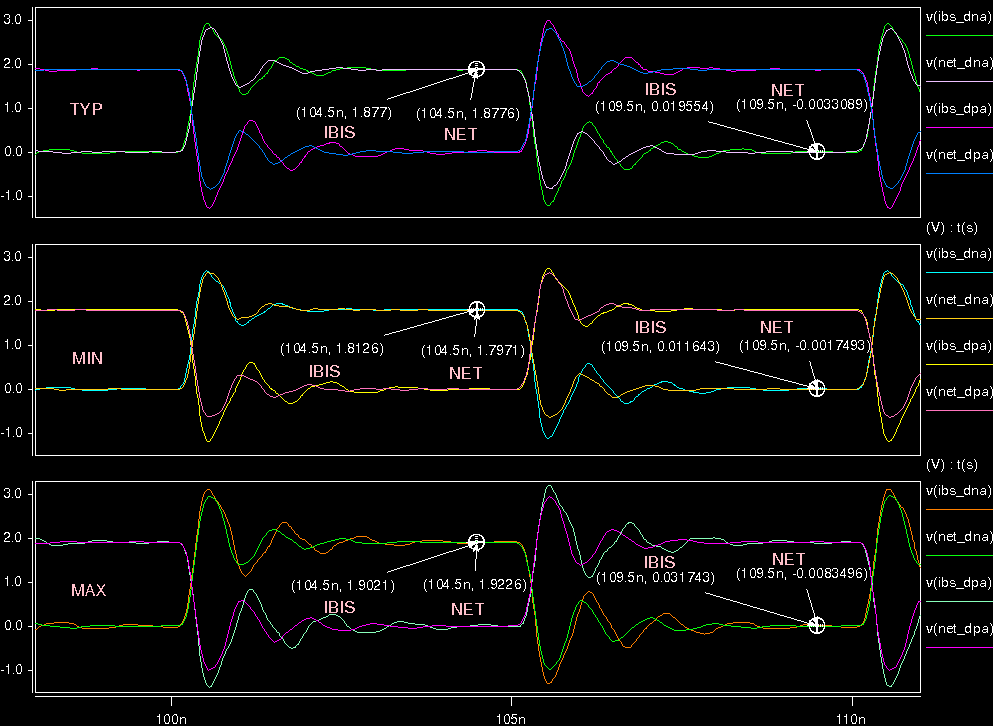
1. **SWITCH1：**
2. Simulation without package data:



1. Simulation with package data(**NEW**):



1. Simulation with package data(**OLD**):

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