**Verification of PI3USB221 IBIS model**

1. **Introduction:**

To verify the correlation between the ibis model and hspice model, we need to do some simulations:

**The frequency of signal is 10MHz:**

vin a 0 pulse 0 **high-level** 0 2n2n 48n 100n

Add **50Ω** pull-down resistor and 50**pF** pull-down capacitance to the OUTPUT：

**high-level=1V**

PI3USB221

**OUT**

**SCL\_C**

**SDA\_C**

R

**SCL\_C**

**SDA\_C**

a

**SCL\_C**

**SDA\_C**

C

**SCL\_C**

**SDA\_C**

**Input Signals**

**SCL\_C**

**SDA\_C**

**Output**

**SCL\_C**

**SDA\_C**

b1

**SCL\_C**

**SDA\_C**

**VIN**

**SCL\_C**

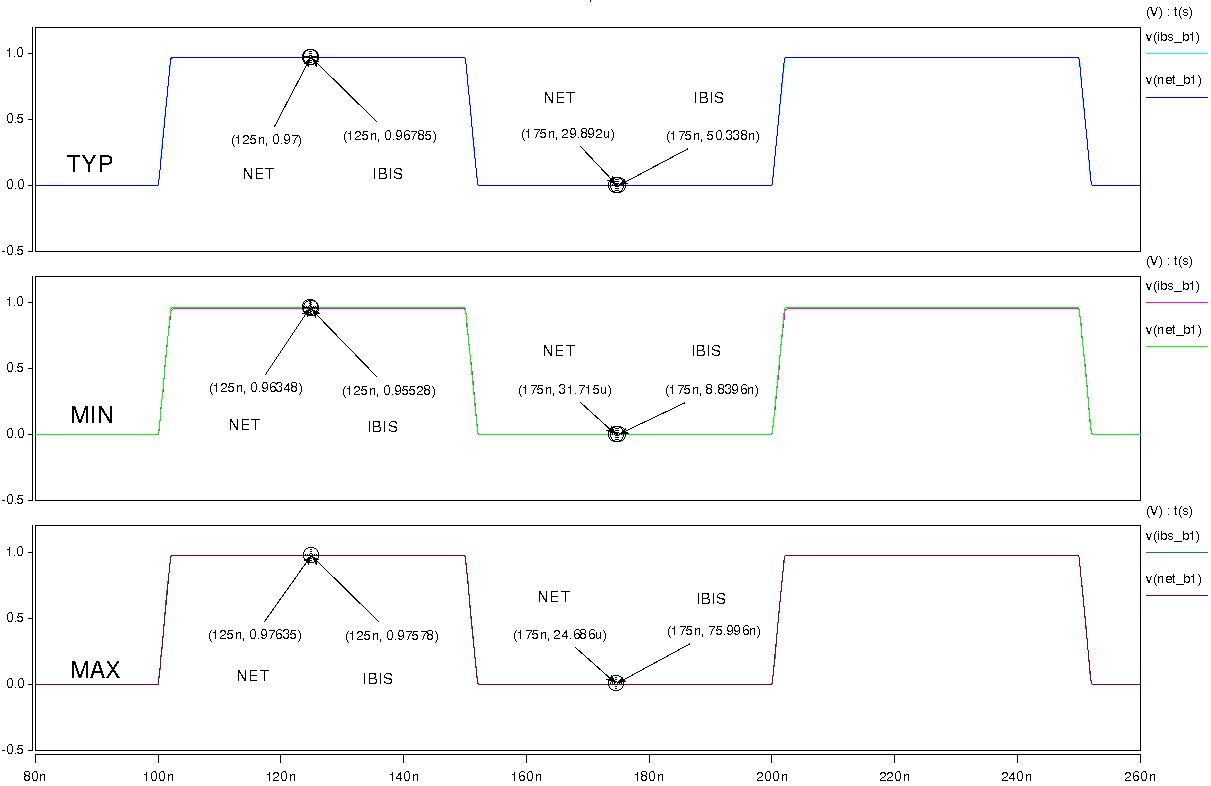
**SDA\_C**

1. without package;
2. with package.
3. **Conclusion:**
4. For SWITCH1, the simulation results of IBIS model can match quite well with the HSPICE model at different load conditions.

1. **Simulation Result:**

**high-level=1V**

1. without package;



1. with package.

