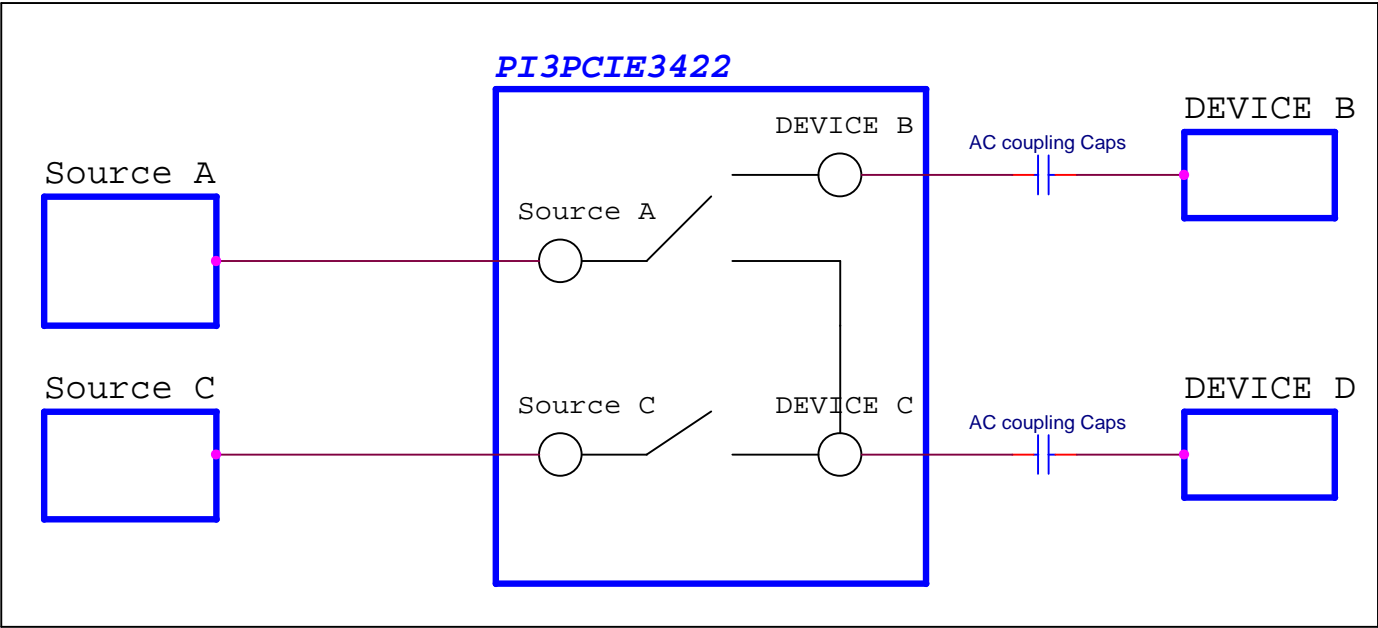
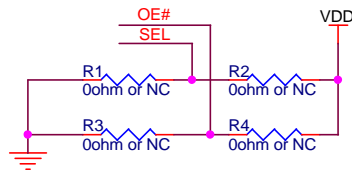
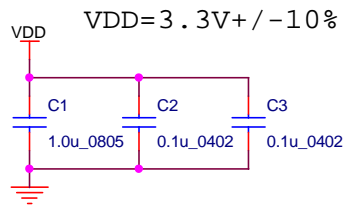


Revision History

Date	History
8/19/2014	First Released

Block Diagram





OE#	SEL	Function
L	L	Ax=Bx, Cx=Dx
L	H	Ax=Dx, Cx=Bx=Hi-Z
H	X	Ax=Bx=Cx=Dx=Hi-Z (Disconnect)

Source A

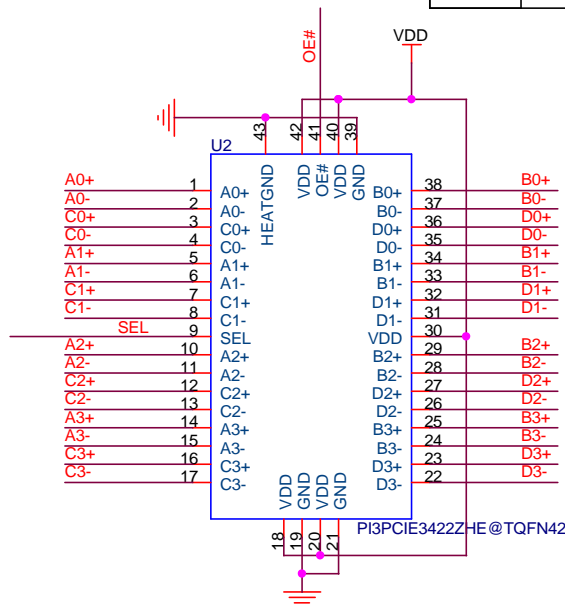


A0_P	AC Coupling Cap	0.22uF	A0+
A0_N	AC Coupling Cap	0.22uF	A0-
A1_P	AC Coupling Cap	0.22uF	A1+
A1_N	AC Coupling Cap	0.22uF	A1-
A2_P	AC Coupling Cap	0.22uF	A2+
A2_N	AC Coupling Cap	0.22uF	A2-
A3_P	AC Coupling Cap	0.22uF	A3+
A3_N	AC Coupling Cap	0.22uF	A3-

Source C

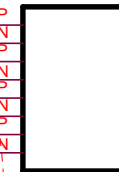


C0_P	AC Coupling Cap	0.22uF	C0+
C0_N	AC Coupling Cap	0.22uF	C0-
C1_P	AC Coupling Cap	0.22uF	C1+
C1_N	AC Coupling Cap	0.22uF	C1-
C2_P	AC Coupling Cap	0.22uF	C2+
C2_N	AC Coupling Cap	0.22uF	C2-
C3_P	AC Coupling Cap	0.22uF	C3+
C3_N	AC Coupling Cap	0.22uF	C3-



B0+	0.22uF	AC Coupling Cap	B0_P
B0-	0.22uF	AC Coupling Cap	B0_N
B1+	0.22uF	AC Coupling Cap	B1_P
B1-	0.22uF	AC Coupling Cap	B1_N
B2+	0.22uF	AC Coupling Cap	B2_P
B2-	0.22uF	AC Coupling Cap	B2_N
B3+	0.22uF	AC Coupling Cap	B3_P
B3-	0.22uF	AC Coupling Cap	B3_N

Device B



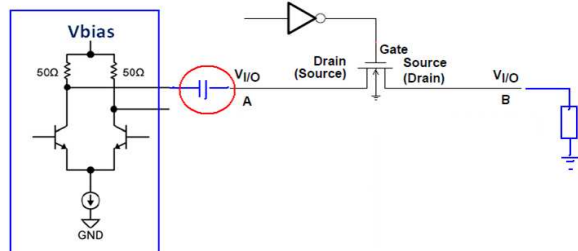
Device D

D0+	0.22uF	AC Coupling Cap	D0_P
D0-	0.22uF	AC Coupling Cap	D0_N
D1+	0.22uF	AC Coupling Cap	D1_P
D1-	0.22uF	AC Coupling Cap	D1_N
D2+	0.22uF	AC Coupling Cap	D2_P
D2-	0.22uF	AC Coupling Cap	D2_N
D3+	0.22uF	AC Coupling Cap	D3_P
D3-	0.22uF	AC Coupling Cap	D3_N



Use AC coupling cap to block DC bias.

(1) If source side signal input Vbias Voltage higher than device Vbias Voltage, need to add the AC coupling capacitor at switch signal input.



(2) If Device side signal Vbias voltage more than source IC, need to add the AC coupling at Switch signal output.

OR

