



HDMI Test Report

Overall Result: **PASS**

Test Configuration Details	
Device Description	
Device ID	Transmitter
Fixture Type	Other
Probe Connection	4 Probes
Probe Head Type	N5444A
Lane Connection	1 Data Lane
HDMI Specification	2.0
HDMI Test Type	TMDS Physical Layer Tests
Test Session Details	
Infiniium SW Version	05.60.00603
Infiniium Model Number	DSOX92504A
Infiniium Serial Number	MY54410104
Application SW Version	2.11
Debug Mode Used	No
Probe (Channel 1)	Model: N2801A Serial: US54094067 Head: N5444A Atten: Calibrated (9 AUG 2016 14:10:30), Using Cal Atten (5.6226E+000) Skew: Calibrated (9 AUG 2016 14:11:12), Using Cal Skew
Probe (Channel 2)	Model: N2801A Serial: US54094054 Head: N5444A Atten: Calibrated (9 AUG 2016 14:15:19), Using Cal Atten (5.4285E+000) Skew: Calibrated (9 AUG 2016 14:16:00), Using Cal Skew
Probe (Channel 3)	Model: N2801A Serial: US54094059 Head: N5444A Atten: Calibrated (9 AUG 2016 14:19:22), Using Cal Atten (5.6325E+000) Skew: Calibrated (9 AUG 2016 14:20:11), Using Cal Skew
Probe (Channel 4)	Model: N2801A Serial: US54094057 Head: N5444A Atten: Calibrated (9 AUG 2016 15:17:37), Using Cal Atten (5.5290E+000) Skew: Calibrated (9 AUG 2016 15:18:23), Using Cal Skew
Last Test Date	2016-08-26 12:54:52 UTC +08:00

Summary of Results

Test Statistics	
Failed	0
Passed	46
Total	46

Margin Thresholds	
Warning	< 2 %
Critical	< 0 %

Pass	# Failed	# Trials	Test Name	Actual Value	Margin	Pass Limits
✓	0	1	HF1-2: Clock Rise Time	127.210 ps	69.6 %	VALUE >= 75.000 ps
✓	0	1	HF1-2: Clock Fall Time	127.596 ps	70.1 %	VALUE >= 75.000 ps
✓	0	1	HF1-6: Clock Duty Cycle(Minimum)	49.760	24.4 %	>=40%
✓	0	1	HF1-6: Clock Duty Cycle(Maximum)	50.310	16.2 %	<=60%
✓	0	1	HF1-6: Clock Rate	148.456300000 MHz	2.4 %	85.000000000 MHz <= VALUE <= 150.000000000 MHz
✓	0	1	HF1-7: Differential Clock Voltage Swing, Vs (TP1)	1.097 V	12.9 %	400 mV < VALUE < 1.200 V
✓	0	1	HF1-7: Clock Jitter (TP2 EQ with Worst Case Positive Skew)	175 mTbit	41.7 %	VALUE <= 300 mTbit
✓	0	1	HF1-7: Clock Jitter (TP2 EQ with Worst Case Negative Skew)	225 mTbit	25.0 %	VALUE <= 300 mTbit
✓	0	1	HF1-5: D0 Maximum Differential Voltage	605 m	22.4 %	VALUE <= 780 m
✓	0	1	HF1-5: D0 Minimum Differential Voltage	-598 m	23.3 %	VALUE >= -780 m
✓	0	1	HF1-2: D0 Rise Time	107.100 ps	152.0 %	VALUE >= 42.500 ps
✓	0	1	HF1-2: D0 Fall Time	107.220 ps	152.3 %	VALUE >= 42.500 ps
✓	0	1	HF1-8: D0 Mask Test (TP2 EQ with Worst Case Positive Skew)	0.000	50.0 %	No Mask Failures
✓	0	1	HF1-8: D0 Mask Test (TP2 EQ with Worst Case Negative Skew)	0.000	50.0 %	No Mask Failures
✓	0	1	HF1-1: VL Clock +	2.731 V	46.1 %	2.300 V <= VALUE <= 3.100 V
✓	0	1	HF1-1:Clock + VSwing	552 mV	12.0 %	200 mV <= VALUE <= 600 mV
✓	0	1	HF1-1: VL Clock -	2.723 V	47.1 %	2.300 V <= VALUE <= 3.100 V
✓	0	1	HF1-1:Clock - VSwing	552 mV	12.0 %	200 mV <= VALUE <= 600 mV
✓	0	1	HF1-4: Intra-Pair Skew - Clock	28 mTbit	40.7 %	-150 mTbit <= VALUE <= 150 mTbit
✓	0	1	HF1-1: VL D0+	2.777 V	20.5 %	2.300 V <= VALUE <= 2.900 V
✓	0	1	HF1-1: D0+ VSwing	488 mV	44.0 %	400 mV <= VALUE <= 600 mV
✓	0	1	HF1-1: VL D0-	2.764 V	22.7 %	2.300 V <= VALUE <= 2.900 V
✓	0	1	HF1-1: D0- VSwing	501 mV	50.0 %	400 mV <= VALUE <= 600 mV
✓	0	1	HF1-4: Intra-Pair Skew - Data Lane 0	50 mTbit	33.3 %	-150 mTbit <= VALUE <= 150 mTbit
✓	0	1	HF1-5: D1 Maximum Differential Voltage	610 m	21.8 %	VALUE <= 780 m
✓	0	1	HF1-5: D1 Minimum Differential Voltage	-601 m	22.9 %	VALUE >= -780 m
✓	0	1	HF1-2: D1 Rise Time	109.200 ps	156.9 %	VALUE >= 42.500 ps
✓	0	1	HF1-2: D1 Fall Time	106.130 ps	149.7 %	VALUE >= 42.500 ps
✓	0	1	HF1-8: D1 Mask Test (TP2 EQ with Worst Case Positive Skew)	0.000	50.0 %	No Mask Failures
✓	0	1	HF1-8: D1 Mask Test (TP2 EQ with Worst Case Negative Skew)	0.000	50.0 %	No Mask Failures
✓	0	1	HF1-4: Intra-Pair Skew - Data Lane 1	-26 mTbit	41.3 %	-150 mTbit <= VALUE <= 150 mTbit
✓	0	1	HF1-1: VL D1+	2.759 V	23.5 %	2.300 V <= VALUE <= 2.900 V
✓	0	1	HF1-1: D1+ VSwing	492 mV	46.0 %	400 mV <= VALUE <= 600 mV
✓	0	1	HF1-1: VL D1-	2.756 V	24.0 %	2.300 V <= VALUE <= 2.900 V
✓	0	1	HF1-1: D1- VSwing	492 mV	46.0 %	400 mV <= VALUE <= 600 mV
✓	0	1	HF1-5: D2 Maximum Differential Voltage	603 m	22.7 %	VALUE <= 780 m
✓	0	1	HF1-5: D2 Minimum Differential Voltage	-593 m	24.0 %	VALUE >= -780 m
✓	0	1	HF1-2: D2 Rise Time	103.460 ps	143.4 %	VALUE >= 42.500 ps
✓	0	1	HF1-2: D2 Fall Time	103.160 ps	142.7 %	VALUE >= 42.500 ps
✓	0	1	HF1-8: D2 Mask Test (TP2 EQ with Worst Case Positive Skew)	0.000	50.0 %	No Mask Failures
✓	0	1	HF1-8: D2 Mask Test (TP2 EQ with Worst Case Negative Skew)	0.000	50.0 %	No Mask Failures
✓	0	1	HF1-1: VL D2+	2.775 V	20.8 %	2.300 V <= VALUE <= 2.900 V
✓	0	1	HF1-1: D2+ VSwing	487 mV	43.5 %	400 mV <= VALUE <= 600 mV

✓	0	1	HF1-1: VL D2-	2.775 V	20.8 %	2.300 V <= VALUE <= 2.900 V
✓	0	1	HF1-1: D2- VSwing	479 mV	39.5 %	400 mV <= VALUE <= 600 mV
✓	0	1	HF1-4: Intra-Pair Skew - Data Lane 2	21 mTbit	43.0 %	-150 mTbit <= VALUE <= 150 mTbit

Report Detail

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✓ HF1-2: Clock Rise Time

Reference: Test ID HF1-2

Test Summary: Pass **Test Description:** 2 Channels Connection Model: The transition time is defined as the time interval between the normalized 20% and 80% amplitude levels. For compliance, the DUT should output the highest supported pixel clock frequency during the test.

Pass Limits: >= 75.000 ps **Raw Clock Transition Time** 127.210 ps

Result Details

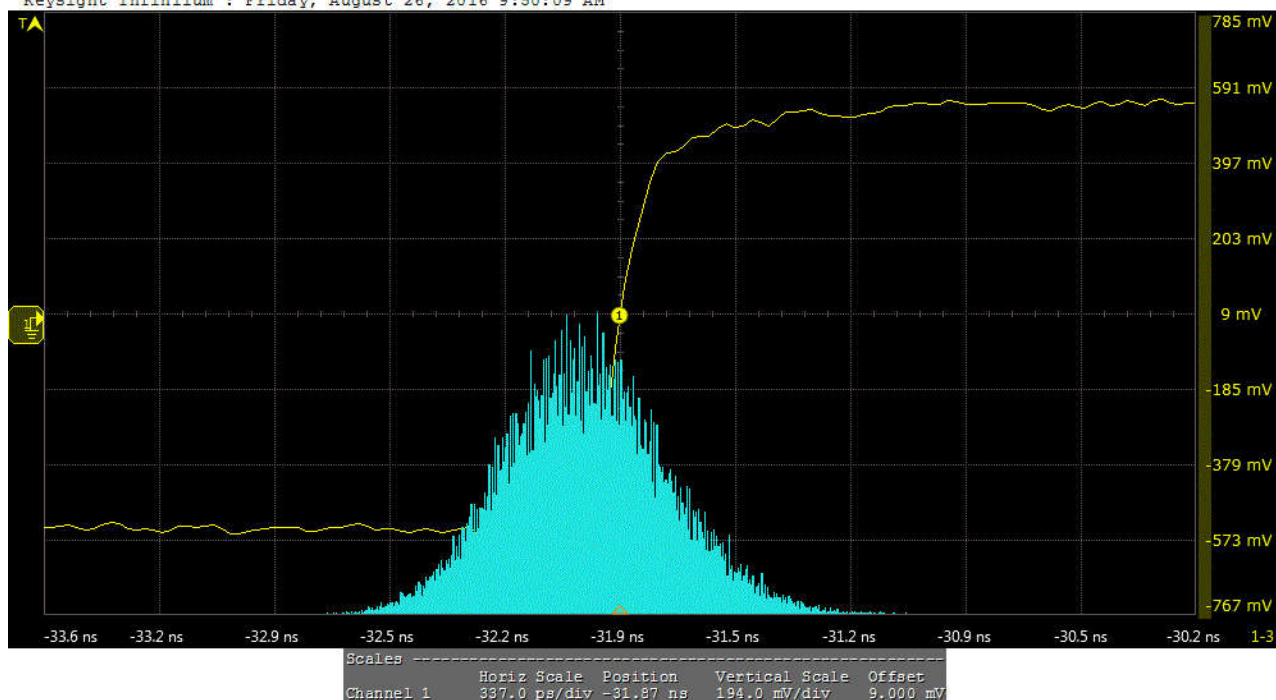
HDMIAutomationConfig	Timing 101	Test Frequency(MHz)	148.456	Upper Threshold(%)	80.000	Lower Threshold(%)	20.000
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# Edges	59.399000 k	Acquisition Bandwidth (GHz)	13.000
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Trial 1

Trial 1: Raw Clock Transition Time

Keysight Infiniium : Friday, August 26, 2016 9:50:09 AM



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✓ HF1-2: Clock Fall Time

Reference: Test ID HF1-2

Test Summary: Pass **Test Description:** 2 Channels Connection Model: The transition time is defined as the time interval between the normalized 20% and 80% amplitude levels. For compliance, the DUT should output the highest supported pixel clock frequency during the test.

Pass Limits: >= 75.000 ps **Raw Clock Transition Time** 127.596 ps

Result Details

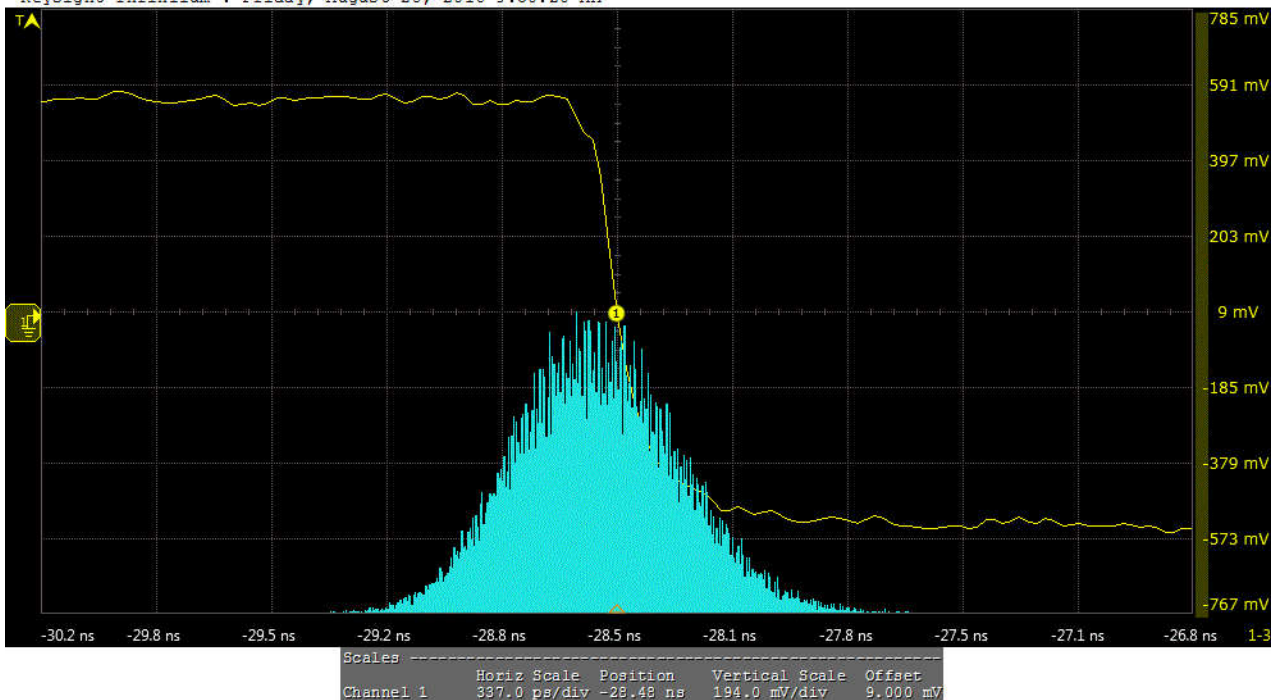
HDMIAutomationConfig	Timing 101	Test Frequency(MHz)	148.456	Upper Threshold(%)	80.000	Lower Threshold(%)	20.000
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# Edges	59.399000 k	Acquisition Bandwidth (GHz)	13.000
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Trial 1

Trial 1: Raw Clock Transition Time

Keysight Infiniium : Friday, August 26, 2016 9:50:20 AM



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HF1-6: Clock Duty Cycle(Minimum)

Reference: Test ID HF1-6

Test Summary: Pass **Test Description:** Confirm that the duty cycle of the differential TMDS clock does not exceed the limits allowed by the specification. For compliance, the DUT should output the highest supported pixel clock frequency during the test.

Pass Limits: >=40% **Clock Duty Cycle Minimum** 49.760

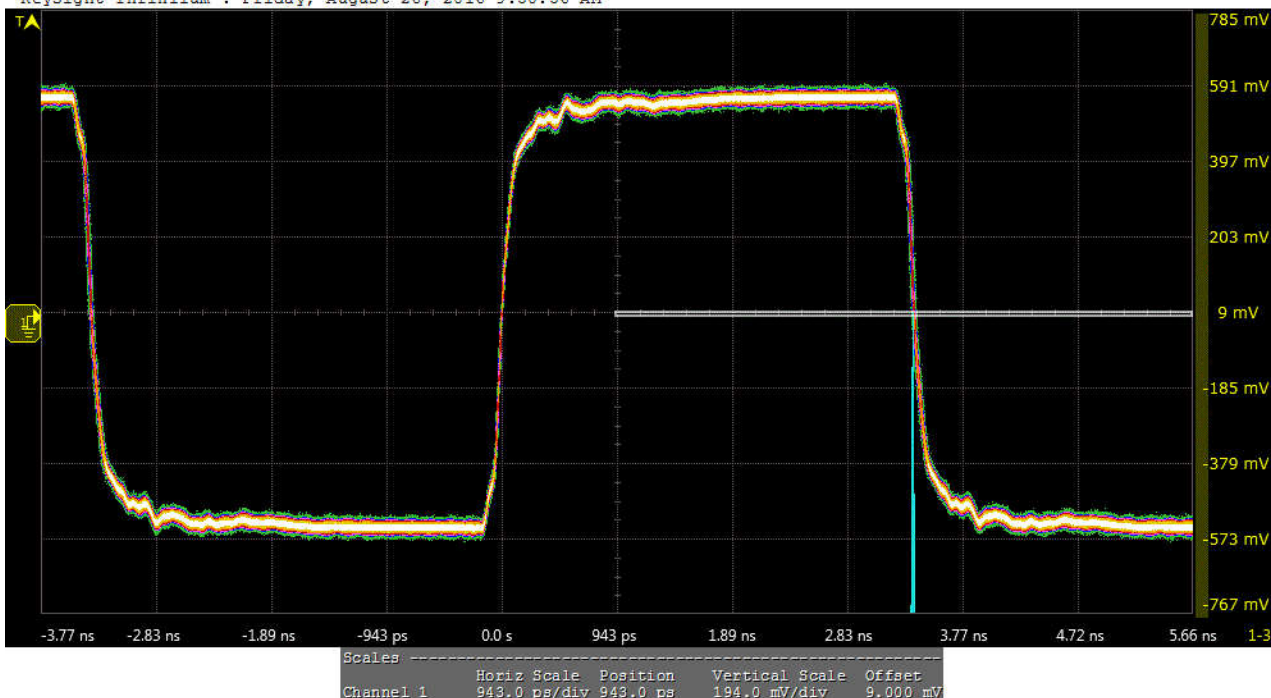
Result Details

HDMIAutomationConfig	Timing 101	Test Frequency(MHz)	148.456	# Edges	(no value)	TdutyMIN(ns)	3.352
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Trial 1

Trial 1: Clock Duty Cycle Minimum

Keysight Infiniium : Friday, August 26, 2016 9:50:56 AM



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HF1-6: Clock Duty Cycle(Maximum)

Reference: Test ID HF1-6

Test Summary: **Pass** Test Description: Confirm that the duty cycle of the differential TMDS clock does not exceed the limits allowed by the specification. For compliance, the DUT should output the highest supported pixel clock frequency during the test.

Pass Limits: **<=60%** Clock Duty Cycle Maximum **50.310**

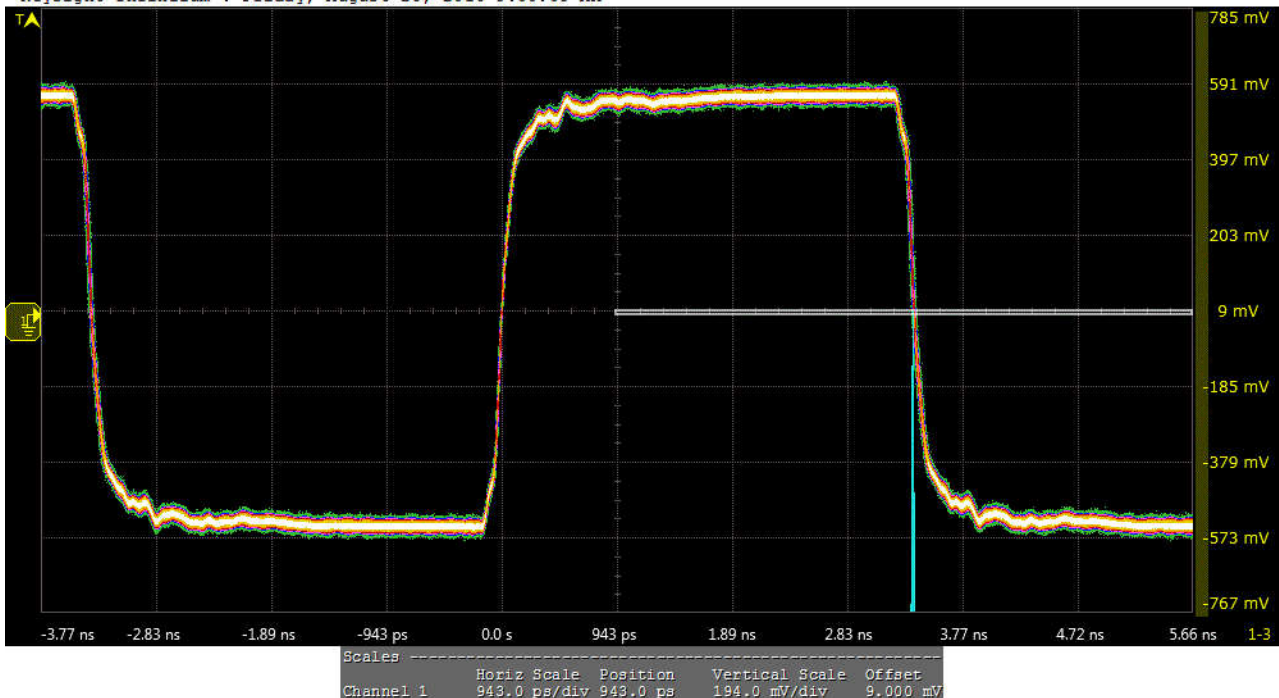
Result Details

HDMIAutomationConfig Timing 101 **Test Frequency(MHz)** 148.456 **# Edges** 10.000000 k **TdutyMAX(ns)** 3.389

Trial 1

Trial 1: Clock Duty Cycle Maximum

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✓ **HF1-6: Clock Rate** Reference: Test ID HF1-6

Test Summary: **Pass** Test Description: Confirm that the duty cycle of the differential TMDS clock does not exceed the limits allowed by the specification. For compliance, the DUT should output the highest supported pixel clock frequency during the test.

Pass Limits: **[85.000000000 MHz to 150.000000000 MHz]** Clock Rate **148.456300000 MHz**

Result Details

HDMIAutomationConfig Timing 101

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✓ **HF1-7: Differential Clock Voltage Swing, Vs (TP1)** Reference: Test ID HF1-7

Test Summary: **Pass** Test Description: This is a subset of the clock jitter test where the differential voltage swing at TP1 must be > 400mV and < 1200mV.

Pass Limits: **(400 mV to 1.200 V)** Clock Differential Voltage Swing, Vs **1.097 V**

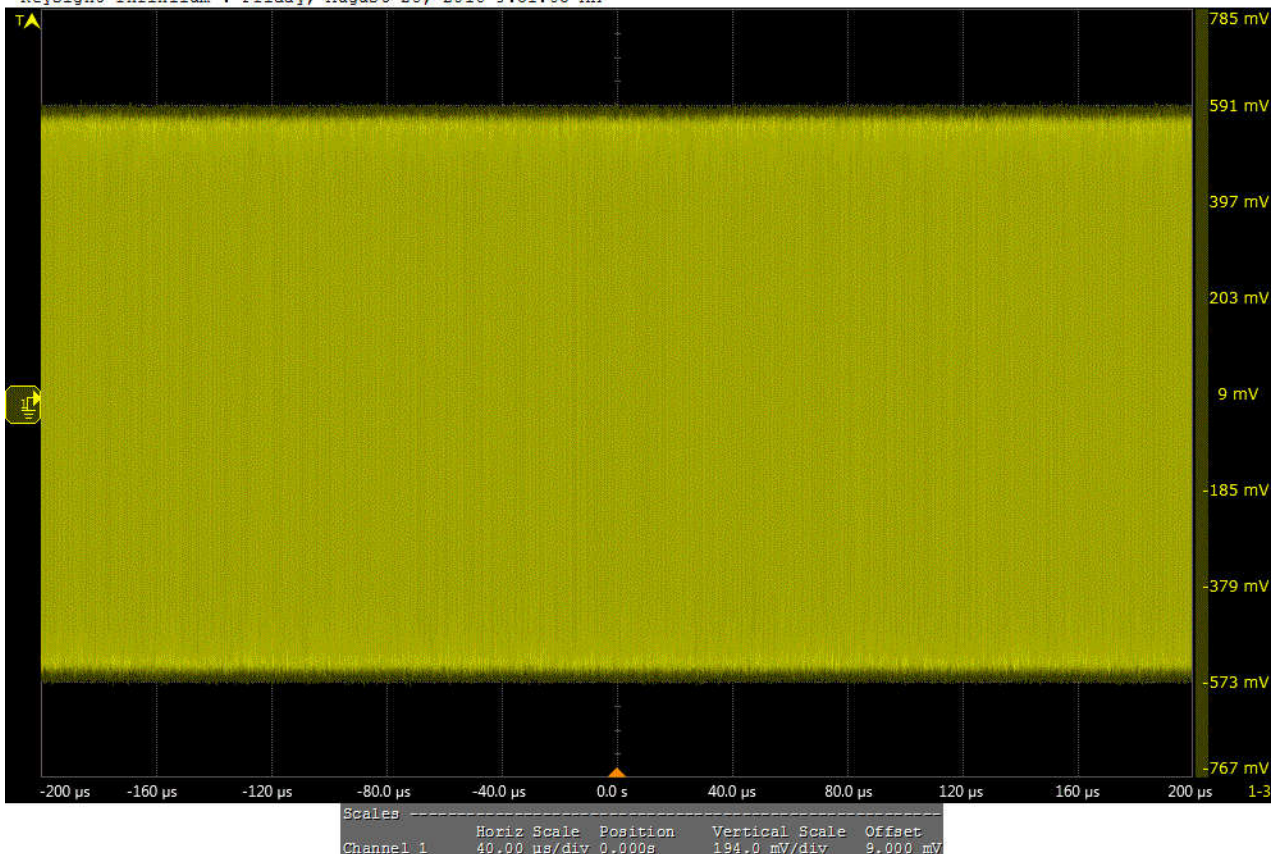
Result Details

HDMIAutomationConfig Timing 101

Trial 1

Trial 1: Clock Differential Voltage Swing, Vs

Keysight Infiniium : Friday, August 26, 2016 9:51:05 AM



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HF1-7: Clock Jitter (TP2_EQ with Worst Case Positive Skew)

Reference: Test ID HF1-7

Test Summary: Pass **Test Description:** 2 Channels Connection Model: TMDS differential clock jitter must not exceed 0.3*Tbit, relative to the ideal Recovery Clock. For compliance, the DUT should output > 340MHz for testing.

Pass Limits: <= 300 mTbit **Clock Jitter** 175 mTbit

Result Details

HDMIAutomationConfig	Timing 101	Test Frequency(MHz)	148.456	Tbit(ps)	168.400	Clock Jitter(ps)	29.460
# Samples	16.000000000 M	# Edges	59.213000 k	Acquisition Bandwidth (GHz)	13.000		

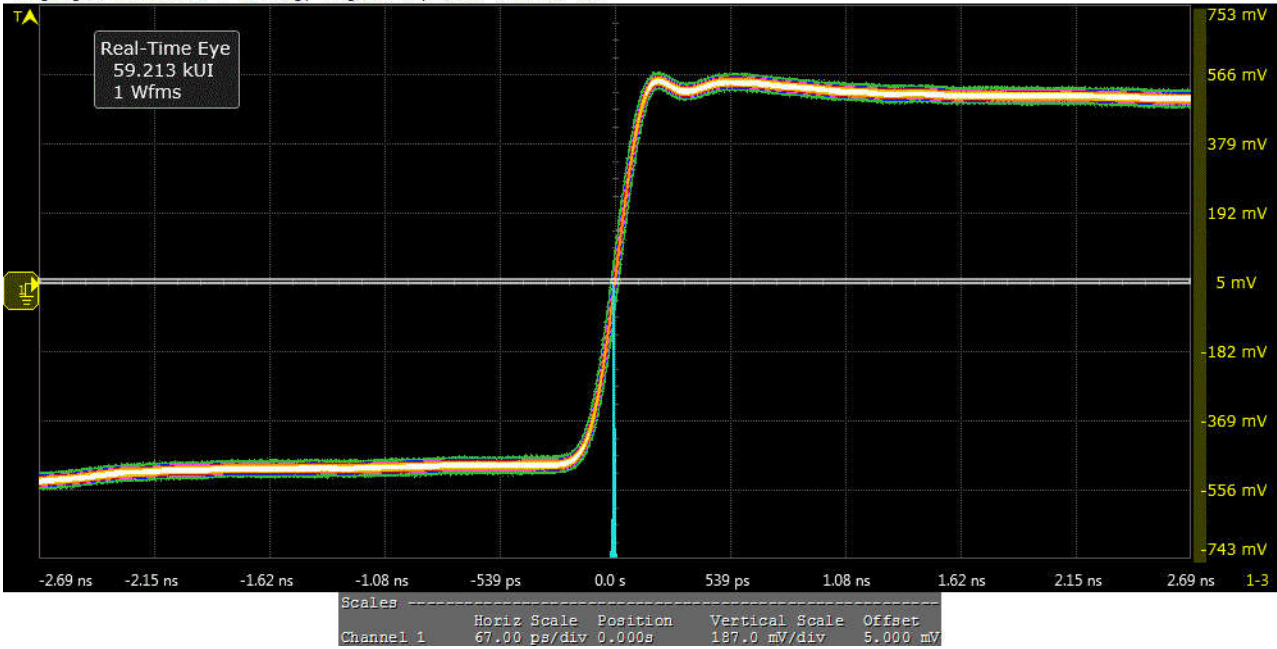
Transfer Function

File C:\ProgramData\Keysight\Infiniium\Apps\HDMIHEACTest\app\config\TransferFunction\Final\Final_WorstCableModel_HDMIEquFile7M_skew_p_112.tf4

Trial 1

Trial 1: Clock Jitter

Keysight Infiniium : Friday, August 26, 2016 9:53:01 AM



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✓ HF1-7: Clock Jitter (TP2_EQ with Worst Case Negative Skew) Reference: Test ID HF1-7

Test Summary: Pass Test Description: 2 Channels Connection Model: TMDS differential clock jitter must not exceed 0.3 Tbit, relative to the ideal Recovery Clock. For compliance, the DUT should output > 340MHz for testing.

Pass Limits: <= 300 mTbit Clock Jitter 225 mTbit

Result Details

HDMIAutomationConfig	Timing 101	Test Frequency(MHz)	148.456	Tbit(ps)	168.400	Clock Jitter(ps)	37.880
# Samples	16.000000000 M	# Edges	59.213000 k	Acquisition Bandwidth (GHz)	13.000		

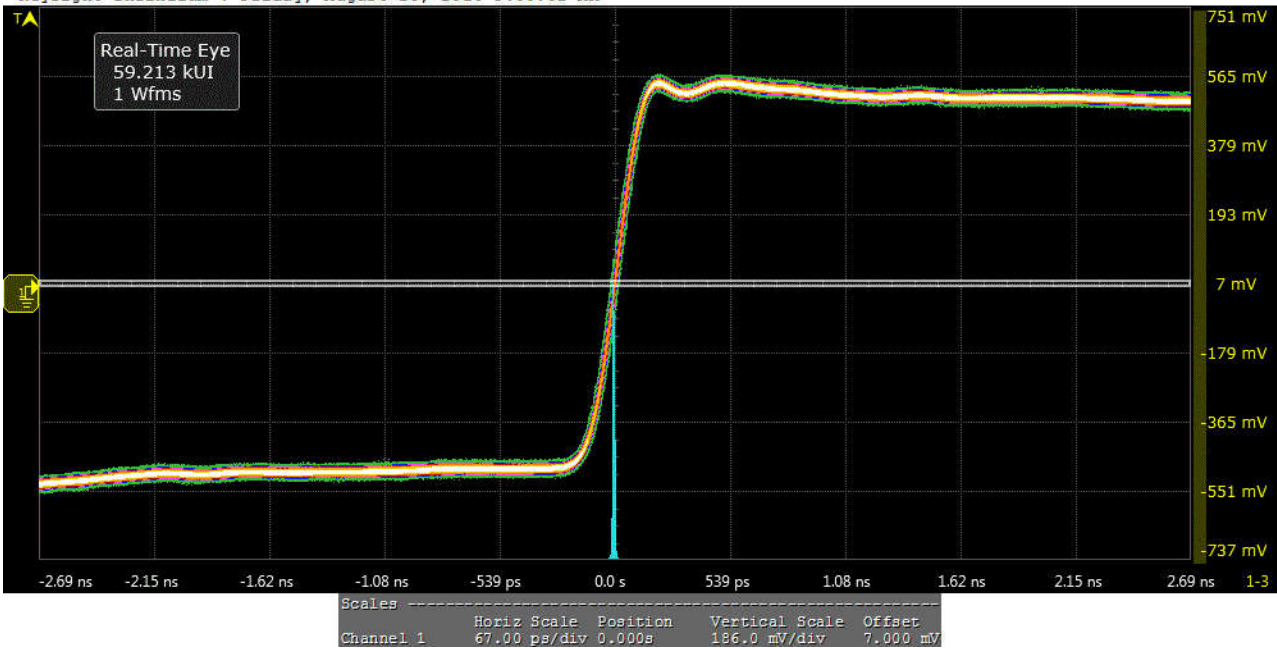
Transfer Function

File C:\ProgramData\Keysight\Infiniium\Apps\HDMIHEACTest\app\config\TransferFunction\Final\Final_WorstCableModel_HDMIEquFile7M_skew_n_112.tf4

Trial 1

Trial 1: Clock Jitter

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✓ HF1-5: D0 Maximum Differential Voltage Reference: Test ID HF1-5

Test Summary: Pass Test Description: Confirm that the differential signal on each TMDS differential data pair does not exceed Maximum/Minimum Differential Voltage.

Pass Limits: <= 780 m Maximum Differential Voltage 605 m

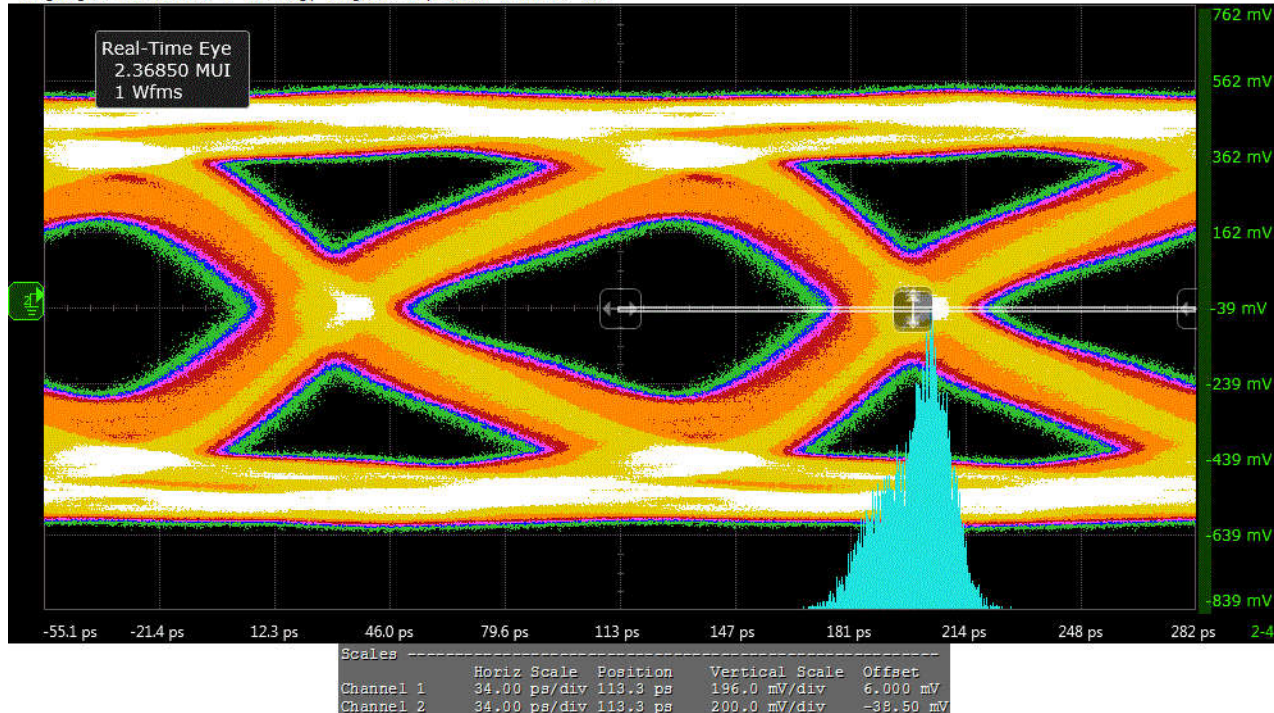
Result Details

HDMIAutomationConfig	Timing 101	Eye Width(ps)	102.620	Eye Height(mV)	319.000	Data Lane A	D0
Test Frequency(MHz)	148.456	Mask Moved(ps)	0.000	# Acquisitions Point	16.000000000 M	Tbit(ps)	168.351
RightJitterData(Tbit)	389 m	LeftJitterData(Tbit)	391 m	RightJitterData(ps)	65.520	LeftJitterData(ps)	65.780
Maximum Margin	NA	Maximum Margin (Vertical)	NA	Differential Swing Voltage, VH(V)	464 m		
Differential Swing Voltage, VL (V)	-531 m	Differential Swing Voltage(V)	995 m	Acquisition Bandwidth (GHz)	13.000		

Trial 1

Trial 1: Maximum Differential Voltage

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✓ HF1-5: D0 Minimum Differential Voltage Reference: Test ID HF1-5

Test Summary: Pass Test Description: Confirm that the differential signal on each TMDS differential data pair does not exceed Maximum/Minimum Differential Voltage.

Pass Limits: >= -780 m Minimum Differential Voltage -598 m

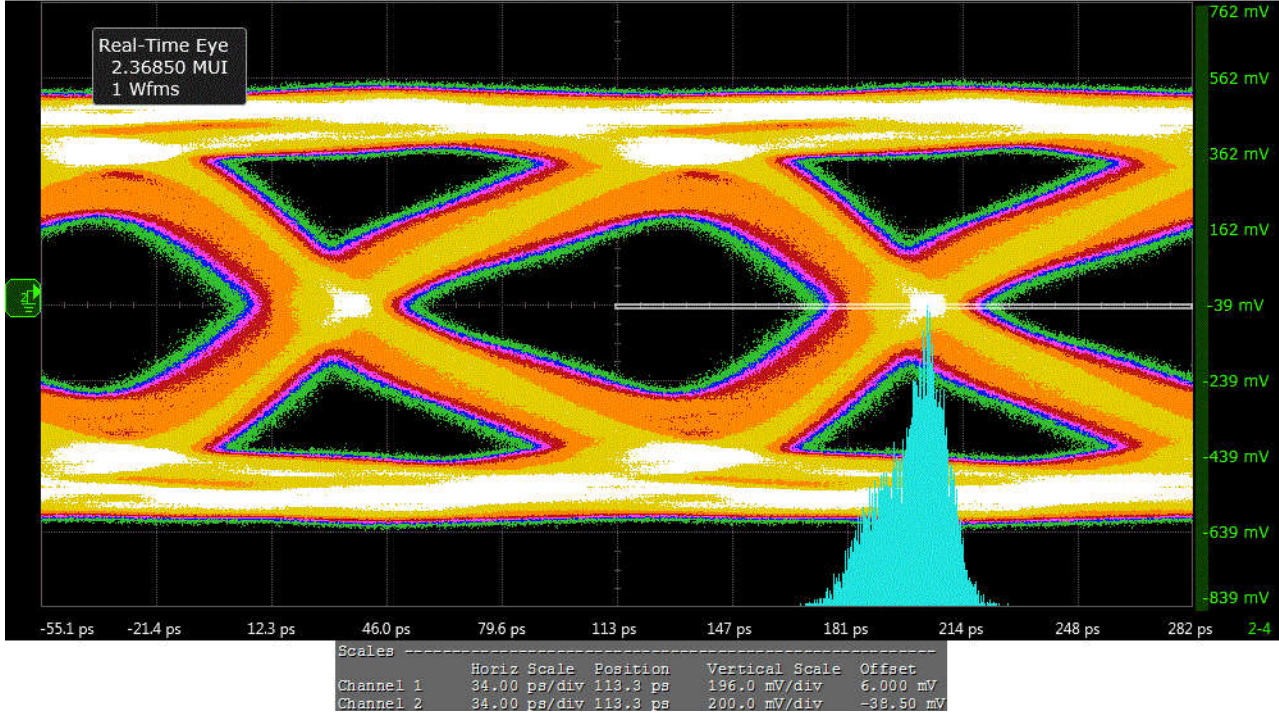
Result Details

HDMIAutomationConfig Timing 101

Trial 1

Trial 1: Minimum Differential Voltage

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✓ HF1-2: D0 Rise Time Reference: Test ID HF1-2

Test Summary: Pass **Test Description:** The transition time is defined as the time interval between the normalized 20% and 80% amplitude levels. For compliance, the DUT should output the highest supported pixel clock frequency during the test.

Pass Limits: >= 42.500 ps **Transition Time** 107.100 ps

Result Details

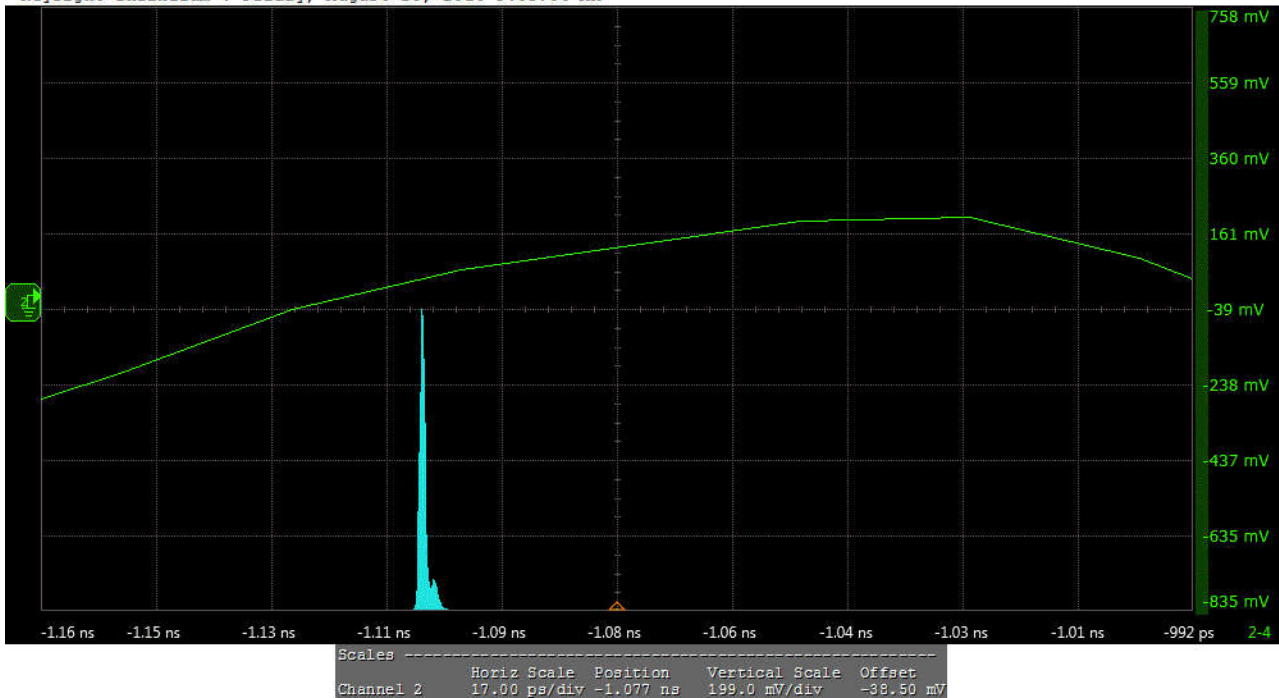
HDMI Automation Config	Timing 101	Test Frequency (MHz)	148.456	Data Lane A	D0	Upper Threshold (%)	80.000
Lower Threshold (%)	20.000	#Edge	281.716000 k	Acquisition Bandwidth (GHz)	13.000		

Transfer Function
File C:\ProgramData\Keysight\Infiniium\Apps\HDMIHEACTestapp\config\TransferFunction\Final\Final_WorstCableModel_HDMIEquFile7M_skew_n_112.tf4

Trial 1

Trial 1: Transition Time

Keysight Infiniium : Friday, August 26, 2016 9:58:04 AM



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HF1-2: D0 Fall Time

Reference: Test ID HF1-2

Test Summary: Pass **Test Description:** The transition time is defined as the time interval between the normalized 20% and 80% amplitude levels. For compliance, the DUT should output the highest supported pixel clock frequency during the test.

Pass Limits: ≥ 42.500 ps **Transition Time** 107.220 ps

Result Details

HDMIAutomationConfig Timing 101 **Test Frequency(MHz)** 148.456 **Data Lane A** D0 **Upper Threshold(%)** 80.000

Lower Threshold(%) 20.000 **#Edge** 281.717000 k **Acquisition Bandwidth (GHz)** 13.000

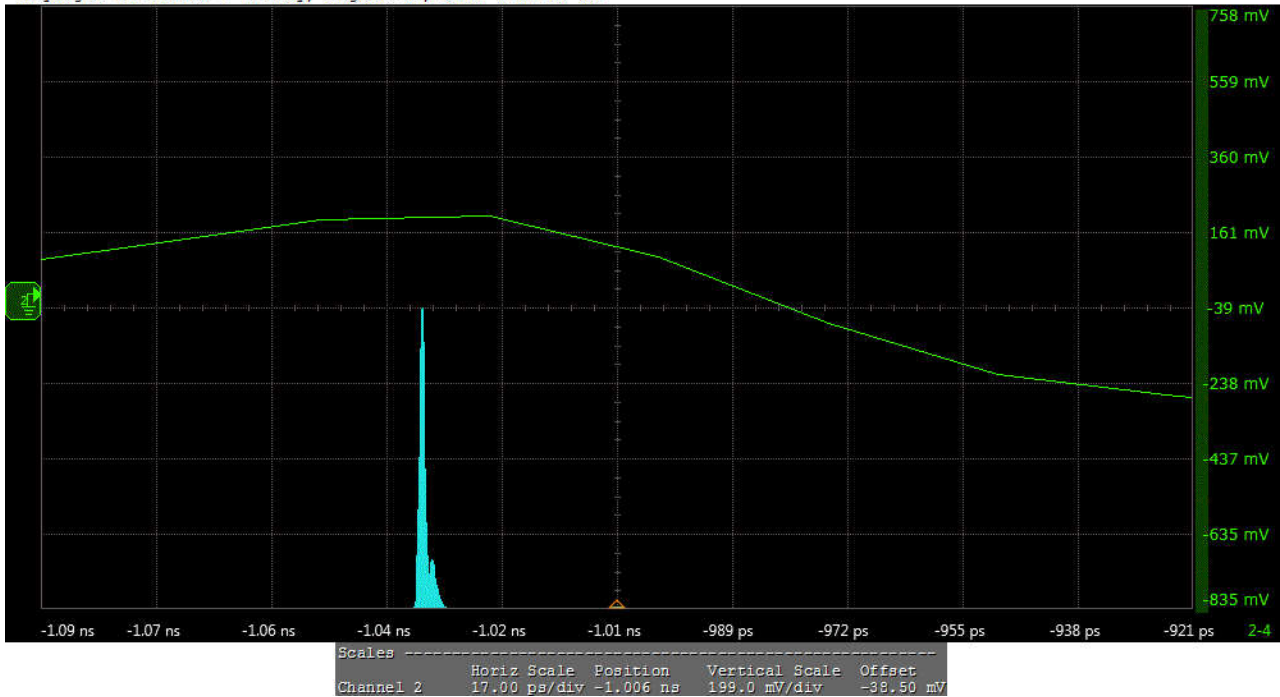
Transfer Function

File C:\ProgramData\Keysight\Infiniium\Apps\HDMIHEACTest\app\config\TransferFunction\Final\Final_WorstCableModel_HDMIEquFile7M_skew_n_112.tf4

Trial 1

Trial 1: Transition Time

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HF1-8: D0 Mask Test (TP2_EQ with Worst Case Positive Skew)

Reference: Test ID HF1-8

Test Summary: Pass **Test Description:** Confirm that the differential signal on each TMDS differential data pair has an "eye opening" (region of valid data) that meets or exceeds the limits on eye opening in the specification.

Pass Limits: No Mask Failures **Total # failures** 0.000

Result Details

HDMIAutomationConfig Timing 101 **Eye Width(ps)** 100.510 **Eye Height(mV)** 208.000 **Data Lane A** D0

Test Frequency(MHz) 148.456 **Mask Moved(ps)** 0.000 **# Acquisitions Point** 16.000000000 M **Tbit(ps)** 168.353

RightJitterData(Tbit) 403 m **LeftJitterData(Tbit)** 402 m **RightJitterData(ps)** 67.890 **LeftJitterData(ps)** 67.620

Maximum Margin NA **Maximum Margin (Vertical)** NA **Differential Swing Voltage, VH(V)** 477 m

Differential Swing Voltage, VL (V) -538 m **Differential Swing Voltage(V)** 1.015 **Acquisition Bandwidth (GHz)** 13.000

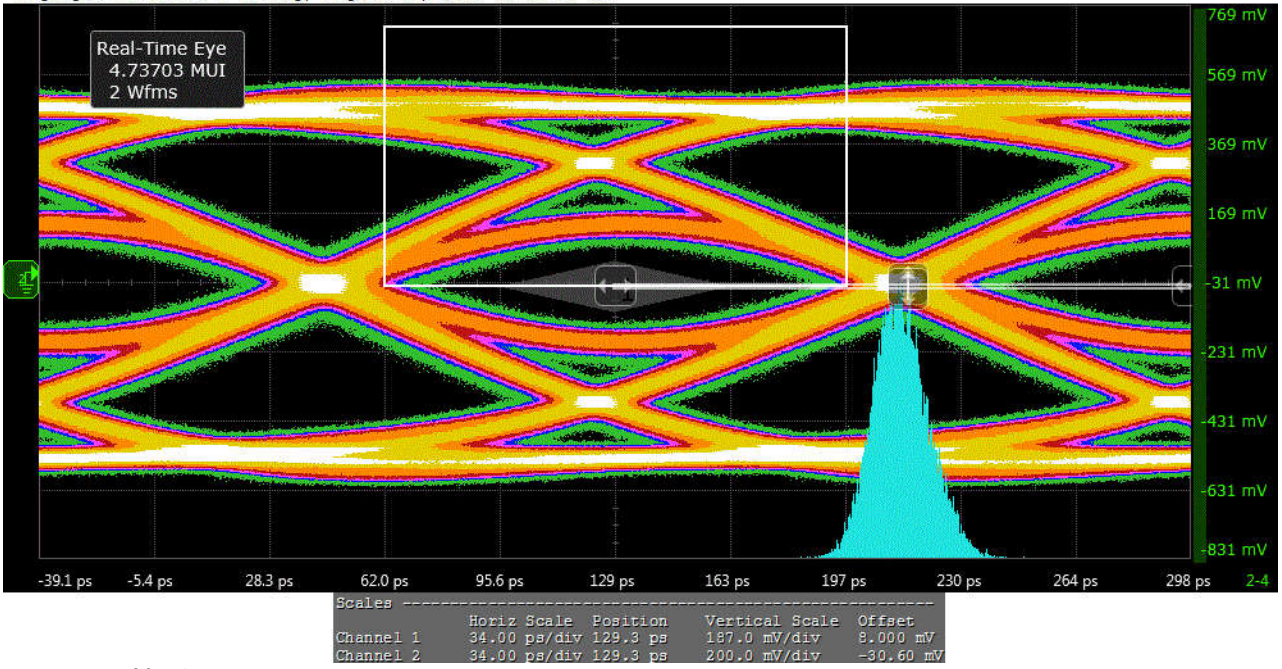
Transfer Function

File C:\ProgramData\Keysight\Infiniium\Apps\HDMIHEACTest\app\config\TransferFunction\Final\Final_WorstCableModel_HDMIEquFile7M_skew_p_112.tf4

Trial 1

Trial 1: Total # failures

Keysight Infiniium : Friday, August 26, 2016 10:05:15 AM



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✓ HF1-8: D0 Mask Test (TP2_EQ with Worst Case Negative Skew) Reference: Test ID HF1-8

Test Summary Pass **Test Description:** Confirm that the differential signal on each TMDS differential data pair has an "eye opening" (region of valid data) that meets or exceeds the limits on eye opening in the specification.

Pass Limits: No Mask Failures **Total # failures** 0.000

Result Details

HDMIAutomationConfig	Timing 101	Eye Width(ps)	101.830	Eye Height(mV)	213.000	Data Lane A	D0
Test Frequency(MHz)	148.456	Mask Moved(ps)	0.000	# Acquisitions Point	16.000000000 M	Tbit(ps)	168.352
RightJitterData(Tbit)	395 m	LeftJitterData(Tbit)	394 m	RightJitterData(ps)	66.570	LeftJitterData(ps)	66.310
Maximum Margin	NA	Maximum Margin (Vertical)	NA	Differential Swing Voltage, VH(V)	479 m		
Differential Swing Voltage, VL (V)	-544 m	Differential Swing Voltage(V)	1.023	Acquisition Bandwidth (GHz)	13.000		

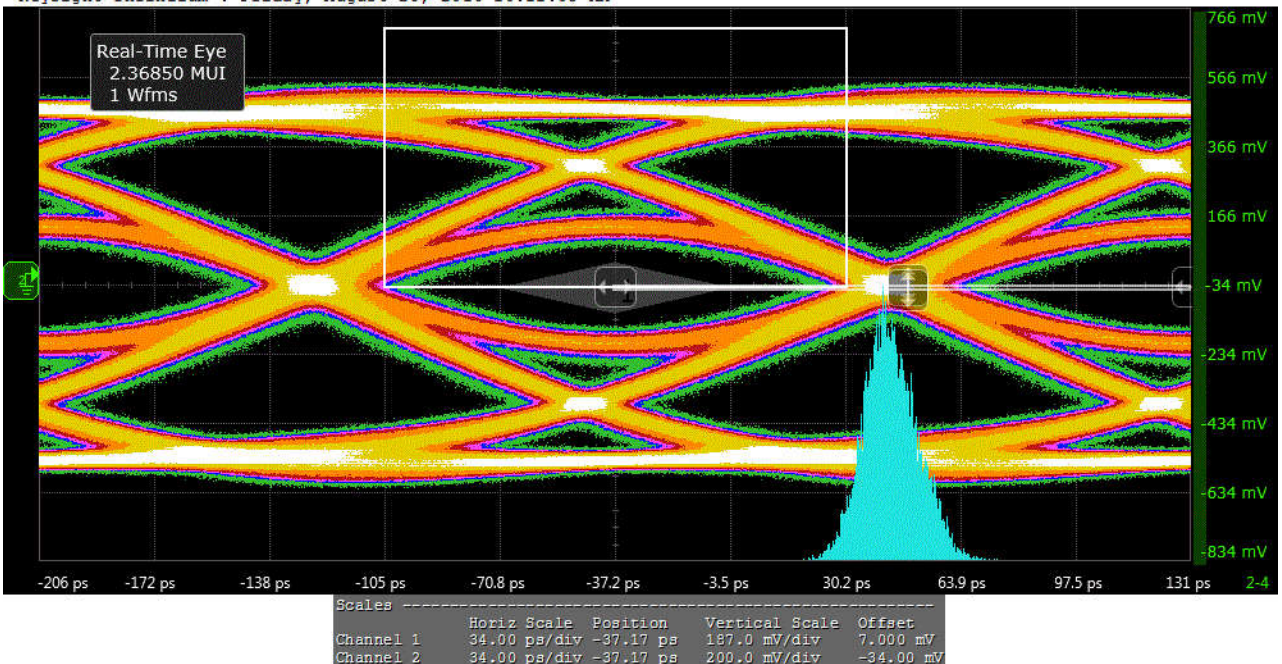
Transfer Function

File C:\ProgramData\Keysight\Infiniium\Apps\HDMIHEACTest\app\config\TransferFunction\Final\Final_WorstCableModel_HDMIEquFile7M_skew_n_112.tf4

Trial 1

Trial 1: Total # failures

Keysight Infiniium : Friday, August 26, 2016 10:11:53 AM



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✓HF1-1: VL Clock + Reference: Test ID HF1-1

Test Summary: Pass Test Description: Confirm that the DC voltage levels on the HDMI link are within the specified limits for each TMDS signal.

Pass Limits: [2.300 V to 3.100 V] VL 2.731 V

Result Details

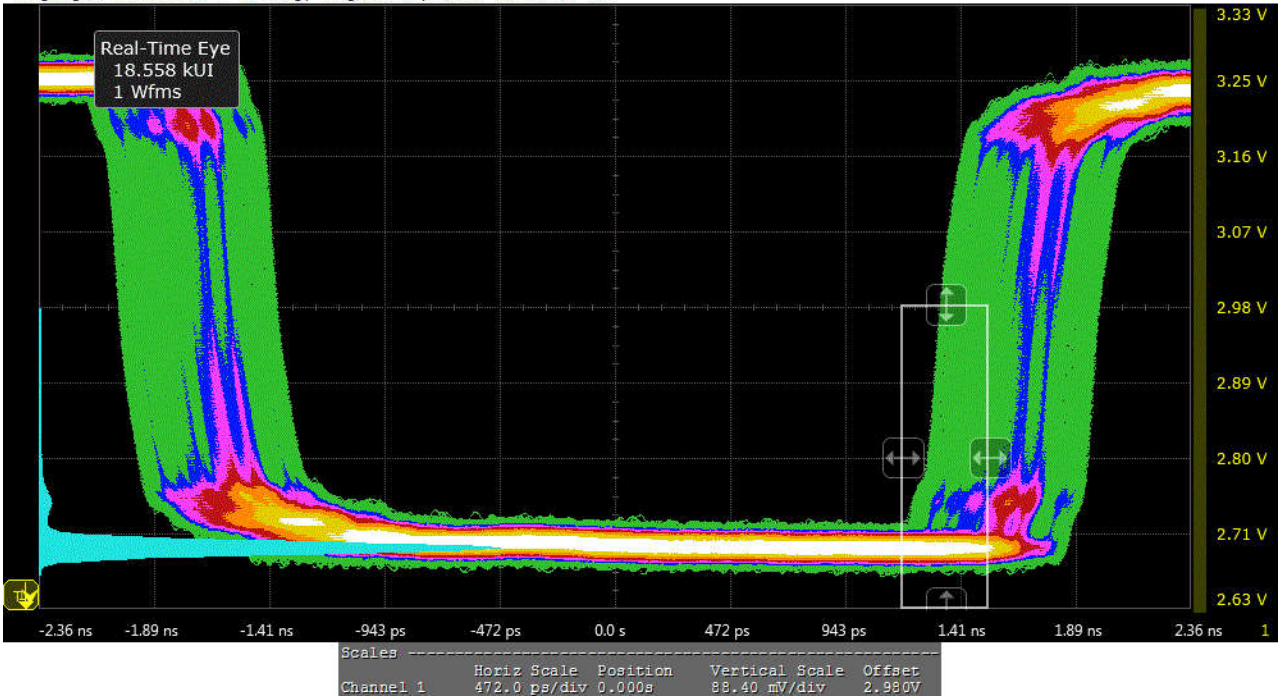
HDMIAutomationConfig Timing 101 Test Frequency 148.456 MHz # Edges 18.559000 k VH 3.283 V VL (See image)

VH (See image)

Trial 1

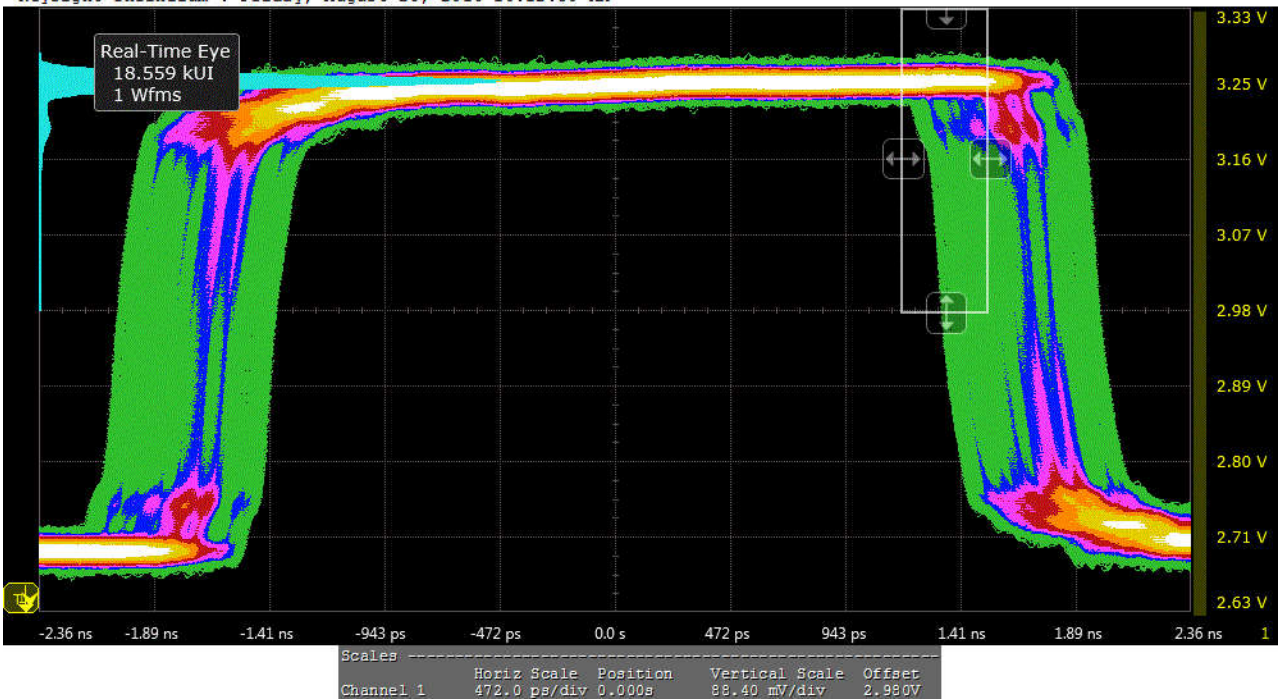
Trial 1: VL

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Trial 1: VH

Keysight Infiniium : Friday, August 26, 2016 10:12:30 AM



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✓HF1-1:Clock + VSwing Reference: Test ID HF1-1

Test Summary: Pass Test Description: Confirm that the DC voltage levels on the HDMI link are within the specified limits for each TMDS signal.

Pass Limits: [200 mV to 600 mV] VSwing 552 mV

Result Details

HDMIAutomationConfig Timing 101

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✓ **HF1-1: VL Clock** Reference: Test ID HF1-1

Test Summary: **Pass** Test Description: Confirm that the DC voltage levels on the HDMI link are within the specified limits for each TMDS signal.

Pass Limits: [2.300 V to 3.100 V] VL 2.723 V

Result Details

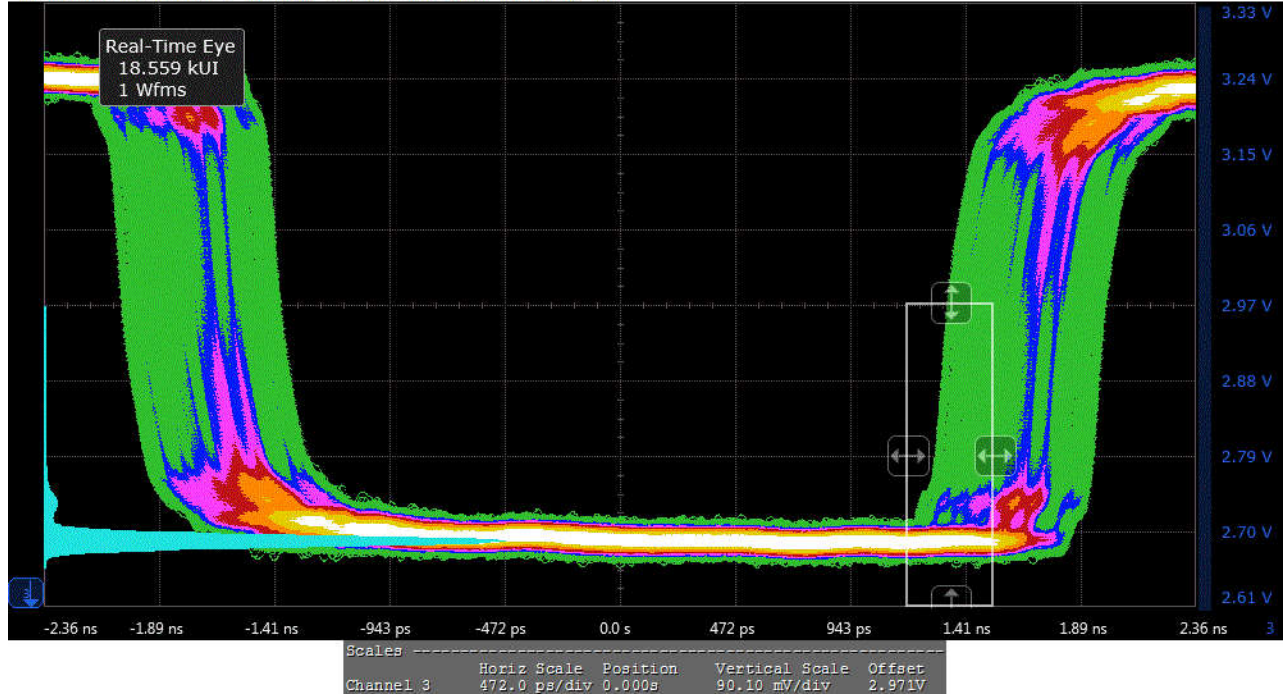
HDMIAutomationConfig Timing 101 **Test Frequency** 148.456 MHz **# Edges** 18.558000 k **VH** 3.275 V **VL** (See image)

VH (See image)

Trial 1

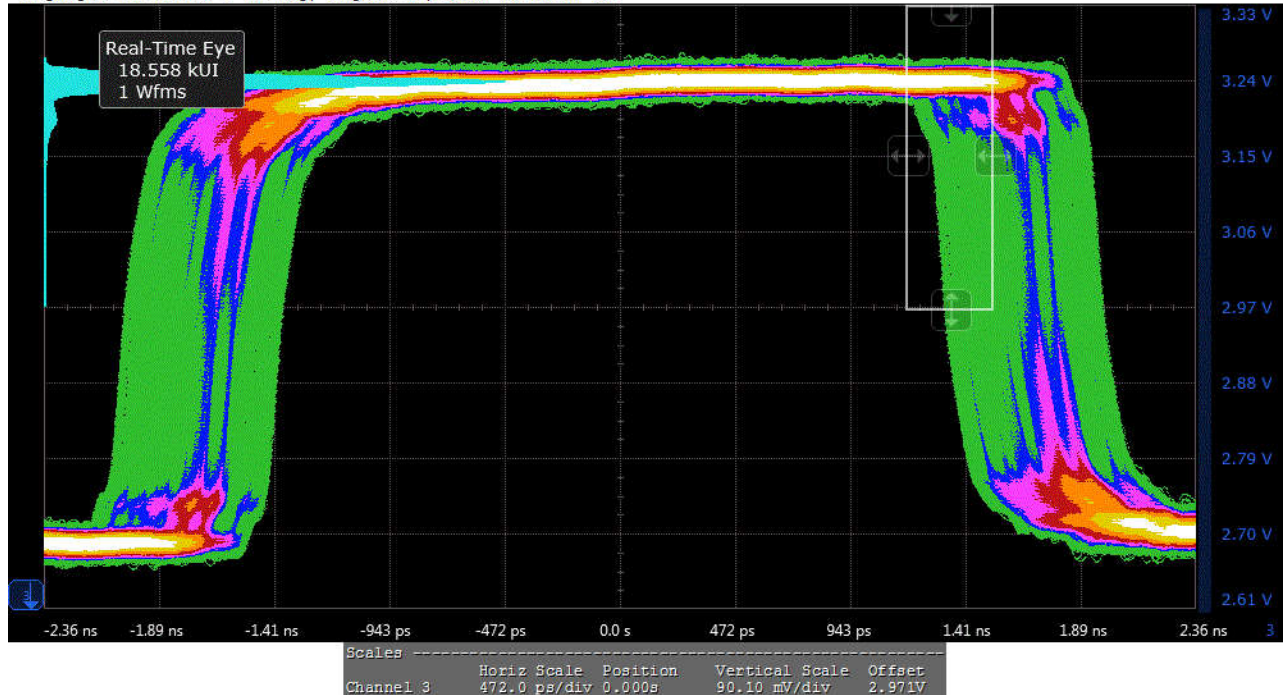
Trial 1: VL

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Trial 1: VH

Keysight Infiniium : Friday, August 26, 2016 10:12:51 AM



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✓ HF1-1: Clock - VSwing Reference: Test ID HF1-1

Test Summary: **Pass** Test Description: Confirm that the DC voltage levels on the HDMI link are within the specified limits for each TMDS signal.

Pass Limits: [200 mV to 600 mV] VSwing 552 mV

Result Details

HDMIAutomationConfig Timing 101

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✓ HF1-4: Intra-Pair Skew - Clock Reference: Test ID HF1-4

Test Summary: **Pass** Test Description: Confirm that any skew within any one differential data pair in the TMDS portion of the HDMI link does not exceed the limits in the specification.

Pass Limits: [-150 mTbit to 150 mTbit] Clock Intra-Pair Skew 28 mTbit

Result Details

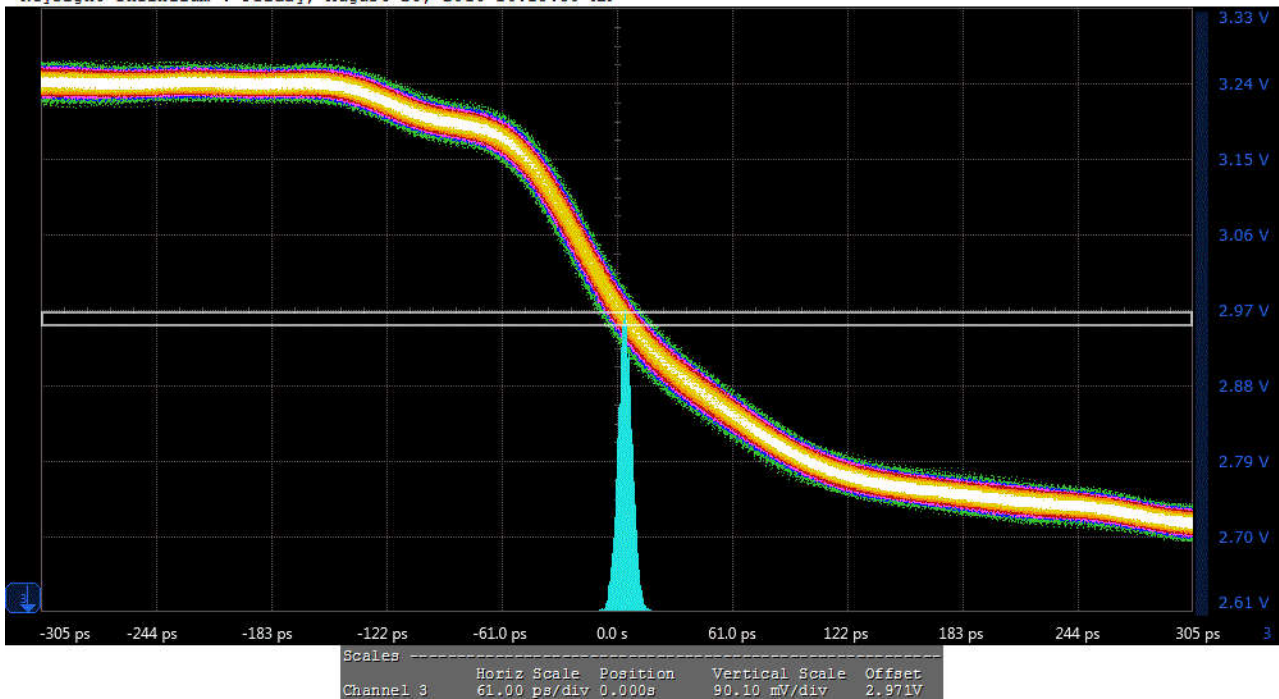
HDMIAutomationConfig Timing 101 Max skew (ps) 18.110 Min skew (ps) -9.050 Clk+ threshold (V) 2.977

Clk- threshold (V) 2.961 Acquisition Bandwidth (GHz) 13.000 Test Frequency(MHz) 148.456 Clock Intra-Pair Skew(ps) 4.770

Trial 1

Trial 1: Clock Intra-Pair Skew

Keysight Infiniium : Friday, August 26, 2016 10:13:30 AM



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✓ HF1-1: VL D0+ Reference: Test ID HF1-1

Test Summary: **Pass** Test Description: Confirm that the DC voltage levels on the HDMI link are within the specified limits for each TMDS signal.

Pass Limits: [2.300 V to 2.900 V] VL 2.777 V

Result Details

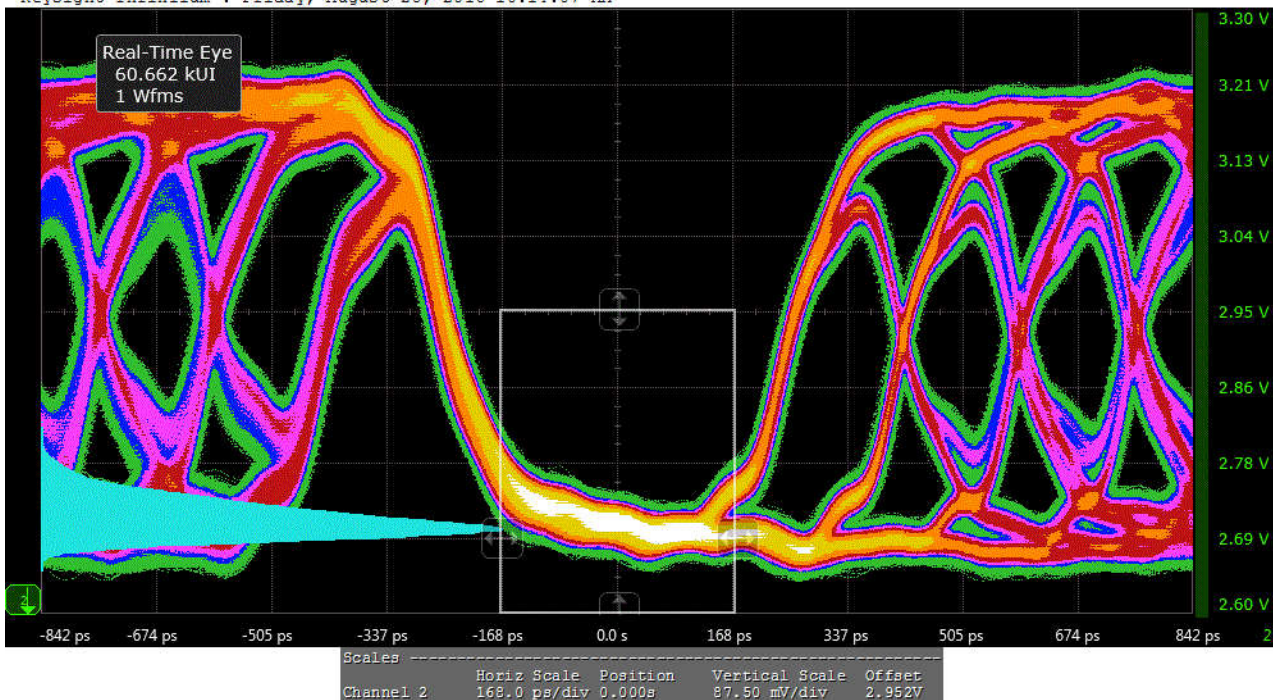
HDMIAutomationConfig Timing 101 Test Frequency 148.456 MHz # Edges,VL 60.662000 k # Edges,VH 60.421000 k VH 3.265 V

VL (See image) VH (See image)

Trial 1

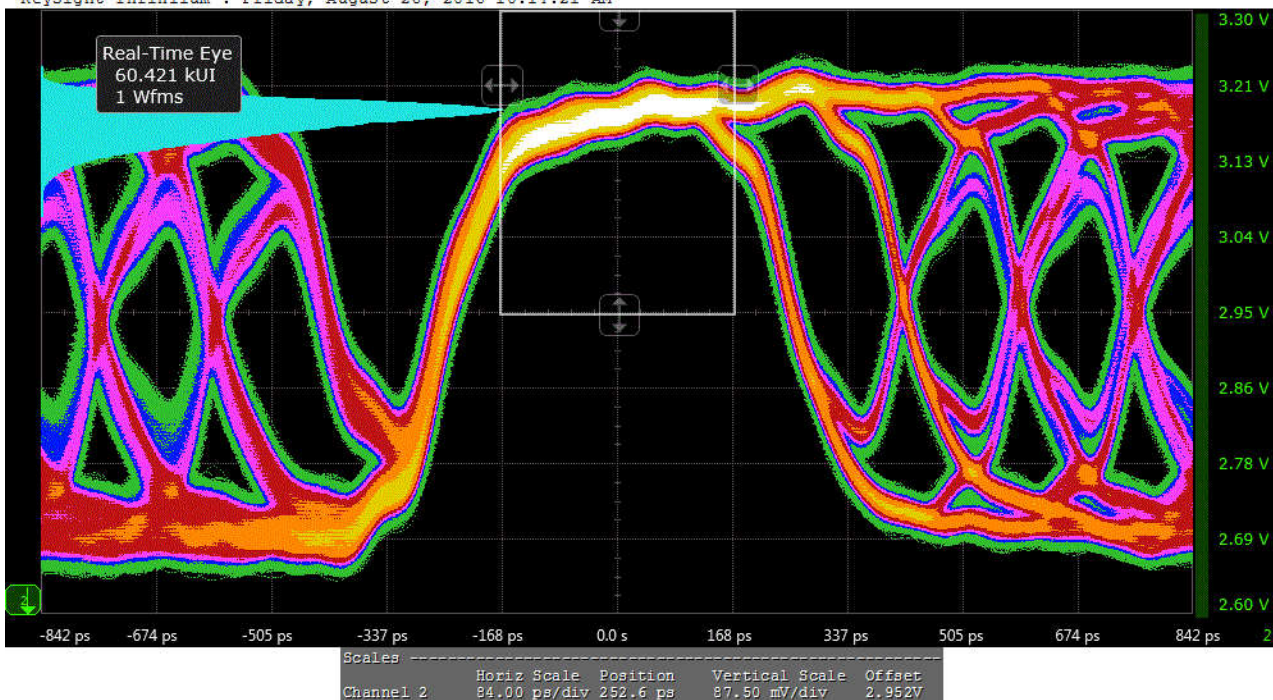
Trial 1: VL

Keysight Infiniium : Friday, August 26, 2016 10:14:07 AM



Trial 1: VH

Keysight Infiniium : Friday, August 26, 2016 10:14:21 AM



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✓ HF1-1: D0+ VSwing Reference: Test ID HF1-1

Test Summary: PASS Test Description: Confirm that the DC voltage levels on the HDMI link are within the specified limits for each TMDS signal.

Pass Limits: [400 mV to 600 mV] VSwing 488 mV

Result Details

HDMIAutomationConfig Timing 101

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✓ HF1-1: VL
D0- Reference: Test ID HF1-1

Test Summary: PASS Test Description: Confirm that the DC voltage levels on the HDMI link are within the specified limits for each TMDS signal.

Pass Limits: [2.300 V to 2.900 V] VL 2.764 V

Result Details

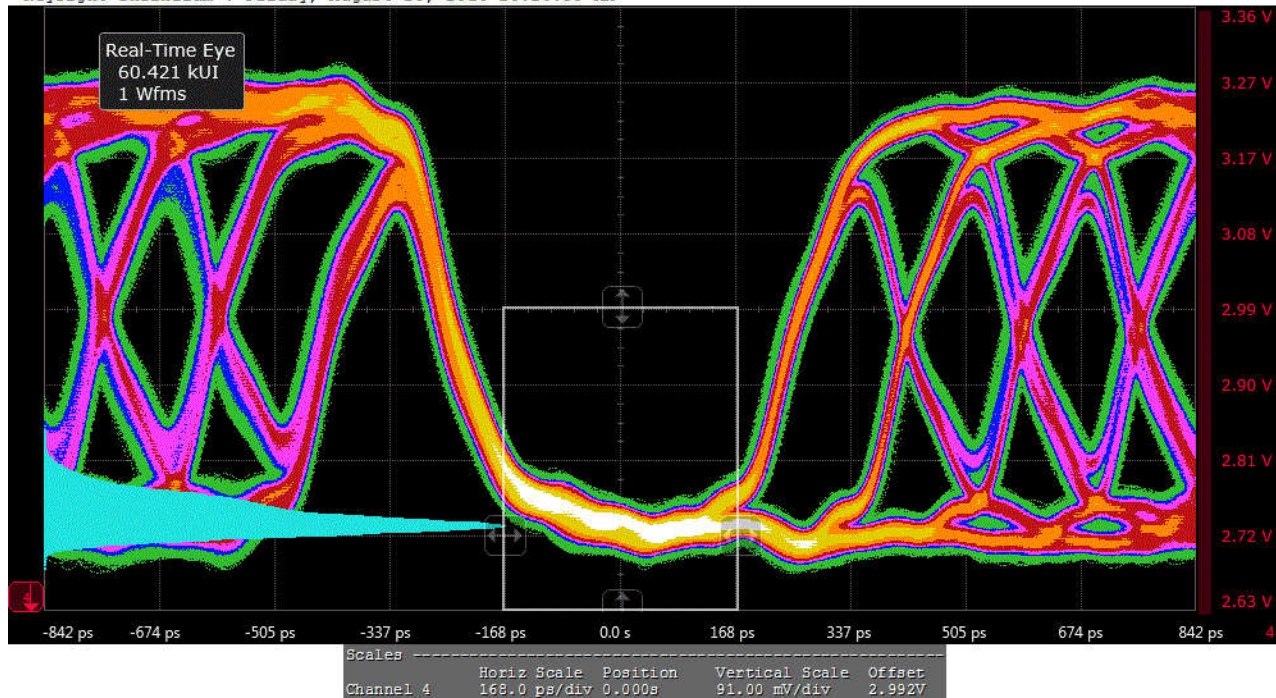
HDMIAutomationConfig Timing 101 **Test Frequency** 148.456 MHz **# Edges,VL** 60.421000 k **# Edges,VH** 60.662000 k **VH** 3.265 V

VL (See image) **VH** (See image)

Trial 1

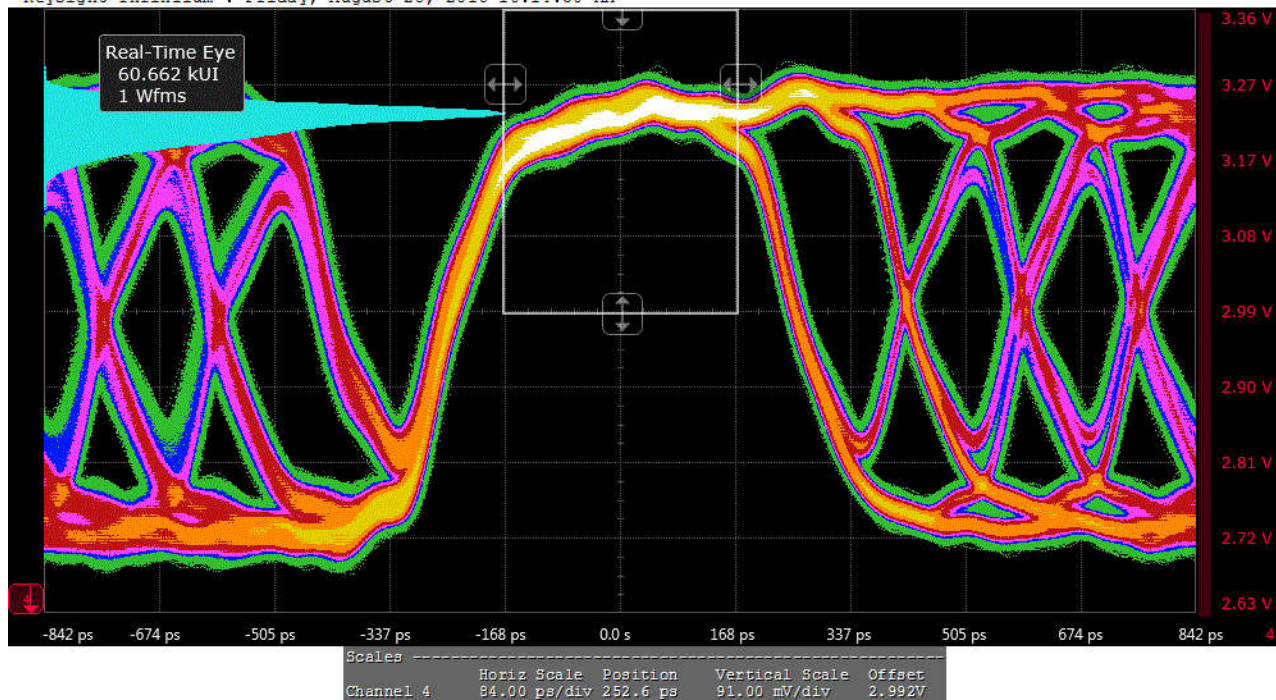
Trial 1: VL

Keysight Infiniium : Friday, August 26, 2016 10:14:35 AM



Trial 1: VH

Keysight Infiniium : Friday, August 26, 2016 10:14:50 AM



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✓ **HF1-1: D0- VSwing** Reference: Test ID HF1-1

Test Summary: **Pass** Test Description: Confirm that the DC voltage levels on the HDMI link are within the specified limits for each TMDS signal.

Pass Limits: [400 mV to 600 mV] **VSwing** 501 mV

Result Details

HDMIAutomationConfig Timing 101

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HF1-4: Intra-Pair Skew - Data Lane 0

Reference: Test ID HF1-4

Test Summary: **Pass** Test Description: Confirm that any skew within any one differential data pair in the TMDS portion of the HDMI link does not exceed the limits in the specification.

Pass Limits: [-150 mTbit to 150 mTbit] Data Intra-Pair Skew 50 mTbit

Result Details

HDMIAutomationConfig Timing 101 **D+ Average Measurement Screenshot** (See image)

D- Average Measurement Screenshot (See image) Max skew (ps) 20.160 Min skew (ps) -230 m D+ threshold (V) 2.945

D- threshold (V) 2.983 Acquisition Bandwidth (GHz) 13.000 Test Frequency(MHz) 148.456 Data Intra-Pair Skew(ps) 8.440

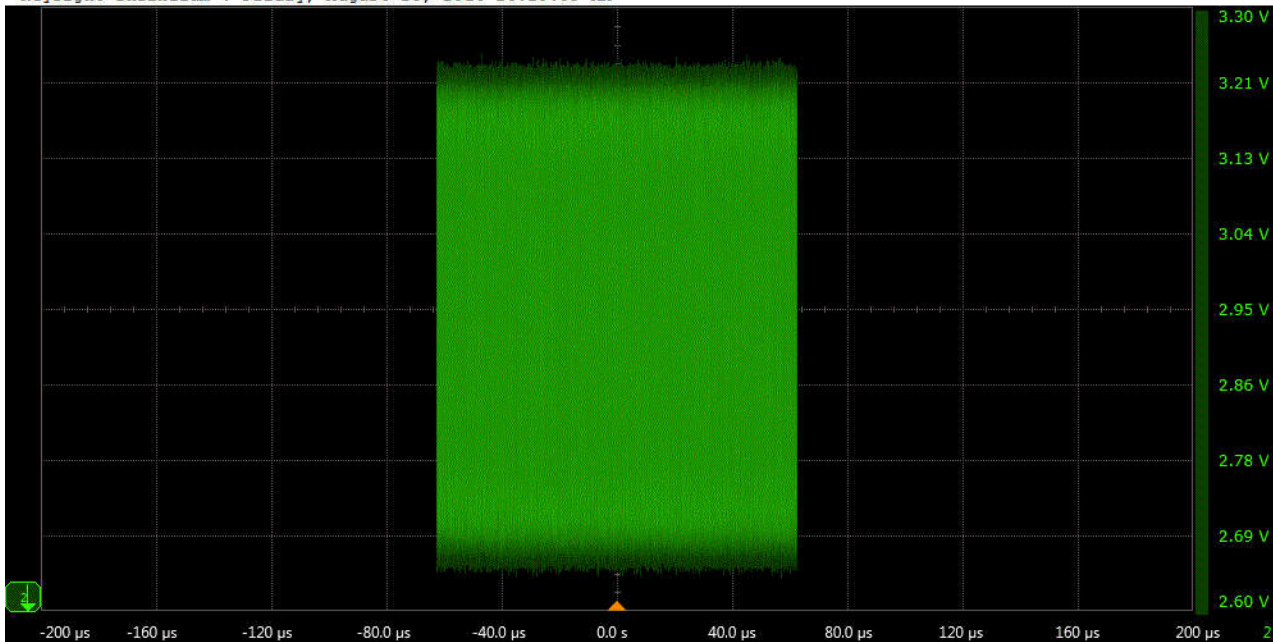
Transfer Function

File C:\ProgramData\Keysight\Infiniium\Apps\HDMIHEACTest\app\config\TransferFunction\Final\Final_WorstCableModel_HDMIEquFile7M_skew_n_112.tf4

Trial 1

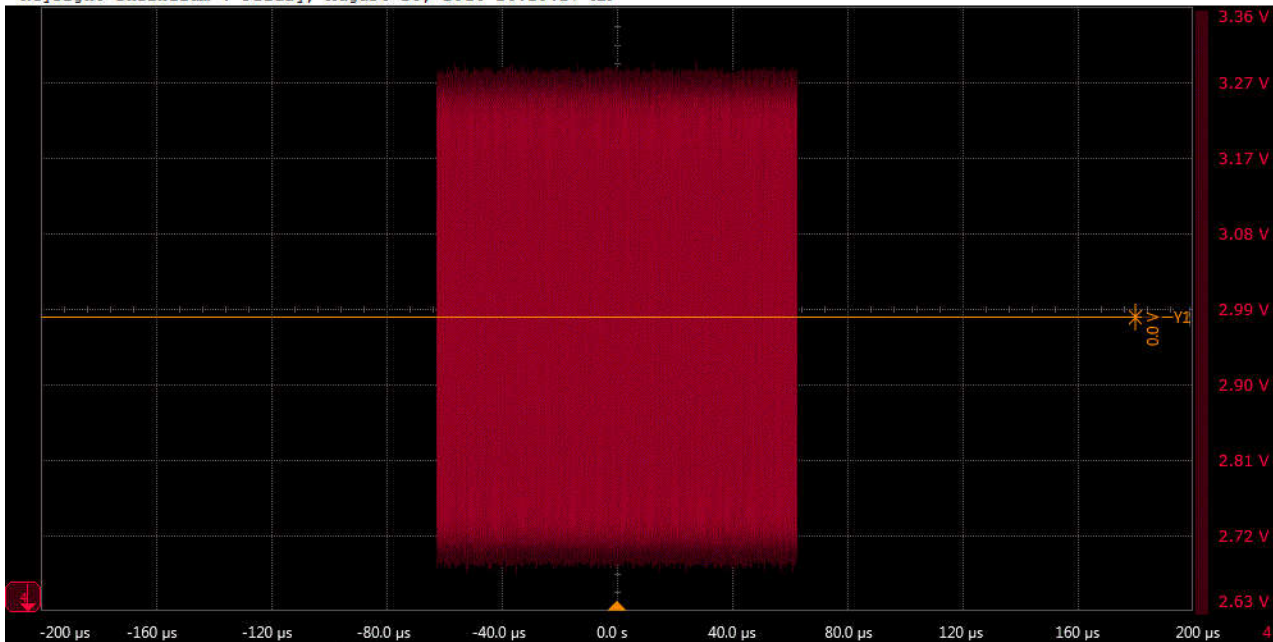
Trial 1: D+ Average Measurement Screenshot

Keysight Infiniium : Friday, August 26, 2016 10:15:09 AM

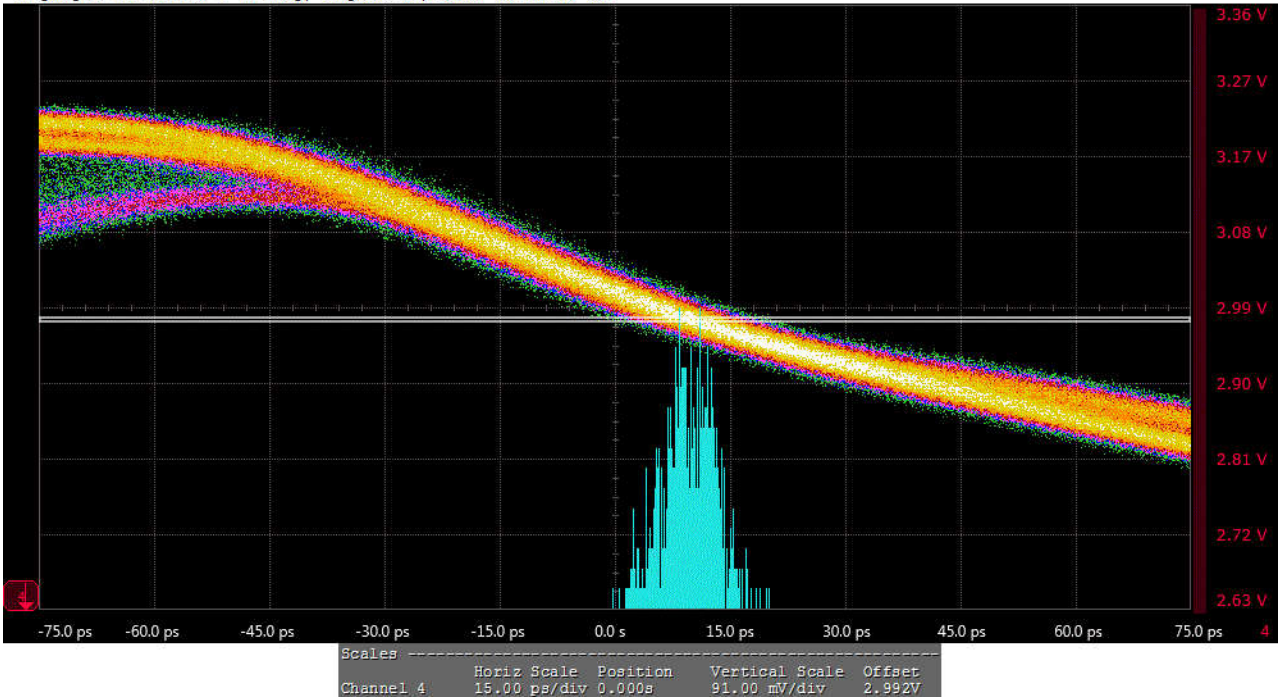


Trial 1: D- Average Measurement Screenshot

Keysight Infiniium : Friday, August 26, 2016 10:15:27 AM



Trial 1: Data Intra-Pair Skew
 Keysight Infiniium : Friday, August 26, 2016 10:15:52 AM



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✓ HF1-5: D1 Maximum Differential Voltage Reference: Test ID HF1-5

Test Summary: PASS Test Description: Confirm that the differential signal on each TMDS differential data pair does not exceed Maximum/Minimum Differential Voltage.

Pass Limits: <= 780 m Maximum Differential Voltage 610 m

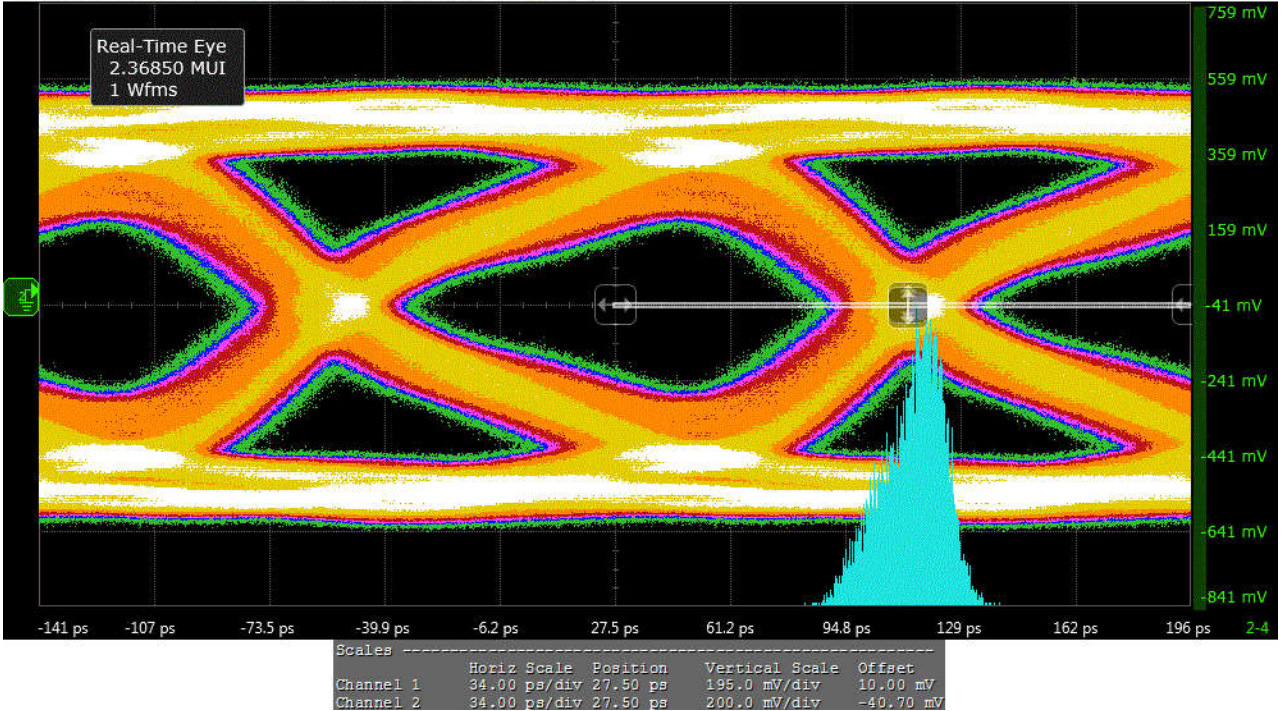
Result Details

HDMIAutomationConfig	Timing 101	Eye Width(ps)	103.400	Eye Height(mV)	297.000	Data Lane A	D1
Test Frequency(MHz)	148.471	Mask Moved(ps)	0.000	# Acquisitions Point	16.000000000 M	Tbit(ps)	168.353
RightJitterData(Tbit)	384 m	LeftJitterData(Tbit)	386 m	RightJitterData(ps)	64.720	LeftJitterData(ps)	64.990
Maximum Margin	NA	Maximum Margin (Vertical)	NA	Differential Swing Voltage, VH(V)	459 m		
Differential Swing Voltage, VL (V)	-539 m	Differential Swing Voltage(V)	998 m	Acquisition Bandwidth (GHz)	13.000		

Trial 1

Trial 1: Maximum Differential Voltage

Keysight Infiniium : Friday, August 26, 2016 11:56:40 AM



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✓ HF1-5: D1 Minimum Differential Voltage

Reference: Test ID HF1-5

Test Summary: **Pass** Test Description: Confirm that the differential signal on each TMDS differential data pair does not exceed Maximum/Minimum Differential Voltage.

Pass Limits: **>= -780 m** Minimum Differential Voltage **-601 m**

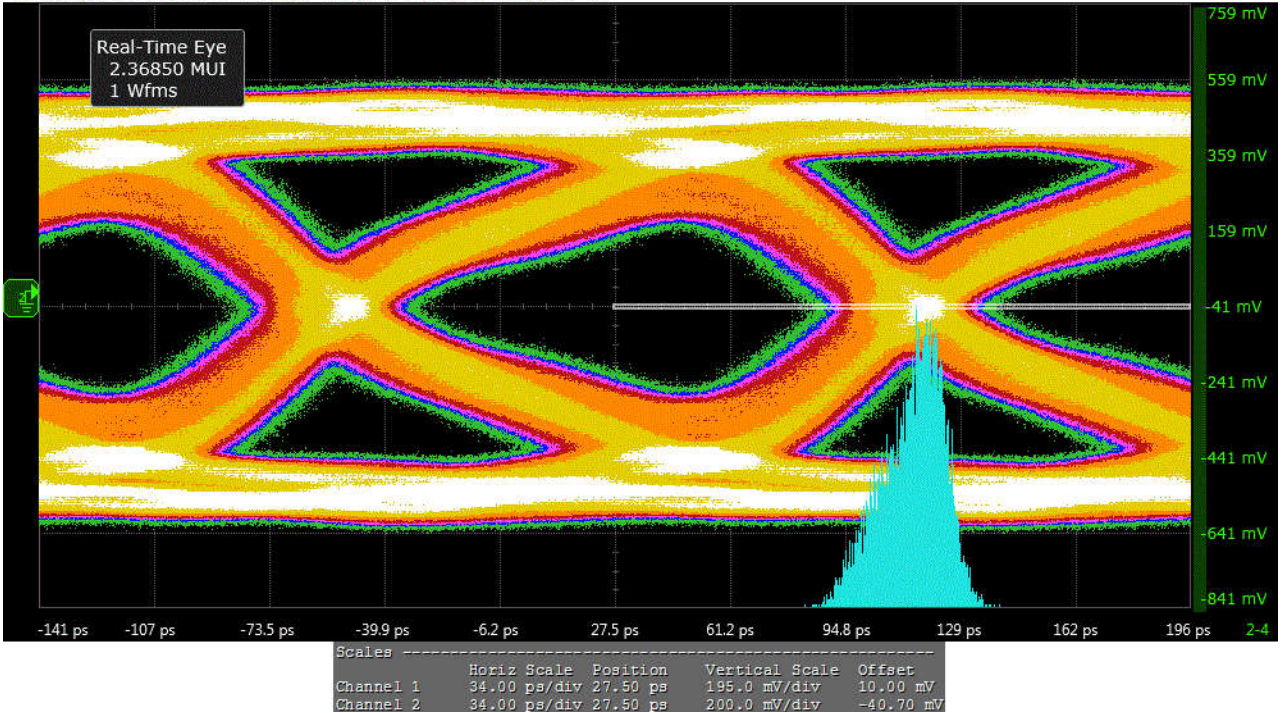
Result Details

HDMIAutomationConfig Timing 101

Trial 1

Trial 1: Minimum Differential Voltage

Keysight Infiniium : Friday, August 26, 2016 11:56:43 AM



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✓ HF1-2: D1 Rise Time

Reference: Test ID HF1-2

Test Summary: **Pass** Test Description: The transition time is defined as the time interval between the normalized 20% and 80% amplitude levels. For compliance, the DUT should output the highest supported pixel clock frequency during the test.

Pass Limits: **>= 42.500 ps** Transition Time **109.200 ps**

Result Details

HDMIAutomationConfig Timing 101 Test Frequency(MHz) 148.471 Data Lane A D1 Upper Threshold(%) 80.000

Lower Threshold(%) 20.000 #Edge 281.678000 k Acquisition Bandwidth (GHz) 13.000

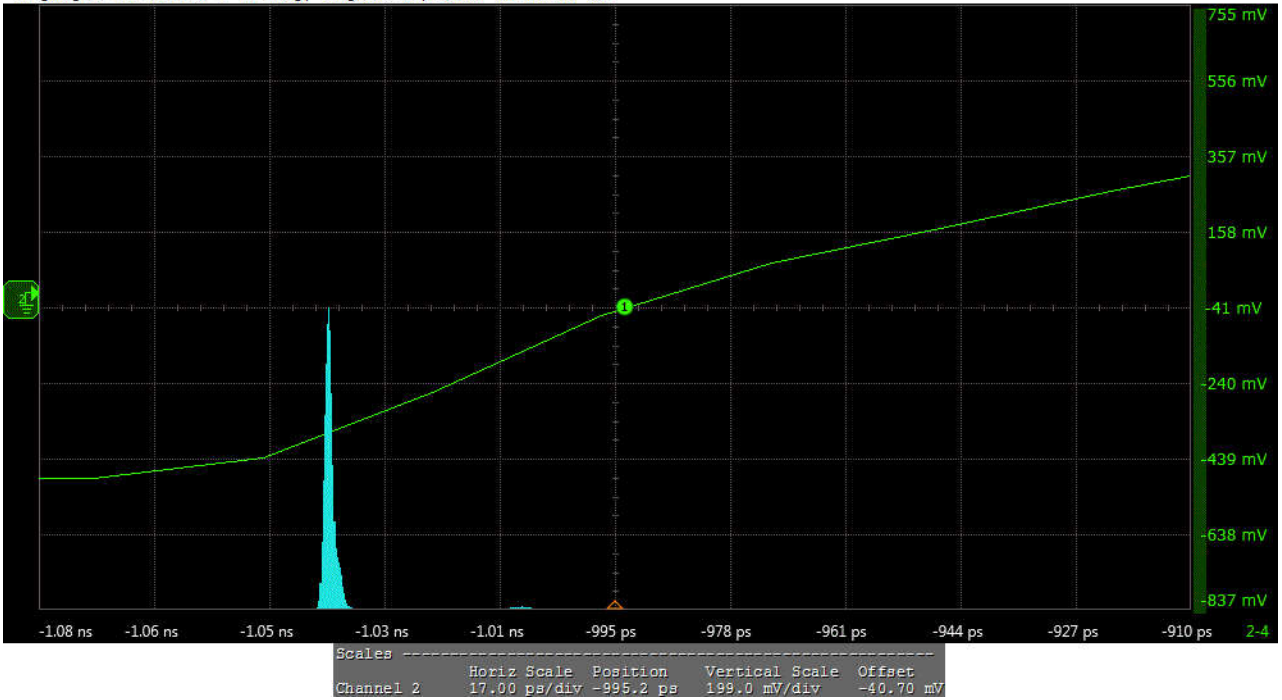
Transfer Function

File C:\ProgramData\Keysight\Infiniium\Apps\HDMIHEACTest\app\config\TransferFunction\Final\Final_WorstCableModel_HDMIEquFile7M_skew_n_112.tf4

Trial 1

Trial 1: Transition Time

Keysight Infiniium : Friday, August 26, 2016 11:56:55 AM



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HF1-2: D1 Fall Time Reference: Test ID HF1-2

Test Summary: Pass **Test Description:** The transition time is defined as the time interval between the normalized 20% and 80% amplitude levels. For compliance, the DUT should output the highest supported pixel clock frequency during the test.

Pass Limits: >= 42.500 ps **Transition Time** 106.130 ps

Result Details

HDMIAutomationConfig	Timing 101	Test Frequency(MHz)	148.471	Data Lane A	D1	Upper Threshold(%)	80.000
Lower Threshold(%)	20.000	#Edge	281.677000 k	Acquisition Bandwidth (GHz)	13.000		

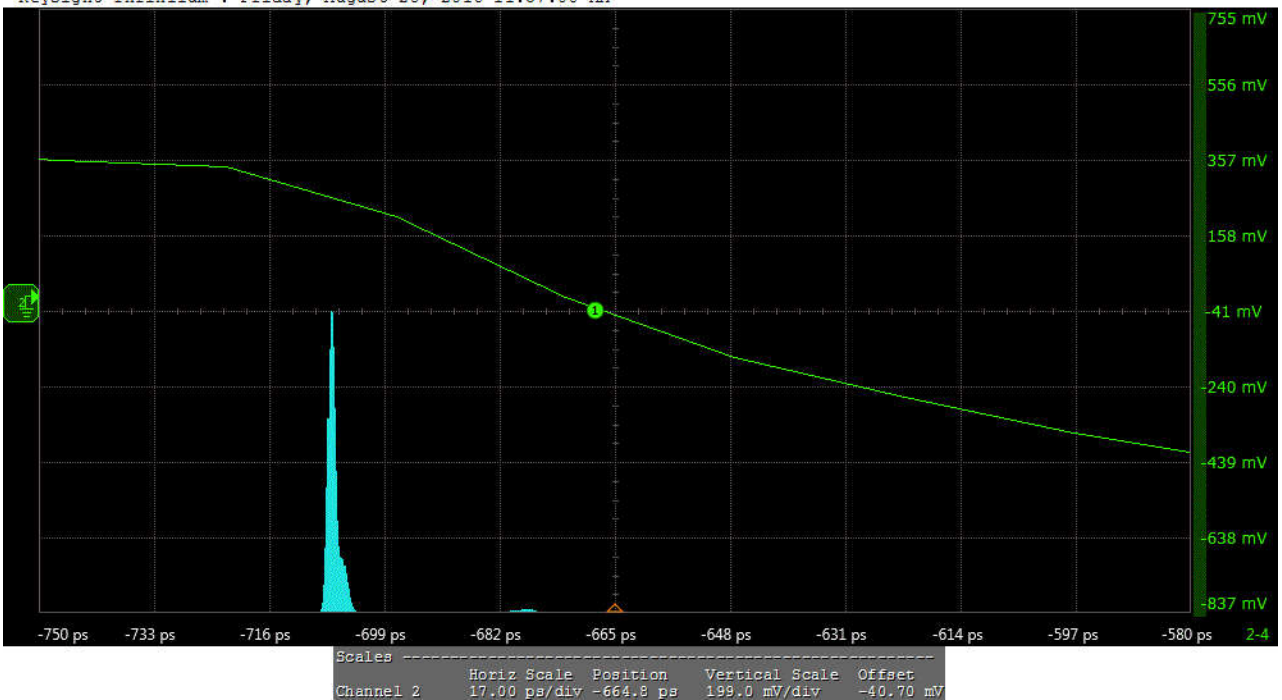
Transfer Function

File C:\ProgramData\Keysight\Infiniium\Apps\HDMIHEACTest\app\config\TransferFunction\Final\Final_WorstCableModel_HDMIEquFile7M_skew_n_112.tf4

Trial 1

Trial 1: Transition Time

Keysight Infiniium : Friday, August 26, 2016 11:57:06 AM



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HF1-8: D1 Mask Test (TP2_EQ with Worst Case Positive Skew) Reference: Test ID HF1-8

Test Summary: **Pass** Test Description: Confirm that the differential signal on each TMDS differential data pair has an "eye opening" (region of valid data) that meets or exceeds the limits on eye opening in the specification.

Pass Limits: **No Mask Failures** Total # failures **0.000**

Result Details

HDMIAutomationConfig	Timing 101	Eye Width(ps)	104.450	Eye Height(mV)	194.000	Data Lane A	D1
Test Frequency(MHz)	148.471	Mask Moved(ps)	0.000	# Acquisitions Point	16.000000000 M	Tbit(ps)	168.352
RightJitterData(Tbit)	380 m	LeftJitterData(Tbit)	380 m	RightJitterData(ps)	63.930	LeftJitterData(ps)	63.930
Maximum Margin	NA	Maximum Margin (Vertical)	NA	Differential Swing Voltage, VH(V)	476 m		
Differential Swing Voltage, VL (V)	-534 m	Differential Swing Voltage(V)	1.010	Acquisition Bandwidth (GHz)	13.000		

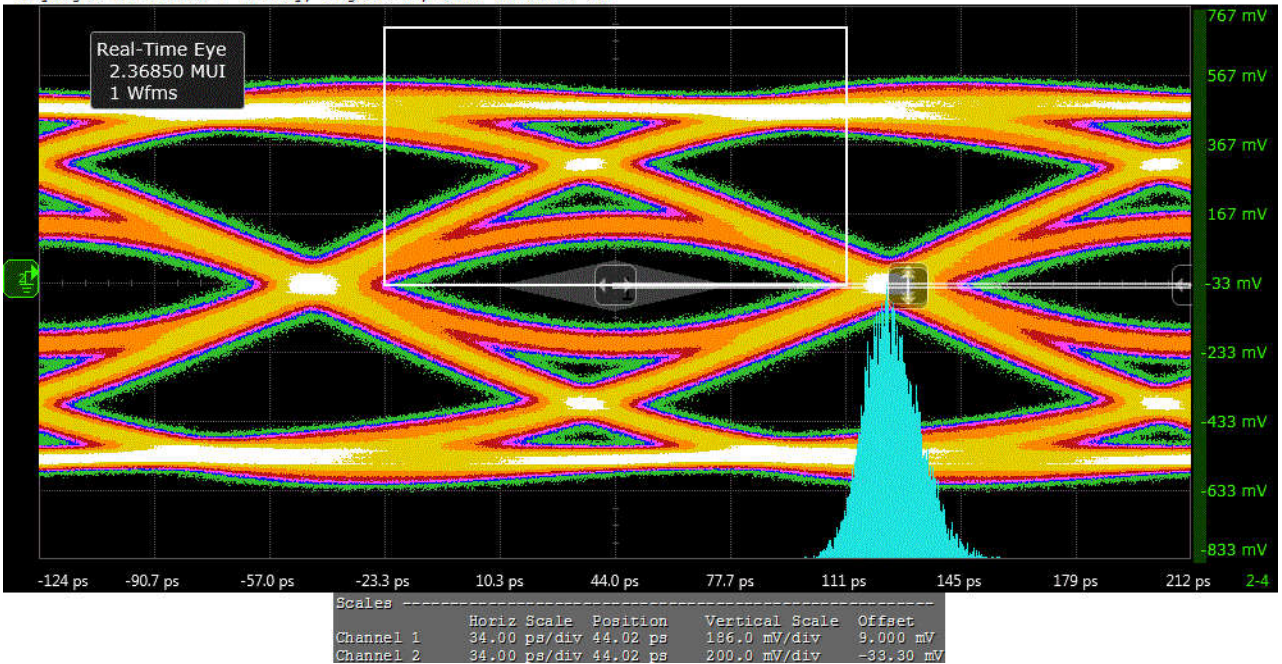
Transfer Function

File C:\ProgramData\Keysight\Infiniium\Apps\HDMIHEACTest\app\config\TransferFunction\Final\Final_WorstCableModel_HDMIEquFile7M_skew_p_112.tf4

Trial 1

Trial 1: Total # failures

Keysight Infiniium : Friday, August 26, 2016 12:03:47 PM



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HF1-8: D1 Mask Test (TP2_EQ with Worst Case Negative Skew) Reference: Test ID HF1-8

Test Summary: **Pass** Test Description: Confirm that the differential signal on each TMDS differential data pair has an "eye opening" (region of valid data) that meets or exceeds the limits on eye opening in the specification.

Pass Limits: **No Mask Failures** Total # failures **0.000**

Result Details

HDMIAutomationConfig	Timing 101	Eye Width(ps)	104.720	Eye Height(mV)	189.000	Data Lane A	D1
Test Frequency(MHz)	148.471	Mask Moved(ps)	0.000	# Acquisitions Point	16.000000000 M	Tbit(ps)	168.353
RightJitterData(Tbit)	378 m	LeftJitterData(Tbit)	378 m	RightJitterData(ps)	63.670	LeftJitterData(ps)	63.670
Maximum Margin	NA	Maximum Margin (Vertical)	NA	Differential Swing Voltage, VH(V)	475 m		
Differential Swing Voltage, VL (V)	-544 m	Differential Swing Voltage(V)	1.019	Acquisition Bandwidth (GHz)	13.000		

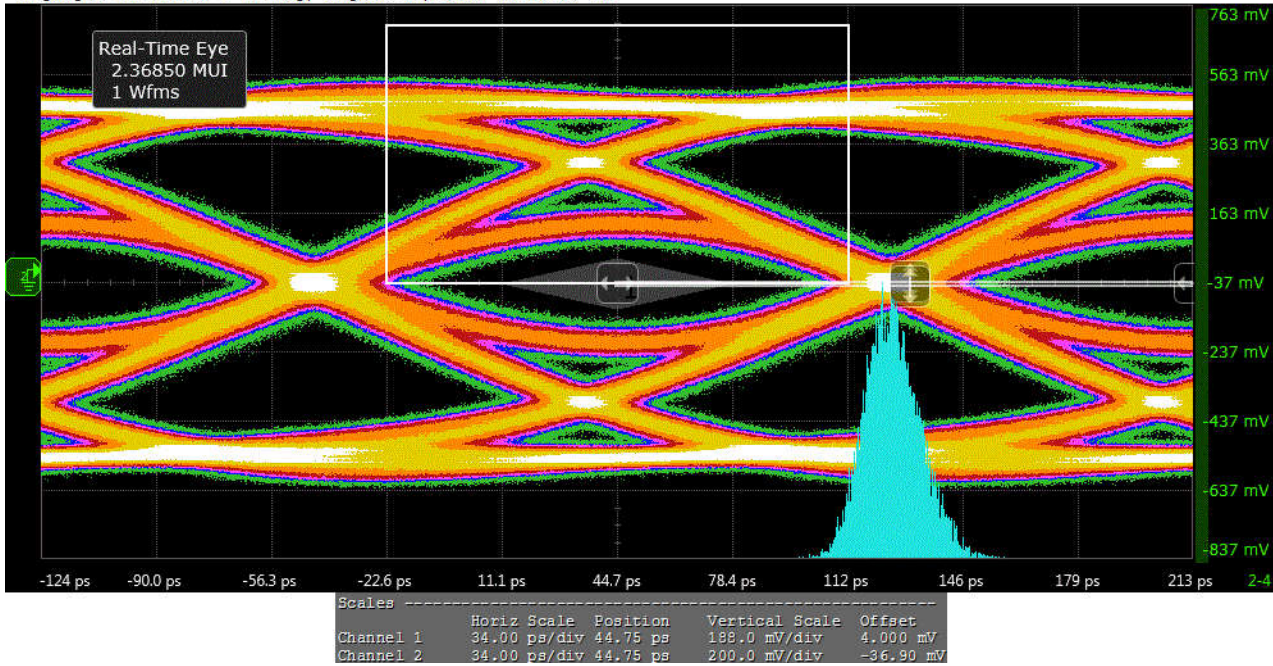
Transfer Function

File C:\ProgramData\Keysight\Infiniium\Apps\HDMIHEACTest\app\config\TransferFunction\Final\Final_WorstCableModel_HDMIEquFile7M_skew_n_112.tf4

Trial 1

Trial 1: Total # failures

Keysight Infiniium : Friday, August 26, 2016 12:10:23 PM



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HF1-4: Intra-Pair Skew - Data Lane 1 Reference: Test ID HF1-4

Test Summary: Pass **Test Description:** Confirm that any skew within any one differential data pair in the TMDS portion of the HDMI link does not exceed the limits in the specification.

Pass Limits: [-150 mTbit to 150 mTbit] **Data Intra-Pair Skew** -26 mTbit

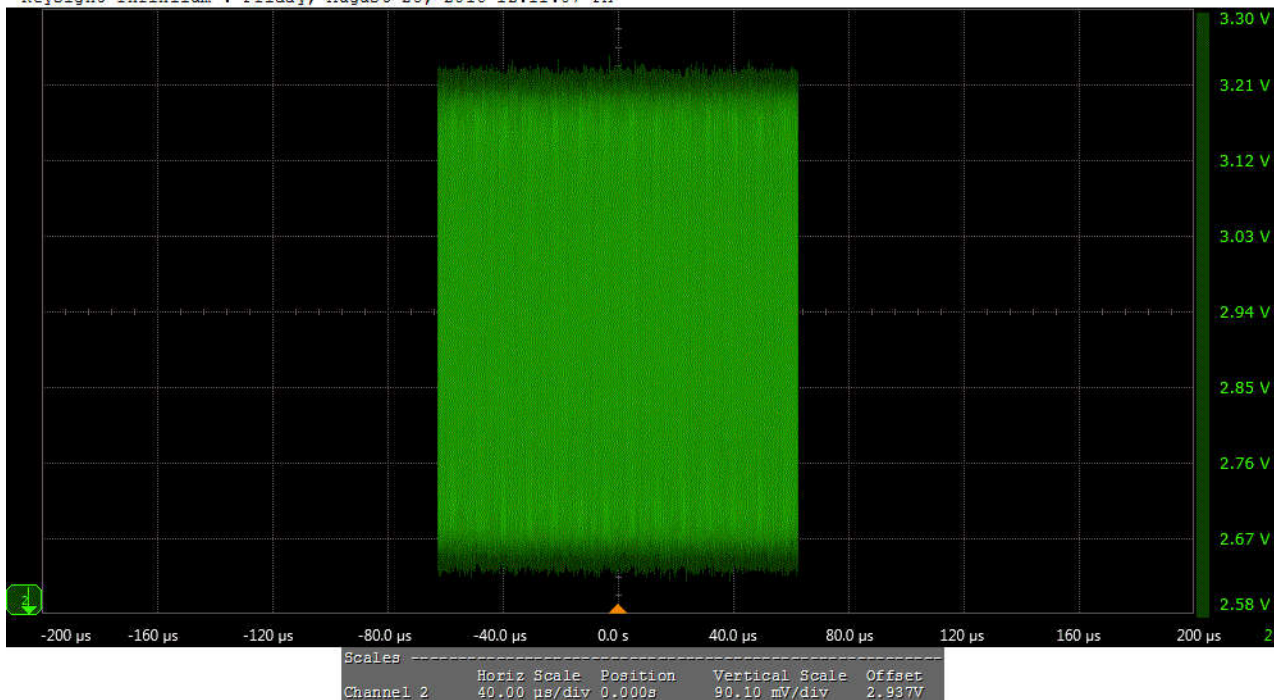
Result Details

HDMIAutomationConfig Timing 101		D+ Average Measurement Screenshot (See image)	
D- Average Measurement Screenshot (See image)		Max skew (ps) 8.200	Min skew (ps) -13.590
D+ threshold (V) 2.967	Acquisition Bandwidth (GHz) 13.000	Test Frequency(MHz) 148.471	Data Intra-Pair Skew(ps) -4.340

Trial 1

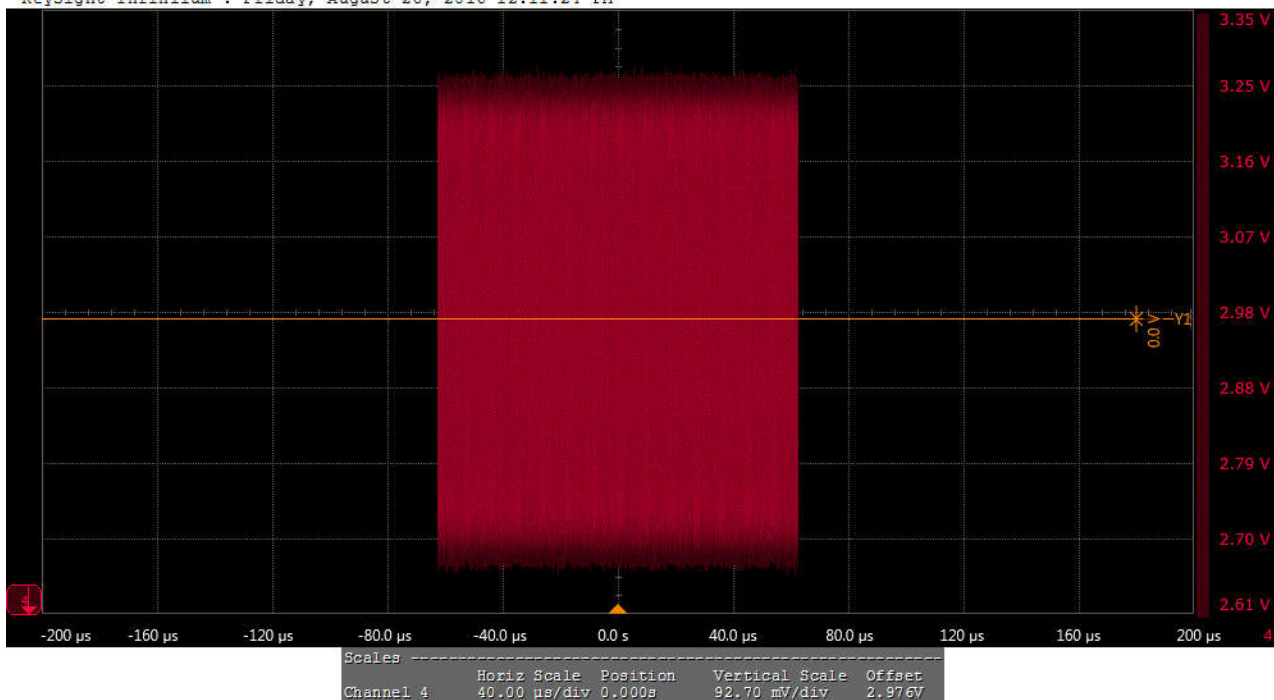
Trial 1: D+ Average Measurement Screenshot

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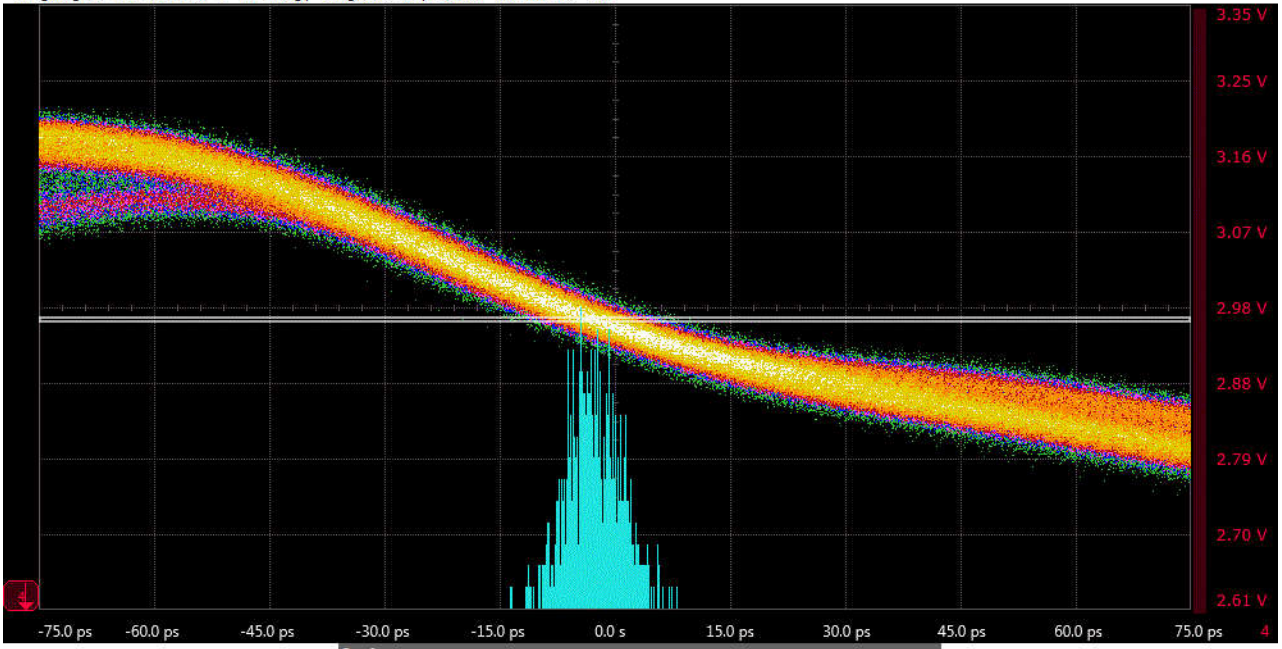
Trial 1: D- Average Measurement Screenshot

Keysight Infiniium : Friday, August 26, 2016 12:11:24 PM



Trial 1: Data Intra-Pair Skew

Keysight Infiniium : Friday, August 26, 2016 12:11:49 PM



Scales	Horiz Scale	Position	Vertical Scale	Offset
Channel 4	15.00 ps/div	0.000s	92.70 mV/div	2.976V

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✓ HF1-1: VL D1+ Reference: Test ID HF1-1

Test Summary: PASS Test Description: Confirm that the DC voltage levels on the HDMI link are within the specified limits for each TMDS signal.

Pass Limits: [2.300 V to 2.900 V] VL 2.759 V

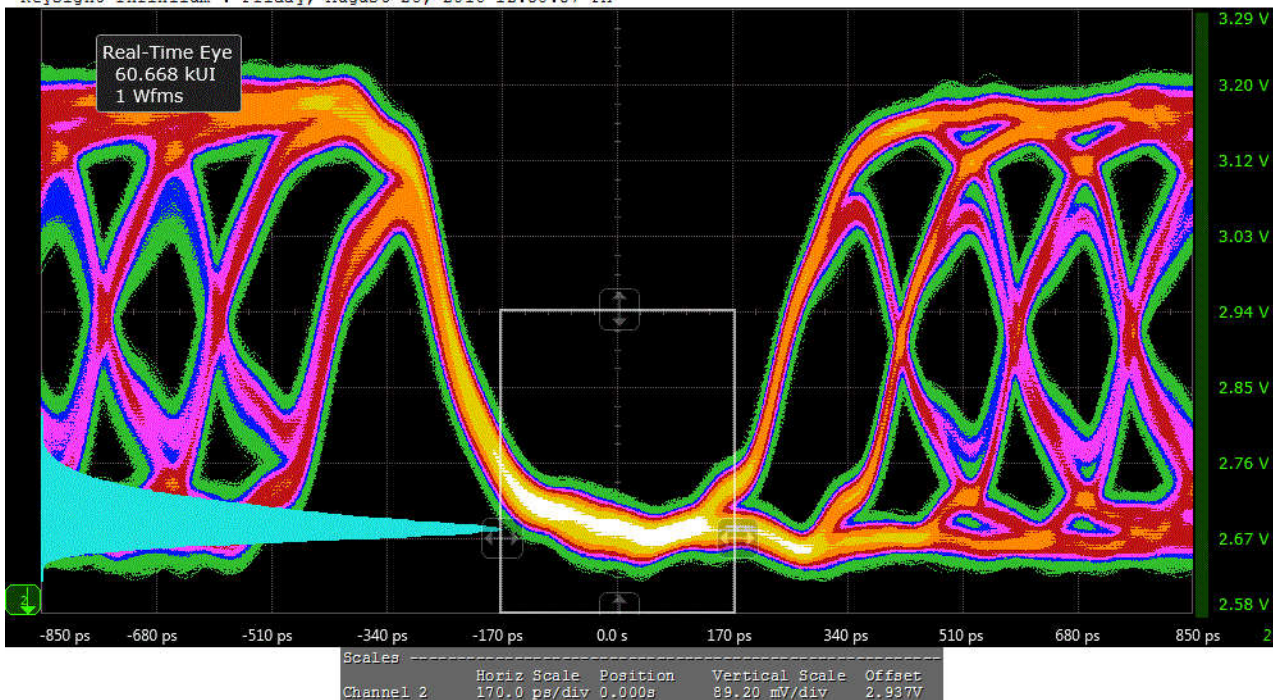
Result Details

HDMIAutomationConfig	Timing 101	Test Frequency	147.140 MHz	# Edges,VL	60.668000 k	# Edges,VH	60.609000 k	VH	3.251 V
VL	(See image)	VH	(See image)						

Trial 1

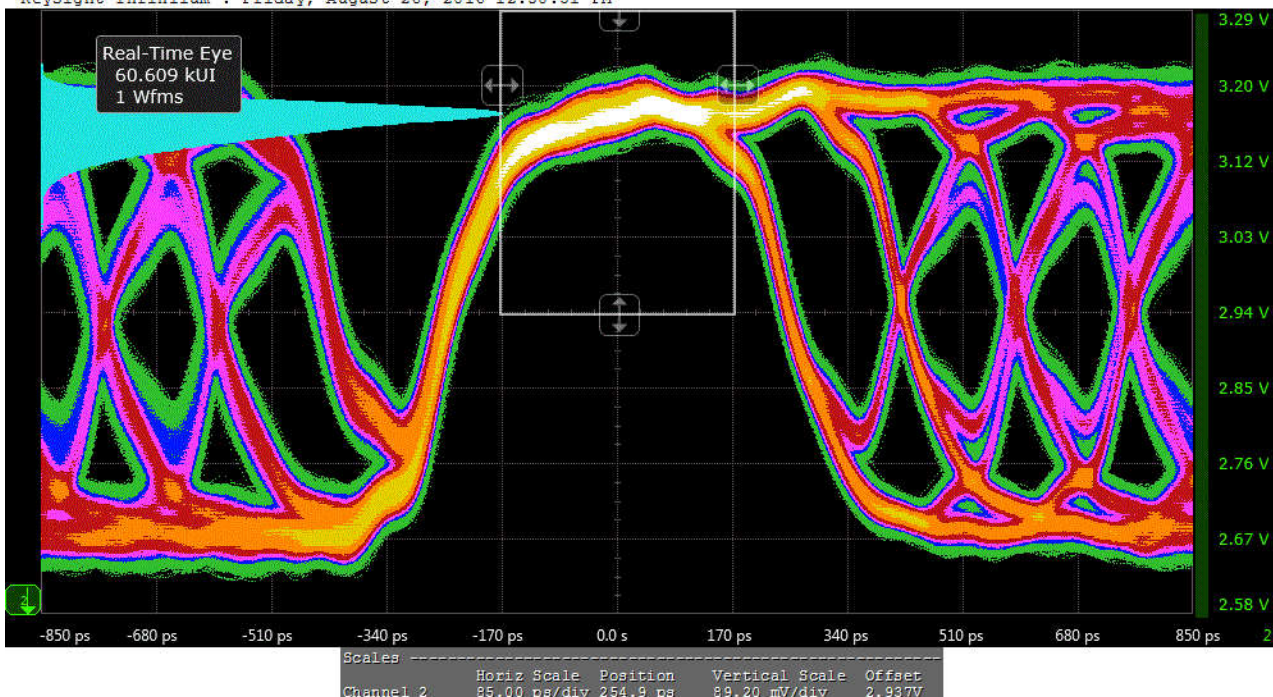
Trial 1: VL

Keysight Infiniium : Friday, August 26, 2016 12:30:37 PM



Trial 1: VH

Keysight Infiniium : Friday, August 26, 2016 12:30:51 PM



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✓ HF1-1: D1+ VSwing Reference: Test ID HF1-1

Test Summary: Pass Test Description: Confirm that the DC voltage levels on the HDMI link are within the specified limits for each TMDS signal.

Pass Limits: [400 mV to 600 mV] VSwing 492 mV

Result Details

HDMIAutomationConfig Timing 101

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✓ HF1-1: VL
D1- Reference: Test ID HF1-1

Test Summary: Pass Test Description: Confirm that the DC voltage levels on the HDMI link are within the specified limits for each TMDS signal.

Pass Limits: [2.300 V to 2.900 V] VL 2.756 V

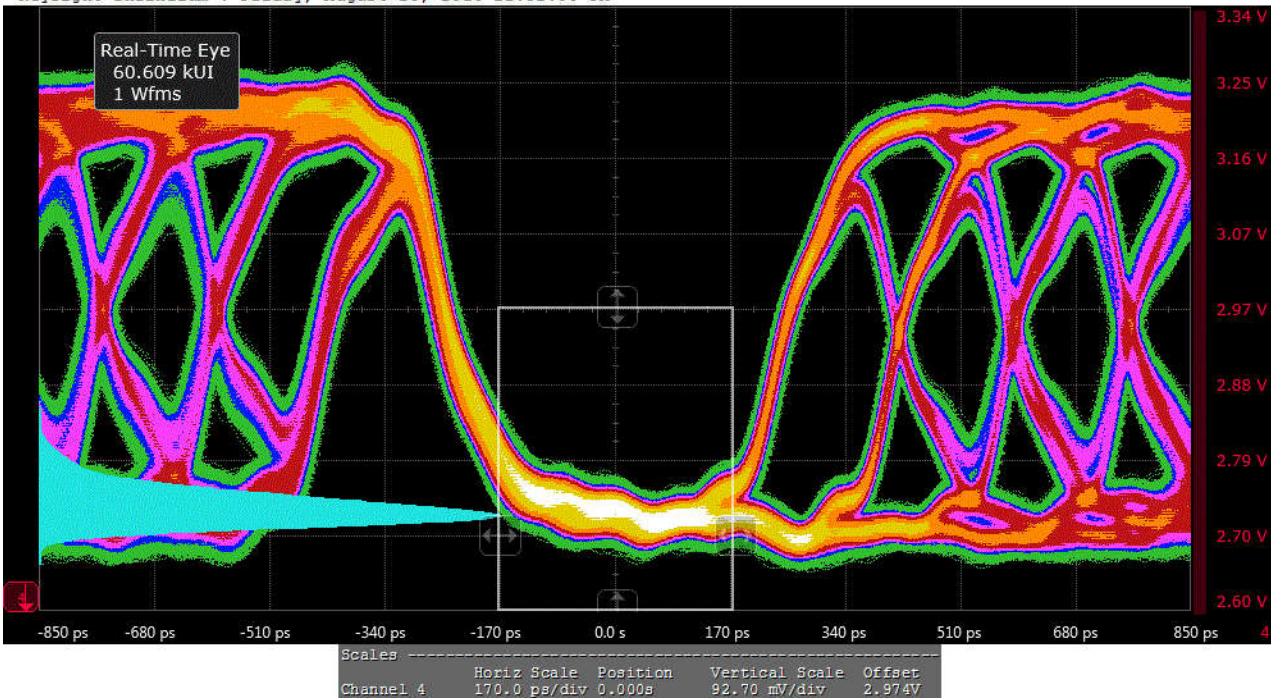
Result Details

HDMIAutomationConfig Timing 101 Test Frequency 147.140 MHz # Edges,VL 60.609000 k # Edges,VH 60.668000 k VH 3.247 V
 VL (See image) VH (See image)

Trial 1

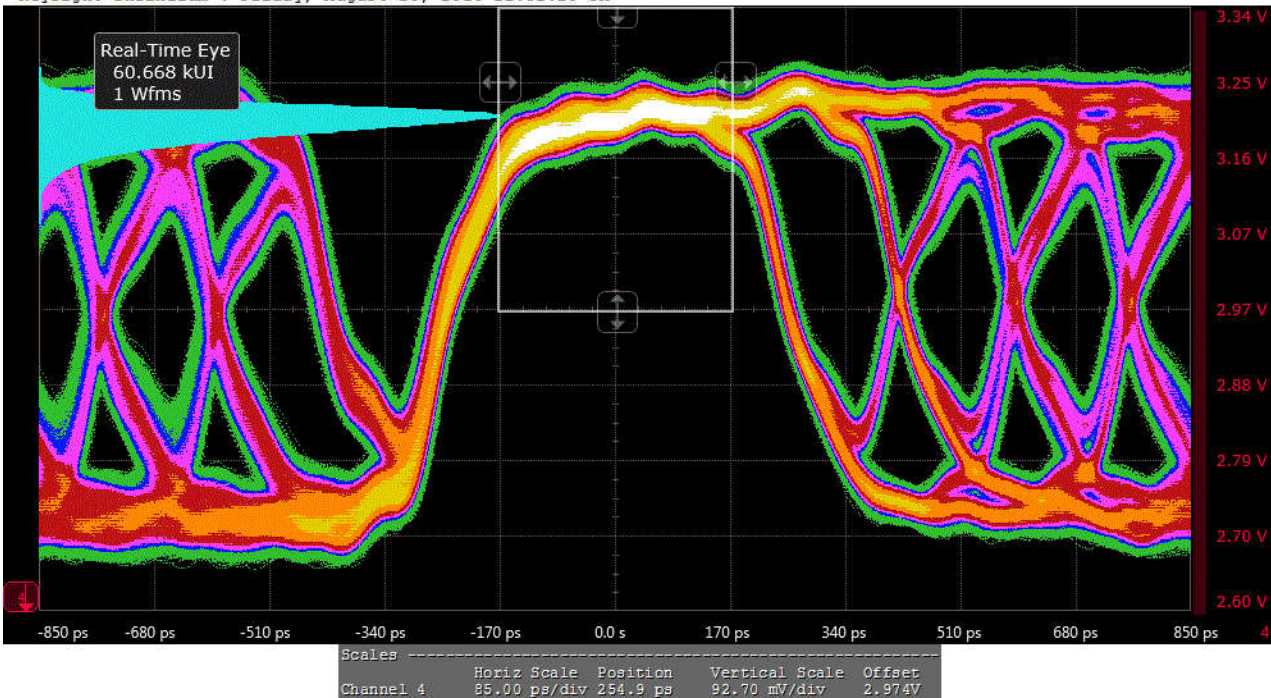
Trial 1: VL

Keysight Infiniium : Friday, August 26, 2016 12:31:06 PM



Trial 1: VH

Keysight Infiniium : Friday, August 26, 2016 12:31:20 PM



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✓HF1-1: D1- VSwing

Reference: Test ID HF1-1

Test Summary: Pass Test Description: Confirm that the DC voltage levels on the HDMI link are within the specified limits for each TMDS signal.

Pass Limits: [400 mV to 600 mV] VSwing 492 mV

Result Details

HDMIAutomationConfig Timing 101

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✓ HF1-5: D2 Maximum Differential Voltage

Reference: Test ID HF1-5

Test Summary: **Pass** Test Description: Confirm that the differential signal on each TMDS differential data pair does not exceed Maximum/Minimum Differential Voltage.

Pass Limits: **<= 780 m** Maximum Differential Voltage **603 m**

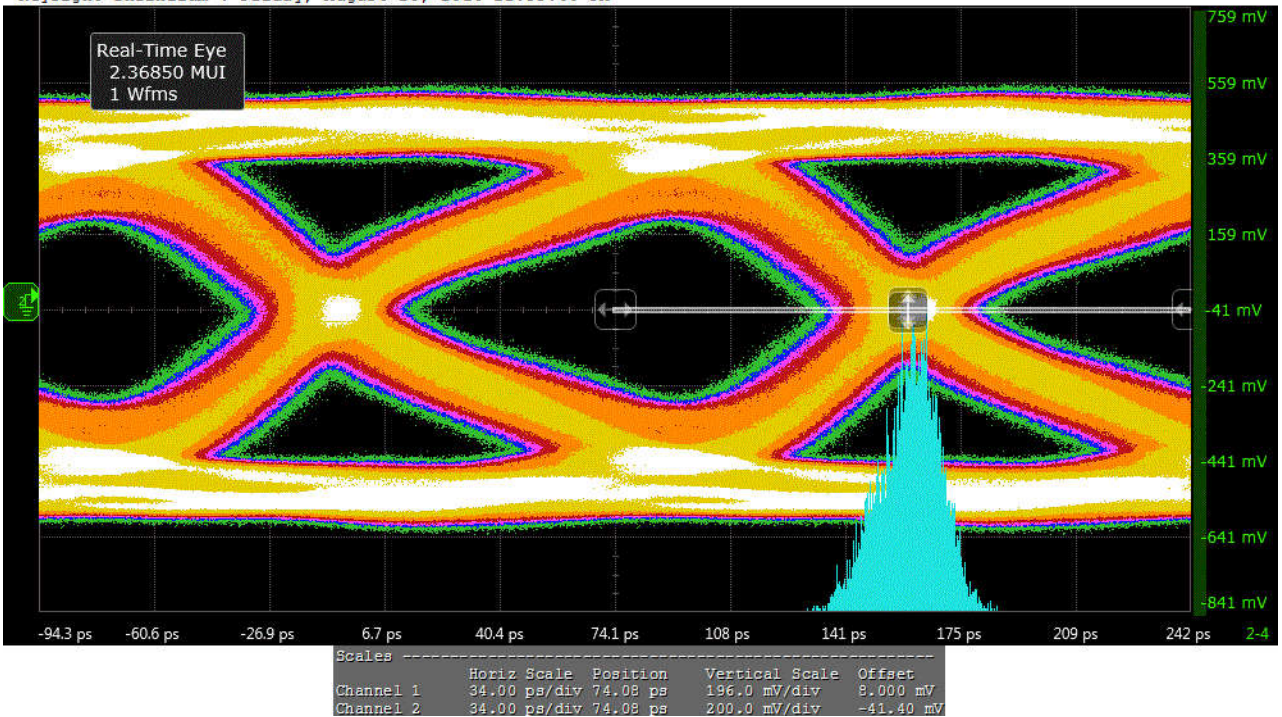
Result Details

HDMIAutomationConfig	Timing 101	Eye Width(ps)	106.020	Eye Height(mV)	322.000	Data Lane A	D2
Test Frequency(MHz)	148.481	Mask Moved(ps)	0.000	# Acquisitions Point	16.000000000 M	Tbit(ps)	168.353
RightJitterData(Tbit)	370 m	LeftJitterData(Tbit)	369 m	RightJitterData(ps)	62.350	LeftJitterData(ps)	62.090
Maximum Margin	NA	Maximum Margin (Vertical)	NA	Differential Swing Voltage, VH(V)	450 m		
Differential Swing Voltage, VL (V)	-534 m	Differential Swing Voltage(V)	984 m	Acquisition Bandwidth (GHz)	13.000		

Trial 1

Trial 1: Maximum Differential Voltage

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✓ HF1-5: D2 Minimum Differential Voltage

Reference: Test ID HF1-5

Test Summary: **Pass** Test Description: Confirm that the differential signal on each TMDS differential data pair does not exceed Maximum/Minimum Differential Voltage.

Pass Limits: **>= -780 m** Minimum Differential Voltage **-593 m**

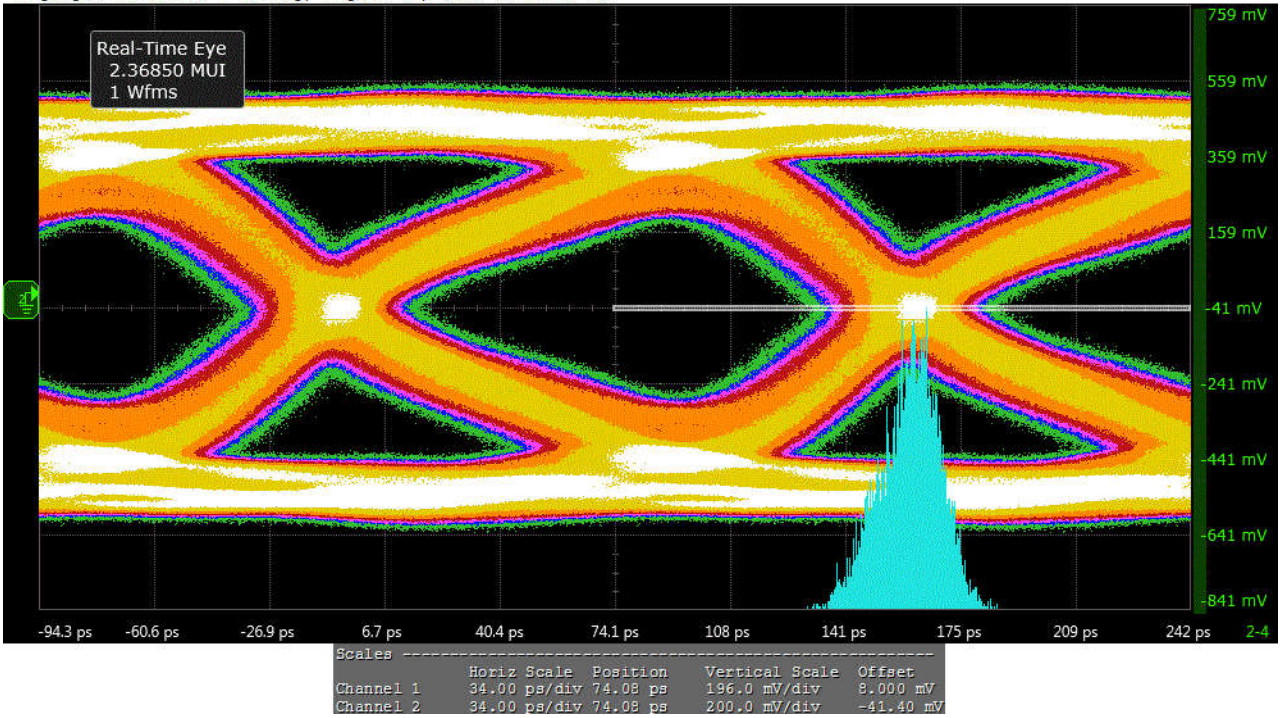
Result Details

HDMIAutomationConfig	Timing 101
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Trial 1

Trial 1: Minimum Differential Voltage

Keysight Infiniium : Friday, August 26, 2016 12:38:48 PM



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HF1-2: D2 Rise Time

Reference: Test ID HF1-2

Test Summary: **Pass** Test Description: Confirm that the rise times and fall times on the TMDS differential signals fall within the limits of the specification.

Pass Limits: **>= 42.500 ps** Transition Time **103.460 ps**

Result Details

HDMIAutomationConfig Timing 101 Test Frequency(MHz) 148.481 Data Lane A D2 Upper Threshold(%) 80.000
 Lower Threshold(%) 20.000 #Edge 312.903000 k Acquisition Bandwidth (GHz) 13.000

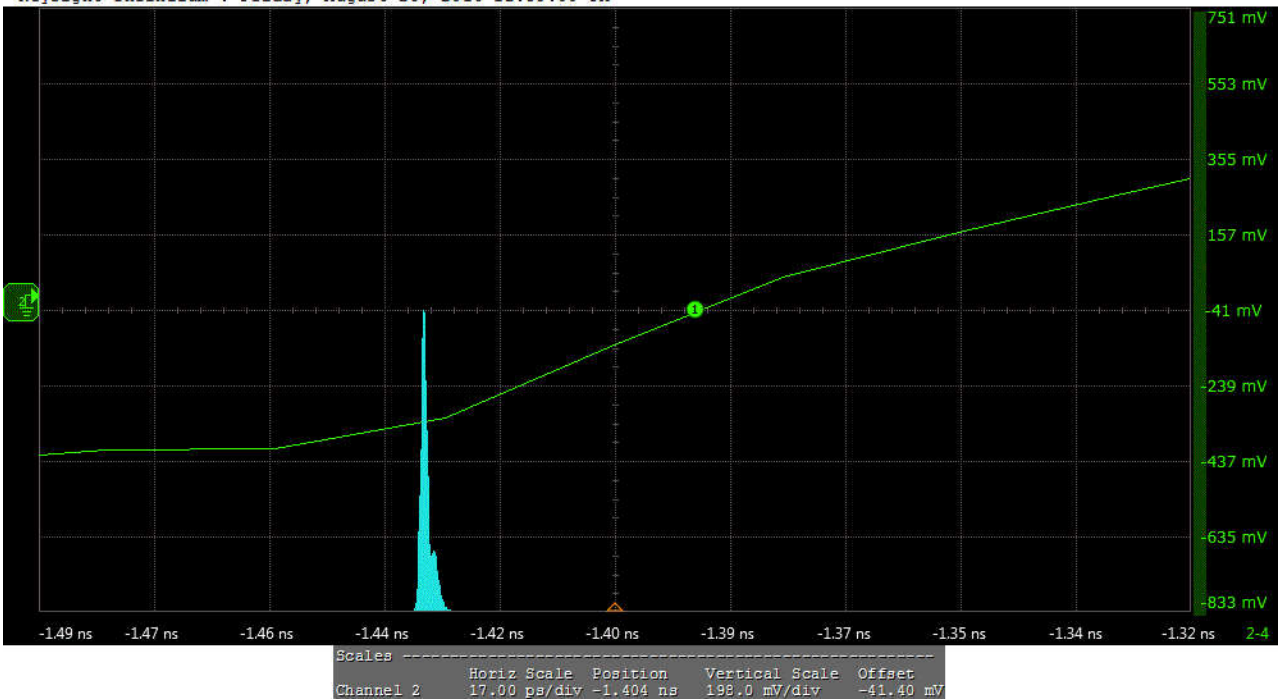
Transfer Function

File C:\ProgramData\Keysight\Infiniium\Apps\HDMIHEACTest\app\config\TransferFunction\Final\Final_WorstCableModel_HDMIEquFile7M_skew_n_112.tf4

Trial 1

Trial 1: Transition Time

Keysight Infiniium : Friday, August 26, 2016 12:39:00 PM



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HF1-2: D2 Fall Time Reference: Test ID HF1-2

Test Summary: **Pass** Test Description: Confirm that the rise times and fall times on the TMDS differential signals fall within the limits of the specification.

Pass Limits: ≥ 42.500 ps Transition Time 103.160 ps

Result Details

HDMIAutomationConfig	Timing 101	Test Frequency(MHz)	148.481	Data Lane A	D2	Upper Threshold(%)	80.000
Lower Threshold(%)	20.000	#Edge	312.903000 k	Acquisition Bandwidth (GHz)	13.000		

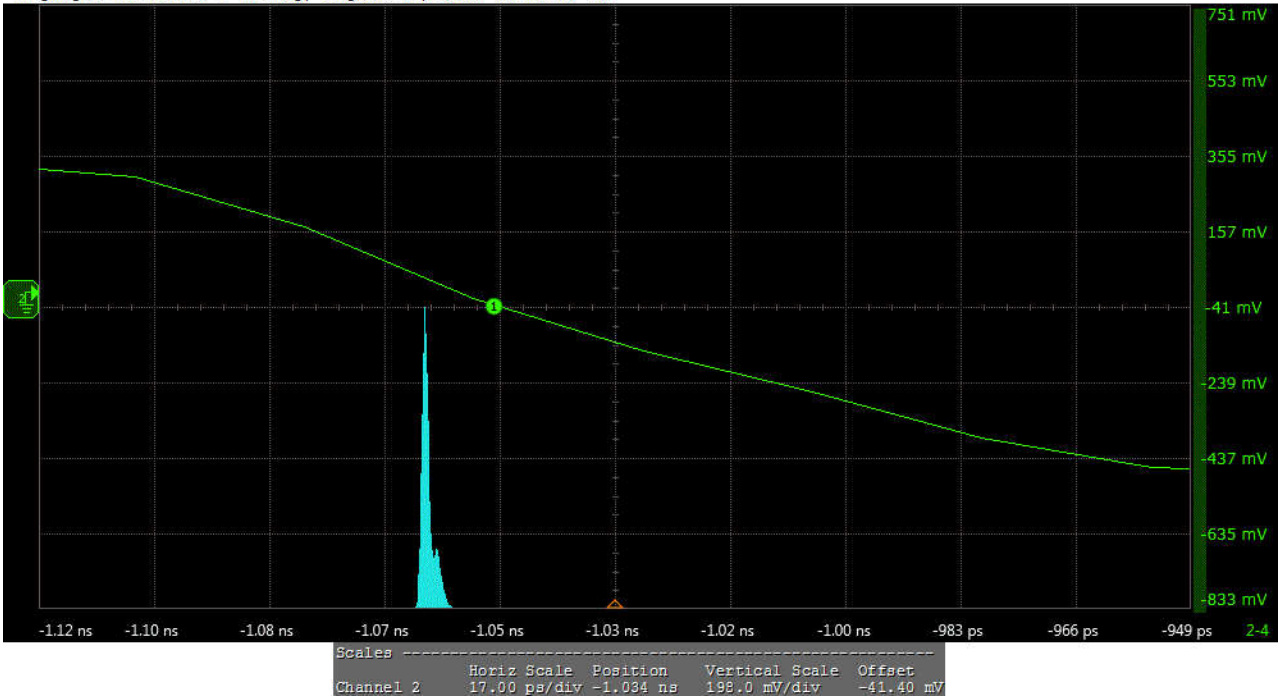
Transfer Function

File C:\ProgramData\Keysight\Infiniium\Apps\HDMIHEACTest\app\config\TransferFunction\Final\Final_WorstCableModel_HDMIEquFile7M_skew_n_112.tf4

Trial 1

Trial 1: Transition Time

Keysight Infiniium : Friday, August 26, 2016 12:39:12 PM



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HF1-8: D2 Mask Test (TP2_EQ with Worst Case Postive Skew) Reference: Test ID HF1-8

Test Summary: **Pass** Test Description: Confirm that the differential signal on each TMDS differential data pair has an "eye opening" (region of valid data) that meets or exceeds the limits on eye opening in the specification.

Pass Limits: No Mask Failures Total # failures 0.000

Result Details

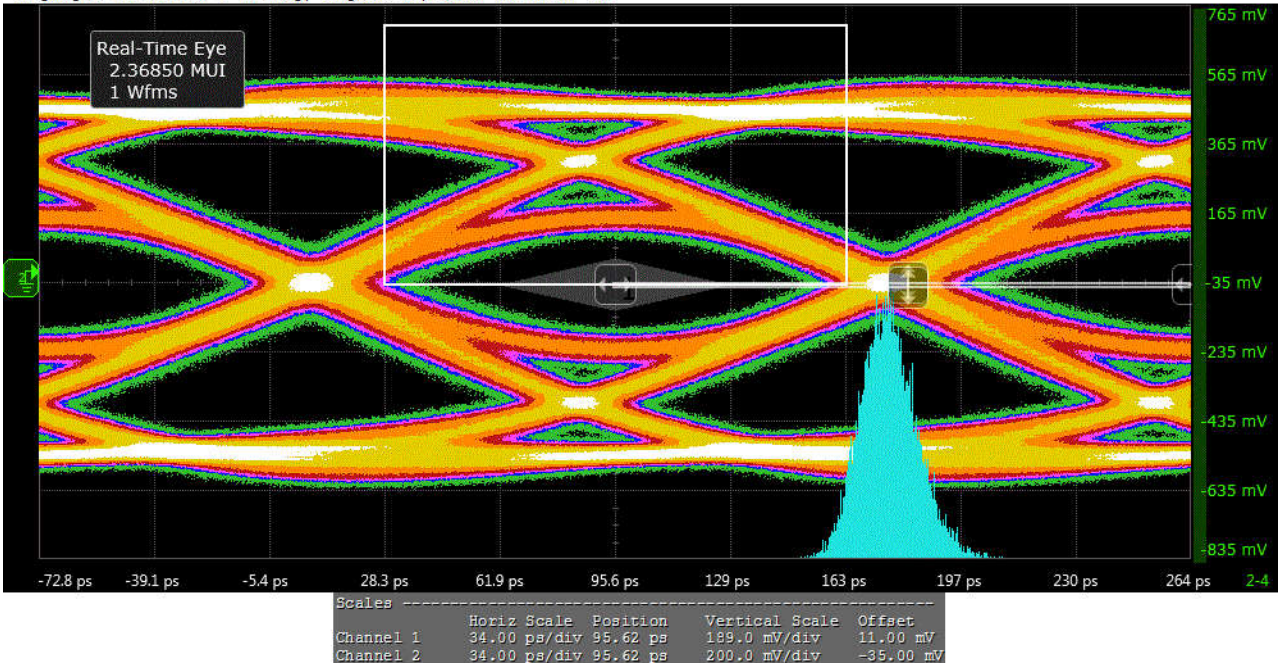
HDMIAutomationConfig	Timing 101	Eye Width(ps)	102.070	Eye Height(mV)	246.000	Data Lane A	D2
Test Frequency(MHz)	148.481	Mask Moved(ps)	0.000	# Acquisitions Point	16.000000000 M	Tbit(ps)	168.353
RightJitterData(Tbit)	394 m	LeftJitterData(Tbit)	394 m	RightJitterData(ps)	66.300	LeftJitterData(ps)	66.300
Maximum Margin	NA	Maximum Margin (Vertical)	NA	Differential Swing Voltage, VH(V)	469 m		
Differential Swing Voltage, VL (V)	-531 m	Differential Swing Voltage(V)	1.000	Acquisition Bandwidth (GHz)	13.000		

Transfer Function

File C:\ProgramData\Keysight\Infiniium\Apps\HDMIHEACTest\app\config\TransferFunction\Final\Final_WorstCableModel_HDMIEquFile7M_skew_p_112.tf4

Trial 1

Trial 1: Total # failures
 Keysight Infiniium : Friday, August 26, 2016 12:45:51 PM



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✓ HF1-8: D2 Mask Test (TP2_EQ with Worst Case Negative Skew) Reference: Test ID HF1-8

Test Summary Pass **Test Description:** Confirm that the differential signal on each TMDS differential data pair has an "eye opening" (region of valid data) that meets or exceeds the limits on eye opening in the specification.

Pass Limits: No Mask Failures **Total # failures** 0.000

Result Details

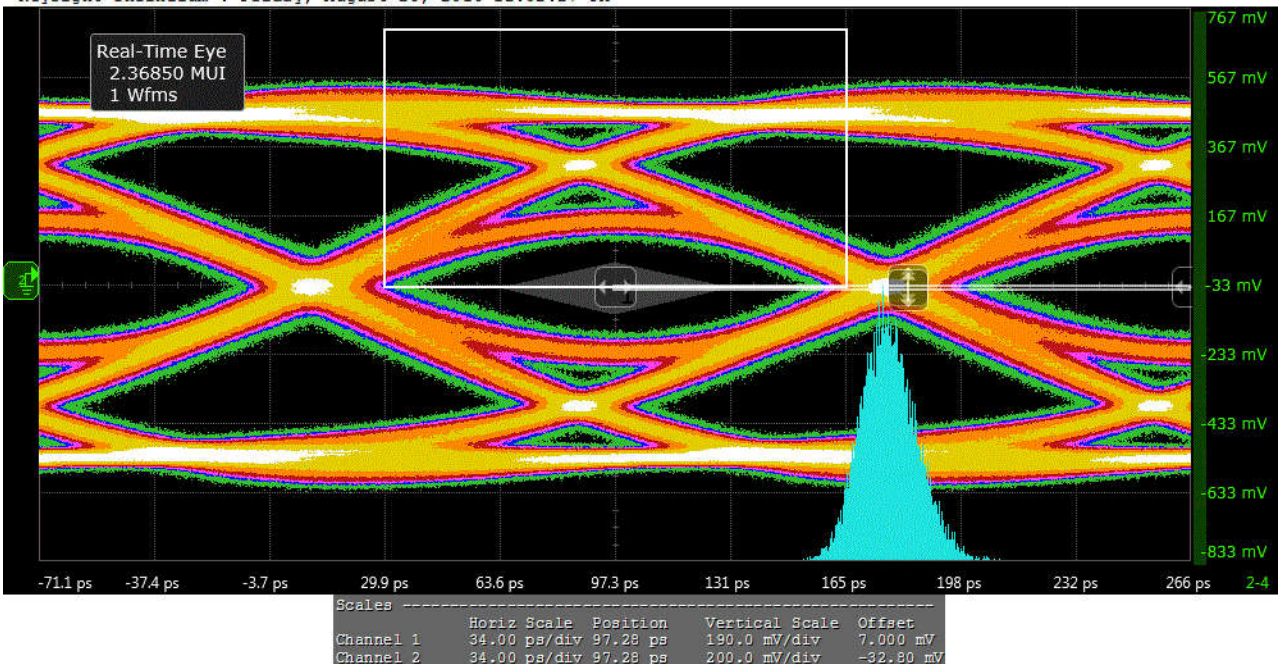
HDMIAutomationConfig	Timing 101	Eye Width(ps)	105.760	Eye Height(mV)	252.000	Data Lane A	D2
Test Frequency(MHz)	148.481	Mask Moved(ps)	0.000	# Acquisitions Point	16.0000000000 M	Tbit(ps)	168.353
RightJitterData(Tbit)	372 m	LeftJitterData(Tbit)	372 m	RightJitterData(ps)	62.610	LeftJitterData(ps)	62.610
Maximum Margin	NA	Maximum Margin (Vertical)	NA	Differential Swing Voltage, VH(V)	468 m		
Differential Swing Voltage, VL (V)	-526 m	Differential Swing Voltage(V)	994 m	Acquisition Bandwidth (GHz)	13.000		

Transfer Function

File C:\ProgramData\Keysight\Infiniium\Apps\HDMIHEACTest\app\config\TransferFunction\Final\Final_WorstCableModel_HDMIEquFile7M_skew_n_112.tf4

Trial 1

Trial 1: Total # failures
 Keysight Infiniium : Friday, August 26, 2016 12:52:27 PM



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✓HF1-1: VL D2+

Reference: Test ID HF1-1

Test Summary: Pass Test Description: Confirm that the DC voltage levels on the HDMI link are within the specified limits for each TMDS signal.

Pass Limits: [2.300 V to 2.900 V] VL 2.775 V

Result Details

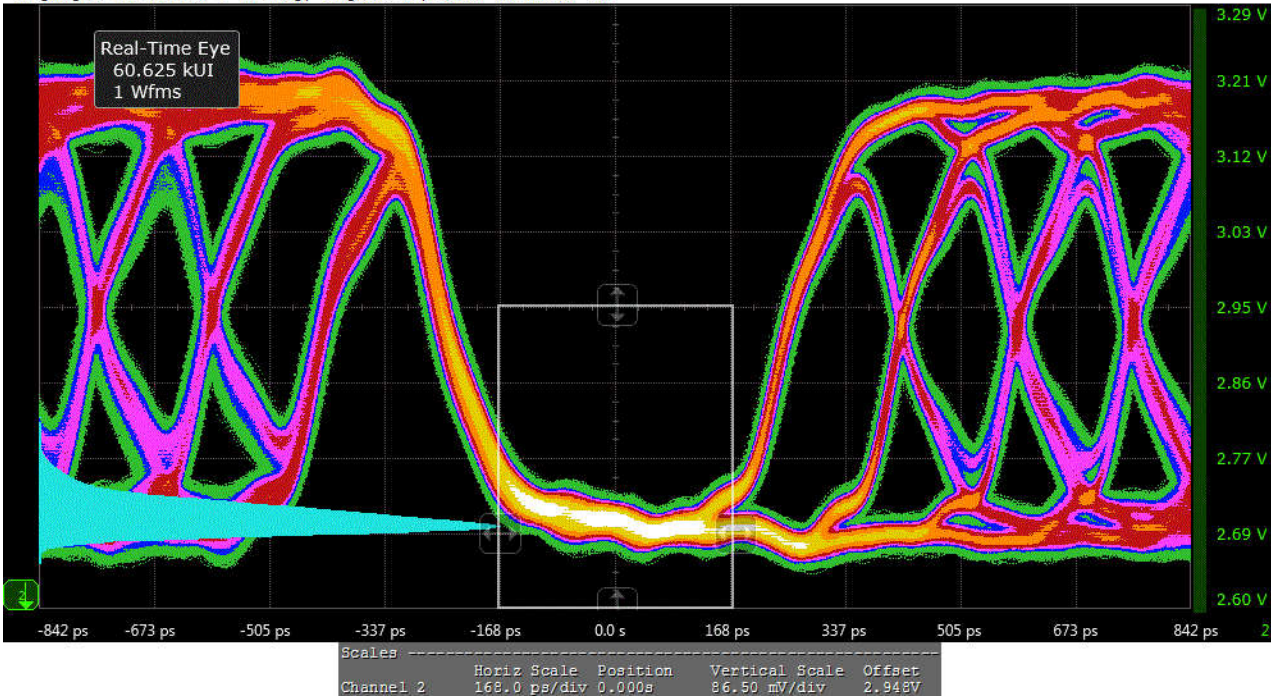
HDMIAutomationConfig Timing 101 Test Frequency 148.481 MHz # Edges,VL 60.625000 k # Edges,VH 60.509000 k VH 3.262 V

VL (See image) VH (See image)

Trial 1

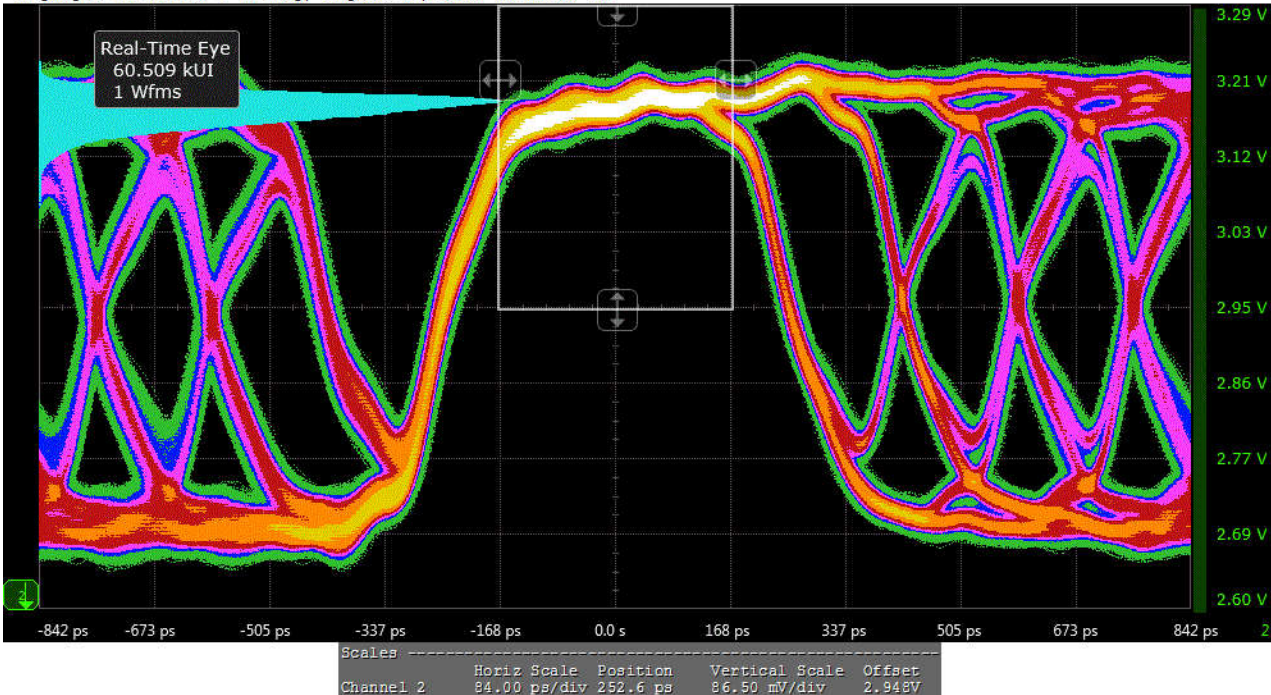
Trial 1: VL

Keysight Infiniium : Friday, August 26, 2016 12:53:06 PM



Trial 1: VH

Keysight Infiniium : Friday, August 26, 2016 12:53:20 PM



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✓HF1-1: D2+ VSwing

Reference: Test ID HF1-1

Test Summary: Pass Test Description: Confirm that the DC voltage levels on the HDMI link are within the specified limits for each TMDS signal.

Pass Limits: [400 mV to 600 mV] VSwing 487 mV

Result Details

HDMIAutomationConfig Timing 101

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HF1-1: VL
D2- Reference: Test ID HF1-1

Test Summary **Pass** Test Description: Confirm that the DC voltage levels on the HDMI link are within the specified limits for each TMDS signal.

Pass Limits: [2.300 V to 2.900 V] VL 2.775 V

Result Details

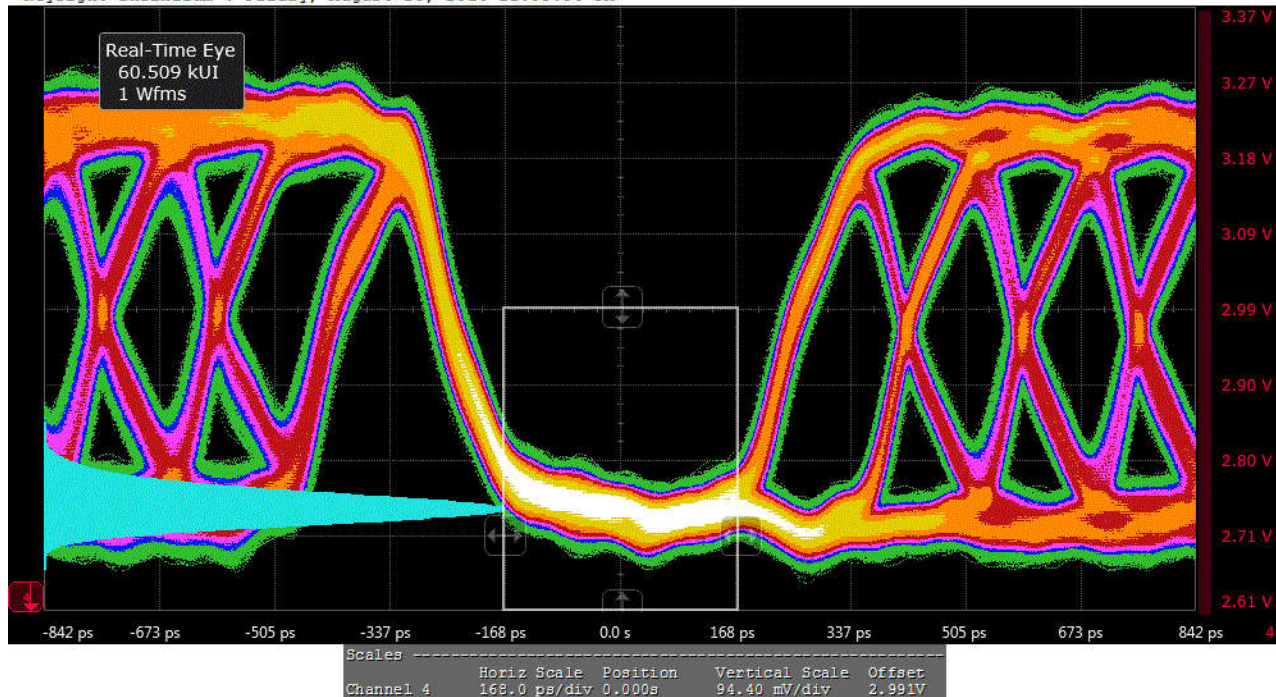
HDMIAutomationConfig Timing 101 Test Frequency 148.481 MHz # Edges,VL 60.509000 k # Edges,VH 60.625000 k VH 3.254 V

VL (See image) VH (See image)

Trial 1

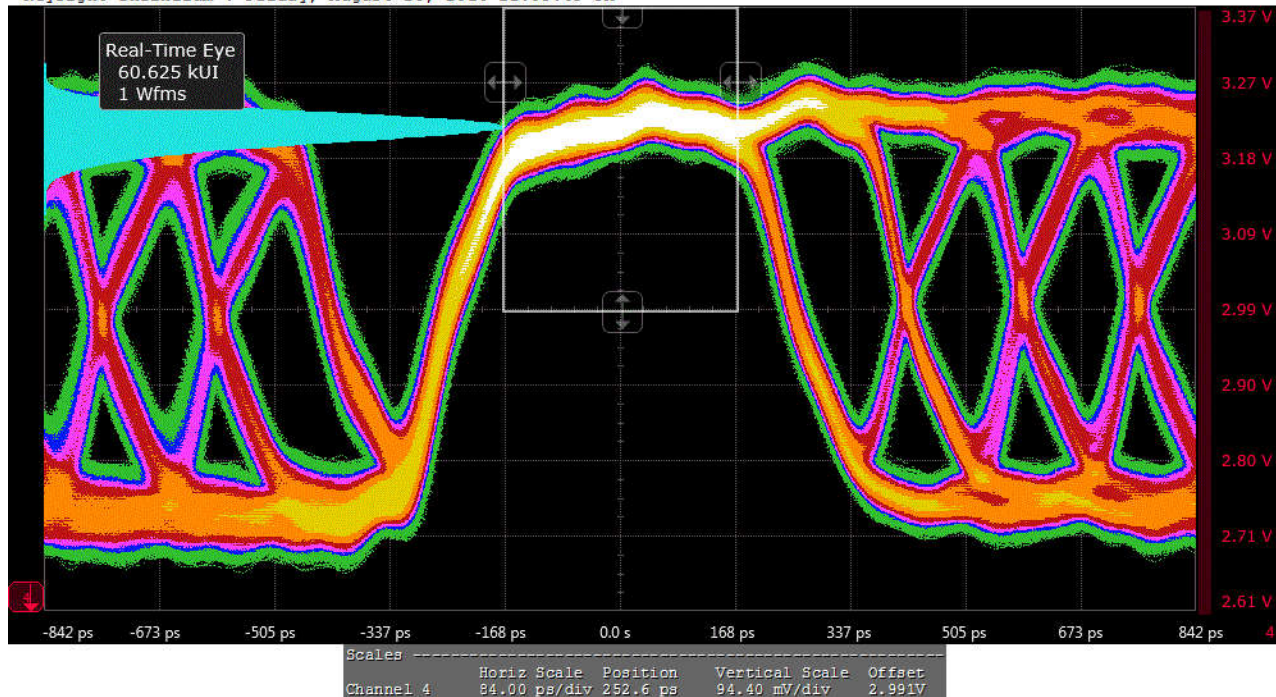
Trial 1: VL

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Trial 1: VH

Keysight Infiniium : Friday, August 26, 2016 12:53:49 PM



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✓ HF1-1: D2- VSwing Reference: Test ID HF1-1

Test Summary: **Pass** Test Description: Confirm that the DC voltage levels on the HDMI link are within the specified limits for each TMDS signal.

Pass Limits: [400 mV to 600 mV] VSwing 479 mV

Result Details

HDMIAutomationConfig Timing 101

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✓ HF1-4: Intra-Pair Skew - Data Lane 2 Reference: Test ID HF1-4

Test Summary: **Pass** Test Description: Confirm that any skew within any one differential data pair in the TMDS portion of the HDMI link does not exceed the limits in the specification.

Pass Limits: [-150 mTbit to 150 mTbit] Data Intra-Pair Skew 21 mTbit

Result Details

HDMIAutomationConfig Timing 101 **D+ Average Measurement Screenshot** (See image)

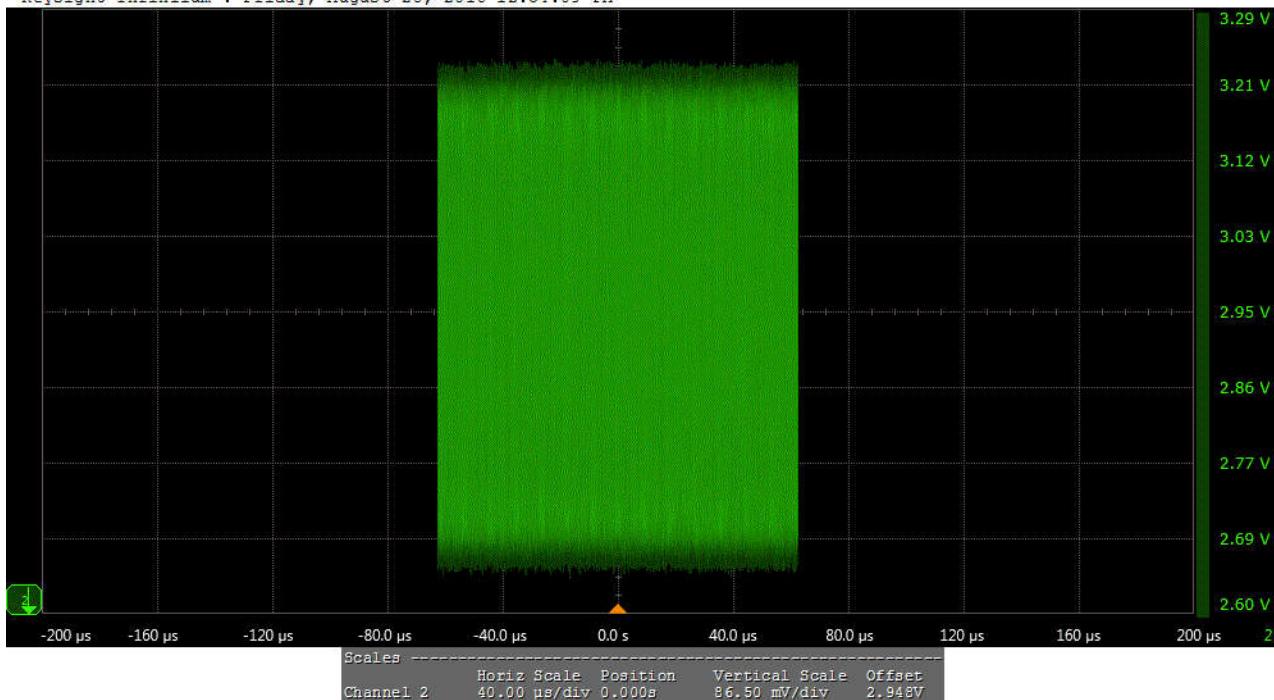
D- Average Measurement Screenshot (See image) **Max skew (ps)** 14.410 **Min skew (ps)** -9.610 **D+ threshold (V)** 2.940

D- threshold (V) 2.981 **Acquisition Bandwidth (GHz)** 13.000 **Test Frequency(MHz)** 148.481 **Data Intra-Pair Skew(ps)** 3.520

Trial 1

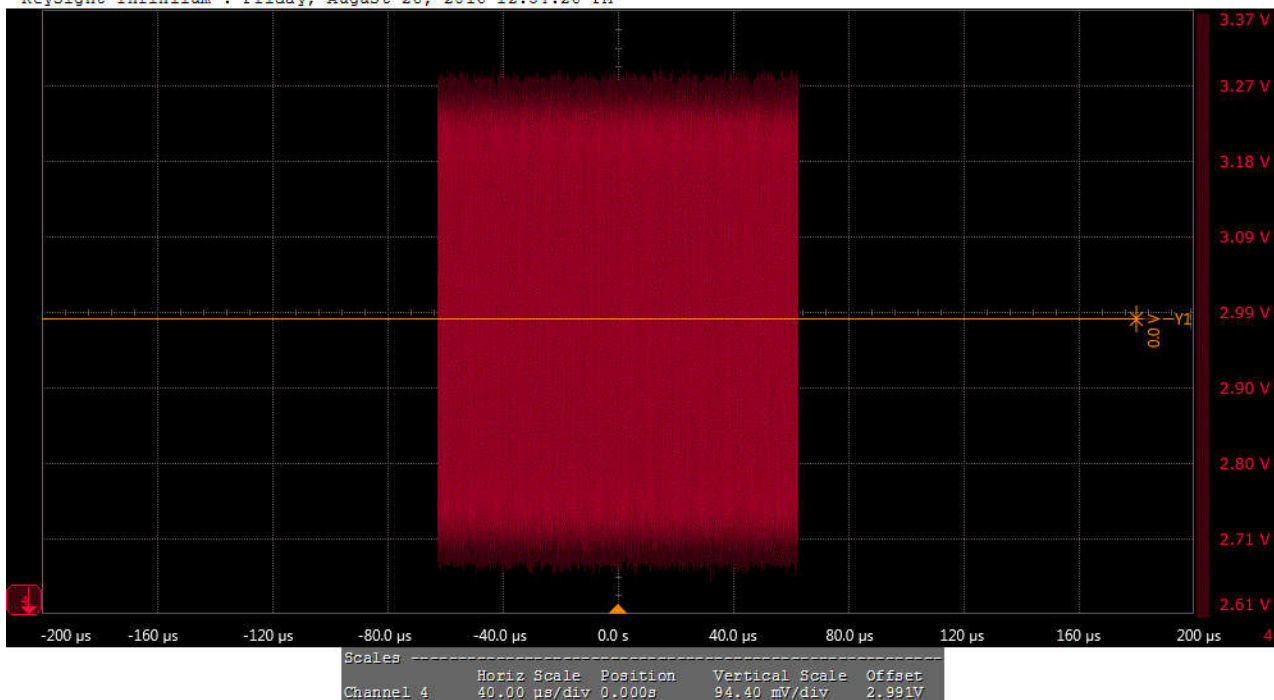
Trial 1: D+ Average Measurement Screenshot

Keysight Infiniium : Friday, August 26, 2016 12:54:09 PM



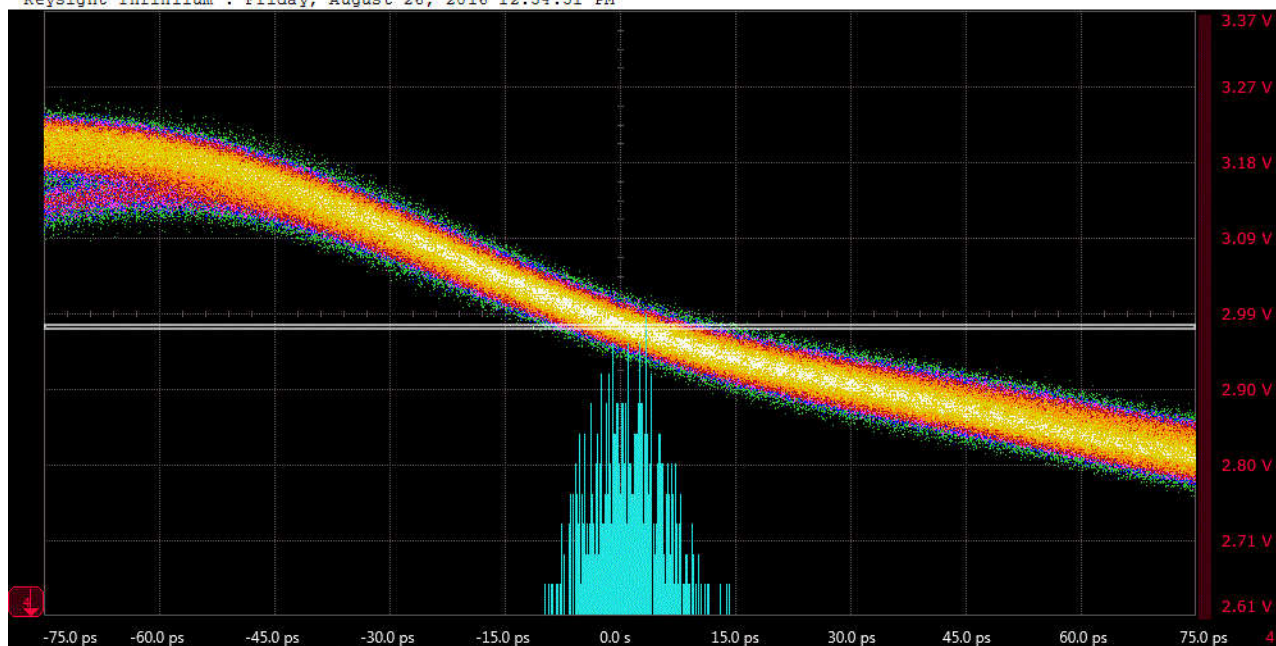
Trial 1: D- Average Measurement Screenshot

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Trial 1: Data Intra-Pair Skew

Keysight Infiniium : Friday, August 26, 2016 12:54:51 PM



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