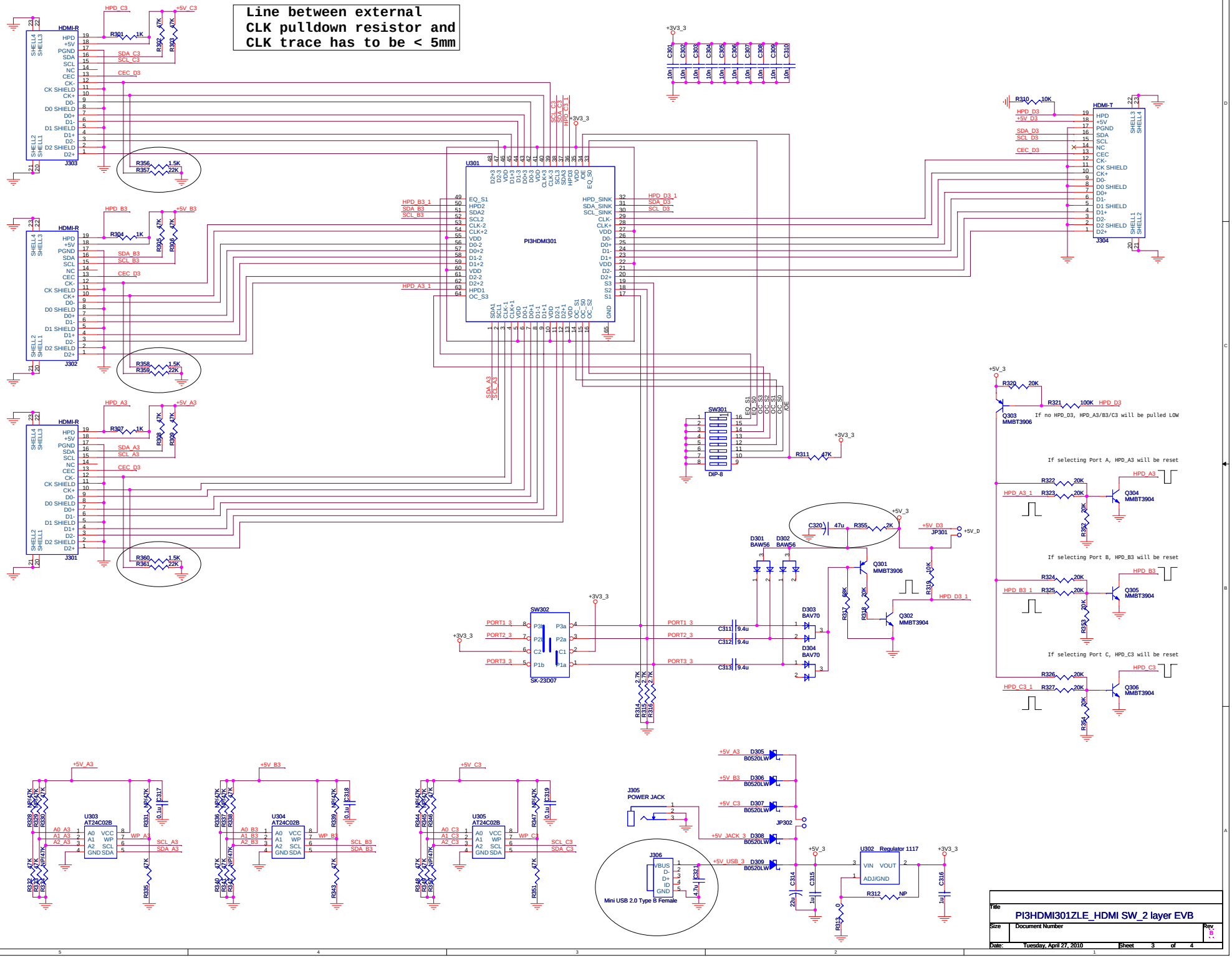
Line between external CLK pulldown resistor and CLK trace has to be < 5mm
PI3HDMI301
U301
HDMI-R
J303
J302
J301
HDMI-T
J304
SW301
DIP-8
SW302
SK-23D07
U303 AT24C02B
U304 AT24C02B
U305 AT24C02B
J305 POWER JACK
J306
Mini USB 2.0 Type B Female
U302 Regulator 1117
JP301
JP302
Q301 MMBT3906
Q302 MMBT3904
Q303 MMBT3906
Q304 MMBT3904
Q305 MMBT3904
Q306 MMBT3904
If no HPD_D3, HPD_A3/B3/C3 will be pulled LOW
If selecting Port A, HPD_A3 will be reset
If selecting Port B, HPD_B3 will be reset
If selecting Port C, HPD_C3 will be reset
Title
PI3HDMI301ZLE_HDMI SW_2 layer EVB
Size
Document Number
Rev
B
Date: Tuesday, April 27, 2010
Sheet 3 of 4