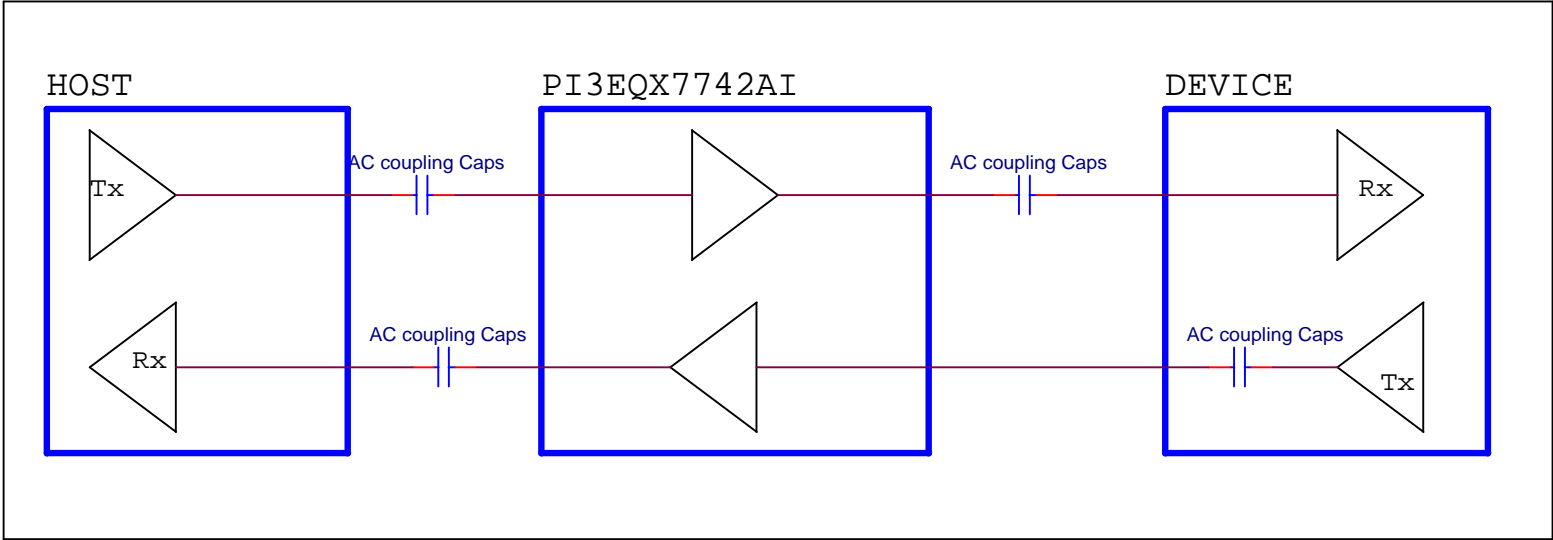
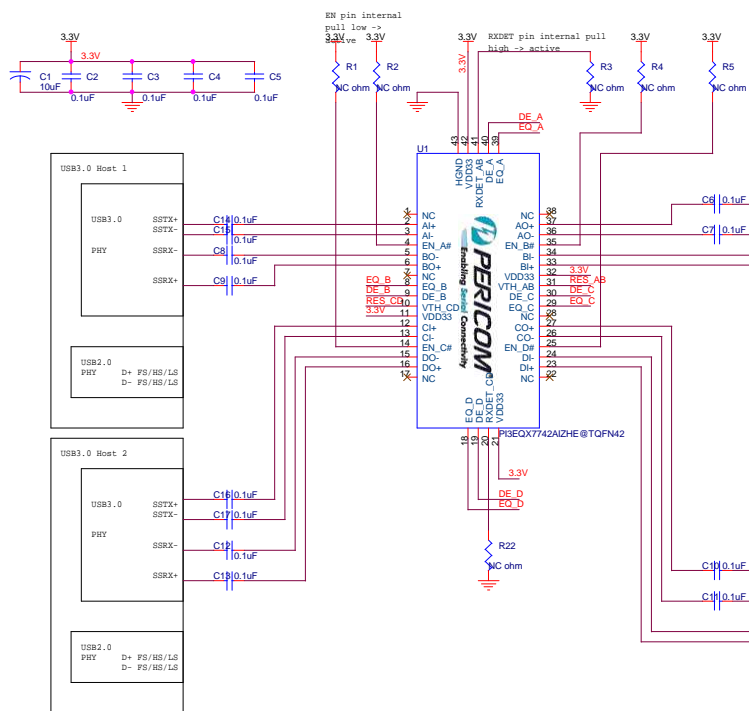


Revision History

Date	History
11/26/2012	First Released PI3EQX7742AI

Block Diagram

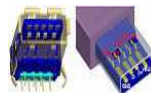




USB3.0 Connector

A type

Type A Connector use
Host side pin define



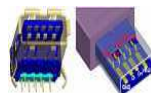
NOTE:
Type B Connector use DEVICE
side pin define



USB3.0 Connector

A type

Type A Connector use
Host side pin define



NOTE:
Type B Connector use DEVICE
side pin define

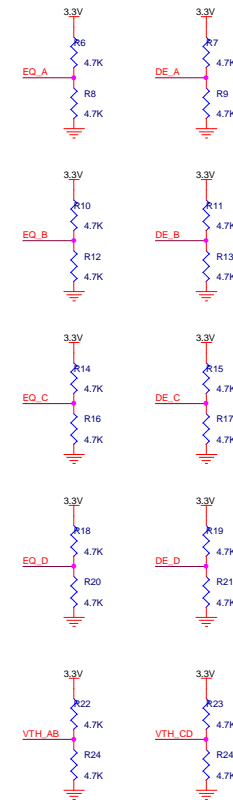


USB3.0 Device

USB3.0 RX+
USB3.0 RX-

USB3.0 TX+
USB3.0 TX-

D+ FS/HS/LS USB2.0
D- FS/HS/LS PHY



NOTE:
After PCB layout, de-emphasis, threshold setting and Equalizer should be fine tune

Output de- emphasis setting	
DE_A/B/C/D	De-emphasis
0	0 dB
open	-3.5 dB
1	-6 dB

Equalizer setting	
EQ_A/B/C/D	@2.5GHz
0	3 dB
open	6 dB
1	9 dB

EN_x#	RxDet	Input R	Output R
1	X	Hi-Z	Hi-Z
0	1	50 ohm / Hi-Z	50 ohm / Hi-Z
0	0	50 ohm	50 ohm