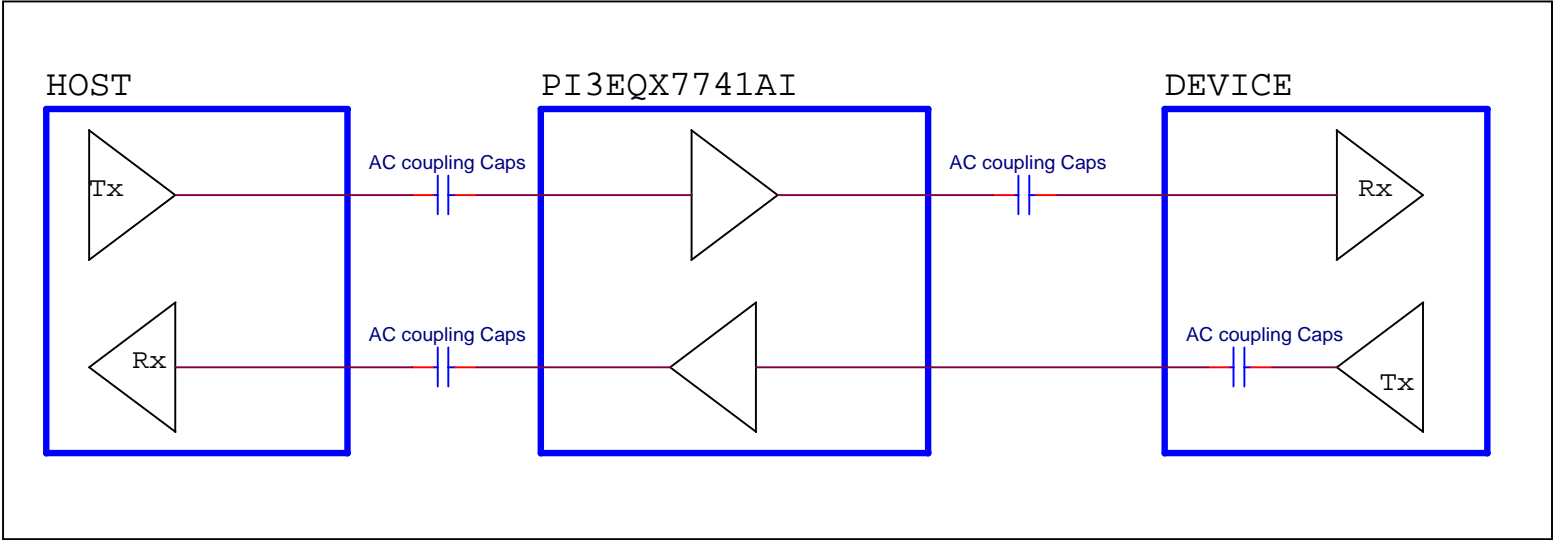


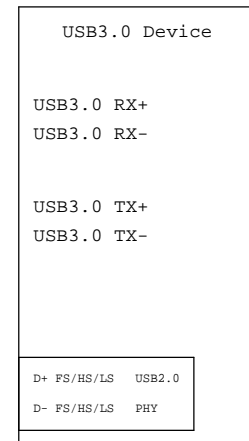
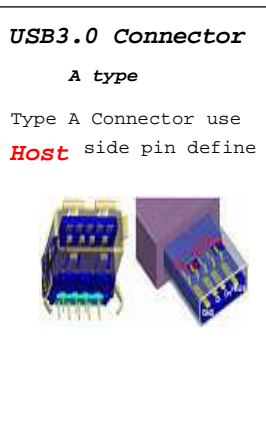
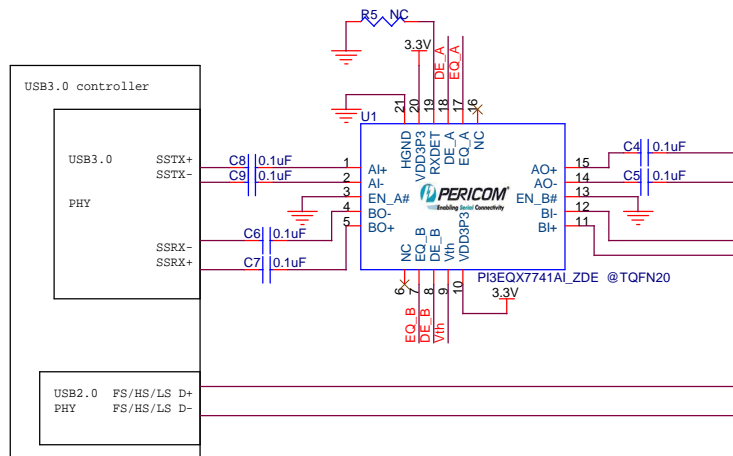
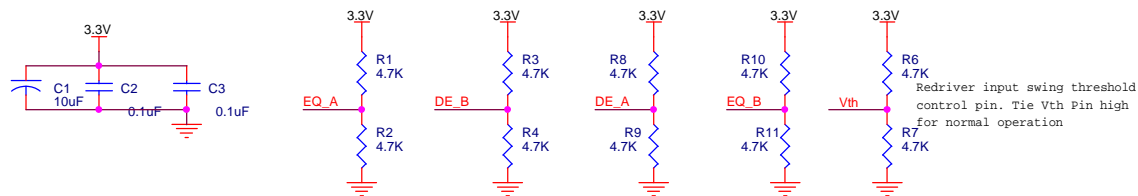
Revision History

Date	History
5/17/2011	First Released
9/8/2011	Update PI3EQX7741"I" app circuit
11/26/2012	Update PI3EQX7741"AI" app circuit Change pin define Vth
01/09/2015	Update Vth pin description by YT

Block Diagram



Title			
<Title>			
Size	Document Number		Rev
A	<Doc>		<RevCode>
Date:	Friday, January 09, 2015		Sheet 1 of 1



NOTE:

Type B Connector use DEVICE side pin define

NOTE:

After PCB layout, de-emphasis, and Equalizer should be fine tune

Output de-emphasis setting		
DE_A/B	De-emphasis	Description
0	0 dB	R3 & R8 NC, R4 & R9 on
open	-3.5 dB	R3 & R8 NC, R4 & R9 NC
1	-6 dB	R3 & R8 on, R4 & R9 NC

Equalizer setting		
EQ_A/B	@2.5GHz	Description
0	3 dB	R1 & R10 NC, R2 & R11 on
open	6 dB	R1 & R10 NC, R2 & R11 NC
1	9 dB	R1 & R10 on, R2 & R11 NC

EN_x#	RxDet	Input R	Output R
1	X	Hi-Z	Hi-Z
0	1	50 ohm / Hi-Z	50 ohm / Hi-Z
0	0	50 ohm	50 ohm

Title		
PI3EQX7741AI reference design draw by PSC Anne		
Size	Document Number	Rev
B	<Doc>	V1.1
Date:	Sheet	of
Friday, January 09, 2015	1	1