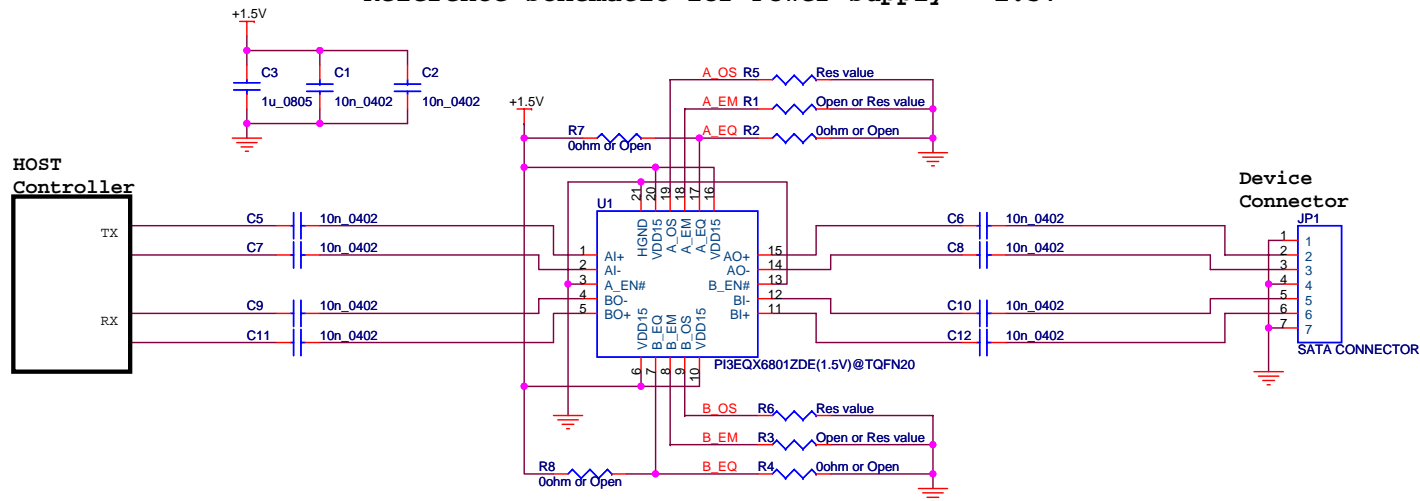


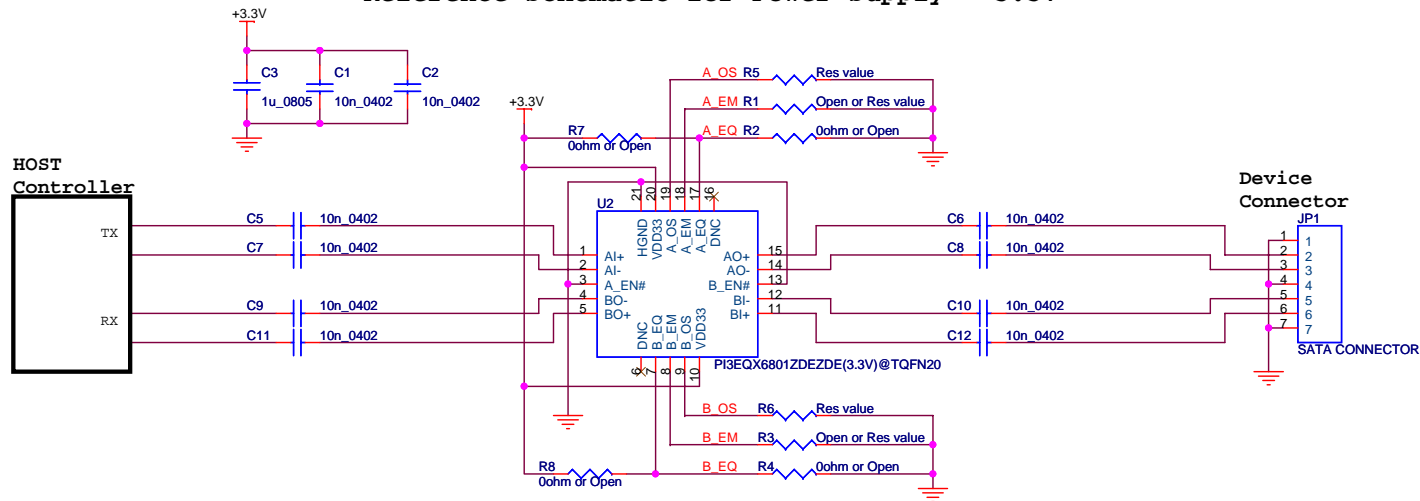
Reference Schematic for Power Supply = 1.5V



PIN CONFIGURATION for CONTROL

PIN NAME	PIN FUNCTION DESCRIPTION	Control Configuration										
A_EN# B_EN#	With Internal 200k-ohm pull-down resistor Low: Normal Operation High: Power Down Mode	For real application, they could be OPEN.										
A_EQ B_EQ	Input Equalization Tri-level Input	Equalization is controled by PIN7&PIN17 <table><tr><td></td><td>Input Equalization for Channel A&B</td></tr><tr><td></td><td>Input Equalization@3.0Gb/s</td></tr><tr><td>0</td><td>8dB(A&B-CH)</td></tr><tr><td>1</td><td>16dB(A&B-CH)</td></tr><tr><td>Vdd/2</td><td>4dB(A&B-CH)</td></tr></table>		Input Equalization for Channel A&B		Input Equalization@3.0Gb/s	0	8dB(A&B-CH)	1	16dB(A&B-CH)	Vdd/2	4dB(A&B-CH)
	Input Equalization for Channel A&B											
	Input Equalization@3.0Gb/s											
0	8dB(A&B-CH)											
1	16dB(A&B-CH)											
Vdd/2	4dB(A&B-CH)											
A_EM B_EM	Output Emphasis Adjustment it is analog resistive adjustment. please refer to the next row table	Emphasis is controlled by PIN4&13, PIN5&12 and PIN6&11 of SW1 for Channel A, PIN4&13, PIN5&12 and PIN6&11 of SW2 for Channel B, <table><tr><td></td><td>Pre-emphasis for Channel A&B</td></tr><tr><td>PIN4&13 is Open</td><td>0dB</td></tr><tr><td>PIN4&13 is short(14k RES)</td><td>+2.0dB</td></tr><tr><td>PIN5&12 is short(10k RES)</td><td>+3.0dB</td></tr><tr><td>PIN6&11 is short(6k RES)</td><td>+4.0dB</td></tr></table>		Pre-emphasis for Channel A&B	PIN4&13 is Open	0dB	PIN4&13 is short(14k RES)	+2.0dB	PIN5&12 is short(10k RES)	+3.0dB	PIN6&11 is short(6k RES)	+4.0dB
	Pre-emphasis for Channel A&B											
PIN4&13 is Open	0dB											
PIN4&13 is short(14k RES)	+2.0dB											
PIN5&12 is short(10k RES)	+3.0dB											
PIN6&11 is short(6k RES)	+4.0dB											
A_OS B_OS	Output Swing Adjustment it is analog resistive adjustment. please refer to the next row table	Swing is controlled by PIN1&16, PIN2&15 and PIN3&14 of SW1 for Channel A, PIN1&16, PIN2&15 and PIN3&14 of SW2 for Channel B, <table><tr><td></td><td>Swing Output for Channel A&B (mV, Vtx-diff-p at 6.0Gb/s)</td></tr><tr><td>PIN1&16 is short(5k RES)</td><td>660</td></tr><tr><td>PIN2&15 is short(4k RES)</td><td>820</td></tr><tr><td>PIN3&14 is short(2k RES)</td><td>1200</td></tr></table>		Swing Output for Channel A&B (mV, Vtx-diff-p at 6.0Gb/s)	PIN1&16 is short(5k RES)	660	PIN2&15 is short(4k RES)	820	PIN3&14 is short(2k RES)	1200		
	Swing Output for Channel A&B (mV, Vtx-diff-p at 6.0Gb/s)											
PIN1&16 is short(5k RES)	660											
PIN2&15 is short(4k RES)	820											
PIN3&14 is short(2k RES)	1200											
PIN6&16 PIN10&20	Voltage PIN	PI3EQX6801ZDE(1.5V)@TQFN20: PIN6&10&16&20=VDD15 (1.5V)										

Reference Schematic for Power Supply = 3.3V



PIN CONFIGURATION for CONTROL

PIN NAME	PIN FUNCTION DESCRIPTION	Control Configuration										
A_EN# B_EN#	With Internal 200k-ohm pull-down resistor Low: Normal Operation High: Power Down Mode	For real application, they could be OPEN.										
A_EQ B_EQ	Input Equalization Tri-level Input	Equalization is controled by PIN7&PIN17 <table><tr><td></td><td>Input Equalization for Channel A&B</td></tr><tr><td></td><td>Input Equalization@3.0Gb/s</td></tr><tr><td>0</td><td>8dB (A&B-CH)</td></tr><tr><td>1</td><td>16dB (A&B-CH)</td></tr><tr><td>Vdd/2</td><td>4dB (A&B-CH)</td></tr></table>		Input Equalization for Channel A&B		Input Equalization@3.0Gb/s	0	8dB (A&B-CH)	1	16dB (A&B-CH)	Vdd/2	4dB (A&B-CH)
	Input Equalization for Channel A&B											
	Input Equalization@3.0Gb/s											
0	8dB (A&B-CH)											
1	16dB (A&B-CH)											
Vdd/2	4dB (A&B-CH)											
A_EM B_EM	Output Emphasis Adjustment it is analog resistive adjustment. please refer to the next row table	Emphasis is controlled by PIN4&13, PIN5&12 and PIN6&11 of SW1 for Channel A, PIN4&13, PIN5&12 and PIN6&11 of SW2 for Channel B, <table><tr><td></td><td>Pre-emphasis for Channel A&B</td></tr><tr><td>PIN4&13 is Open</td><td>0dB</td></tr><tr><td>PIN4&13 is short(14k RES)</td><td>+2.0dB</td></tr><tr><td>PIN5&12 is short(10k RES)</td><td>+3.0dB</td></tr><tr><td>PIN6&11 is short(6k RES)</td><td>+4.0dB</td></tr></table>		Pre-emphasis for Channel A&B	PIN4&13 is Open	0dB	PIN4&13 is short(14k RES)	+2.0dB	PIN5&12 is short(10k RES)	+3.0dB	PIN6&11 is short(6k RES)	+4.0dB
	Pre-emphasis for Channel A&B											
PIN4&13 is Open	0dB											
PIN4&13 is short(14k RES)	+2.0dB											
PIN5&12 is short(10k RES)	+3.0dB											
PIN6&11 is short(6k RES)	+4.0dB											
A_OS B_OS	Output Swing Adjustment it is analog resistive adjustment. please refer to the next row table	Swing is controlled by PIN1&16, PIN2&15 and PIN3&14 of SW1 for Channel A, PIN1&16, PIN2&15 and PIN3&14 of SW2 for Channel B, <table><tr><td></td><td>Swing Output for Channel A&B (mV, Vtx-diff-p at 6.0Gb/s)</td></tr><tr><td>PIN1&16 is short(5k RES)</td><td>660</td></tr><tr><td>PIN2&15 is short(4k RES)</td><td>820</td></tr><tr><td>PIN3&14 is short(2k RES)</td><td>1200</td></tr></table>		Swing Output for Channel A&B (mV, Vtx-diff-p at 6.0Gb/s)	PIN1&16 is short(5k RES)	660	PIN2&15 is short(4k RES)	820	PIN3&14 is short(2k RES)	1200		
	Swing Output for Channel A&B (mV, Vtx-diff-p at 6.0Gb/s)											
PIN1&16 is short(5k RES)	660											
PIN2&15 is short(4k RES)	820											
PIN3&14 is short(2k RES)	1200											
PIN6&16 PIN10&20	Voltage PIN	PI3EQX6801ZDE(3.3V)@TQFN20: PIN10&20=VDD33 (3.3V), PIN6&16=DNC										