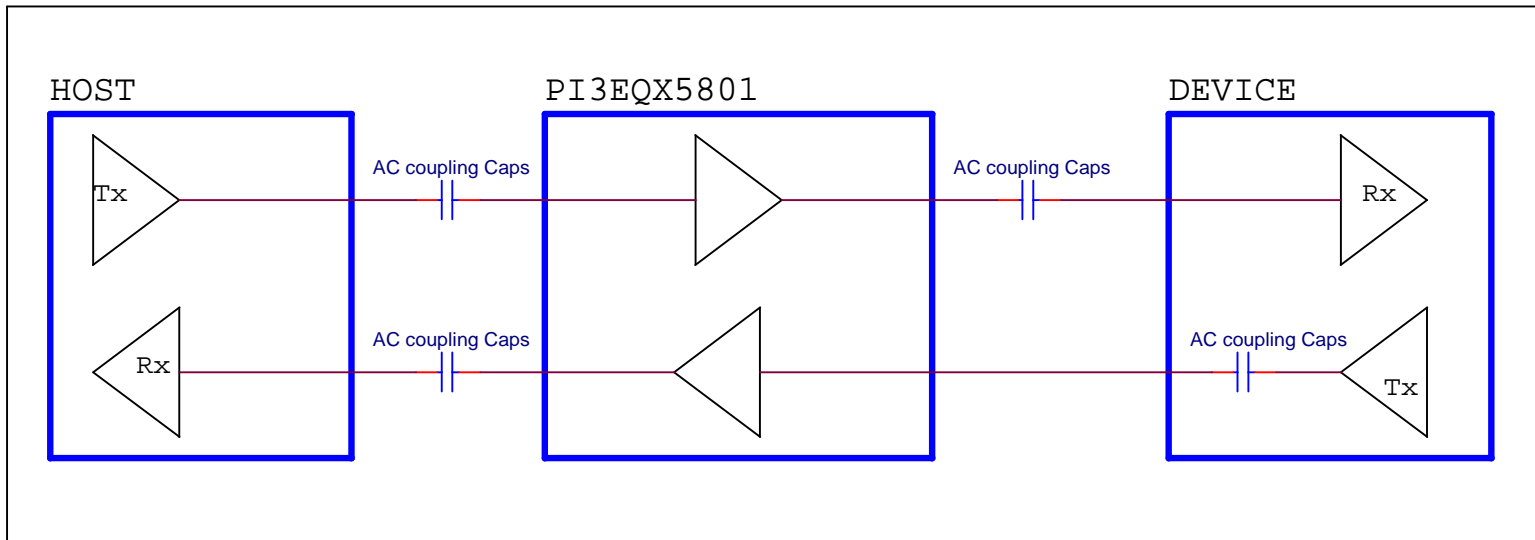


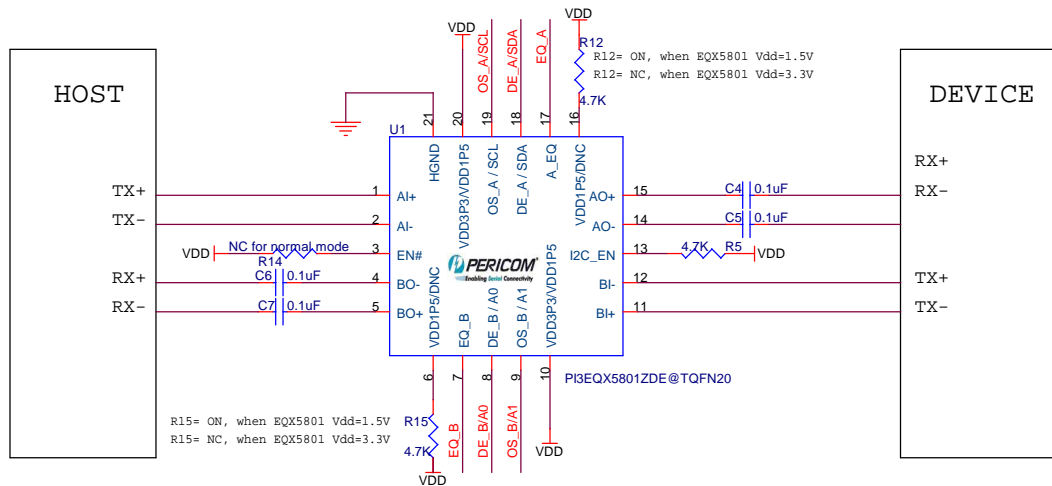
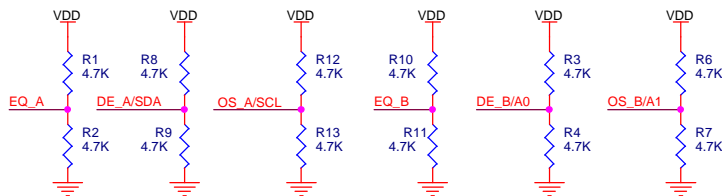
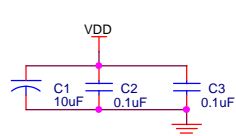
## Revision History

Date	History
2/3/2011	First Released
2/8/2011	Modify description table at pin18(DE_A/SDA) and pin19(OS_A/SCL)
Aug06 2013	datasheet chage I2C define
Aug14 2013	Update pin13 I2C_EN pin name and EQ table since datasheet change Modify EQX5801 pin6 R13 name issue to R15

## Block Diagram



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CONTROL PIN FUNCTION DESCRIPTION

PIN Name	PIN FUNCTION DESCRIPTION	Pin strapping	I2C operator
I2C_EN	I2C Control enable. When the pin is driven "High", chip is in I2C control mode. When the pin is driven "Low", chip is in pin strap control mode. With internal 200K ohm pull-down resistor.	R5 = open(NC)	R5 = on
OS_A/SCL	Set the output swing of Channel A. This is a Tri-level input pins. This pin is also used as Clock Line for I2C programming interface.	R42=open, Tri-level control by R12 & R13	R42 = on R12 =open & R13=open
DE_A/SDA	Set the de-emphasis of output CML buffer for Channel A. These is a Tri-level input pins.This pin is also used as Data Line for I2C programming interface.	R43=open, Tri-level control by R8 & R9	R43 = on R8=open & R9=open
DE_B/A0	Set the de-emphasis of output CML buffer for Channel B. These is a Tri-level input pins.This pin is also used for I2C programming interface. when set to "high", I2C address bit A0 is set to "1". When set to "low"or floating, I2C address bit A0 is set to "0"	Tri-level control by R3 & R4	For I2C Register Address A0 Selection
OS_B/A1	Set the output swing of Channel B. This is a Tri-level input pins. This pin is also used for I2C programming interface. when set to "high", I2C address bit A1 is set to "1". When set to "low"or floating, I2C address bit A1 is set to "0"	Tri-level control by R6 & R7	For I2C Register Address A1 Selection

NOTE:

After PCB layout, de-emphasis, output swing and Equalizer should be fine tune

DE_A/B	@2.5GHz	Description
0	0 dB	R3 & R8 NC, R4 & R9 on
open	-3.5 dB	R3 & R8 NC, R4 & R9 NC
1	-6 dB	R3 & R8 on, R4 & R9 NC

EQ_A/B	@2.5GHz	Description
0	3.3 dB	R1 & R10 NC, R2 & R11 on
open	8.1 dB	R1 & R10 NC, R2 & R11 NC
1	11.7 dB	R1 & R10 on, R2 & R11 NC

OS_A/B	@2.5GHz	Description
0	900mVppd	R6 & R12 NC, R7 & R13 on
open	1000mVppd	R6 & R12 NC, R7 & R13 NC
1	1200mVppd	R6 & R12 on, R7 & R13 NC

EN#	RxDet	Input R	Output R
1	X	Hi-Z	Hi-Z
0	1	50 ohm / Hi-Z	50 ohm / Hi-Z
0	0	50 ohm	50 ohm

Title		
PI3EQ5801 reference design draw by PSC Anne		
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