**Verification of PI3DBS3224 IBIS model**

1. **IBIS Models: PI3DBS3224\_ZF\_33V.ibs**
2. **Introduction:**

To verify the correlation between the ibis model and hspice model, we need to do the following simulation:

PI3DBS3224

**OUT**

R2

**YA**

C

**Output**

IA0

**IN0**

**Input**

R1

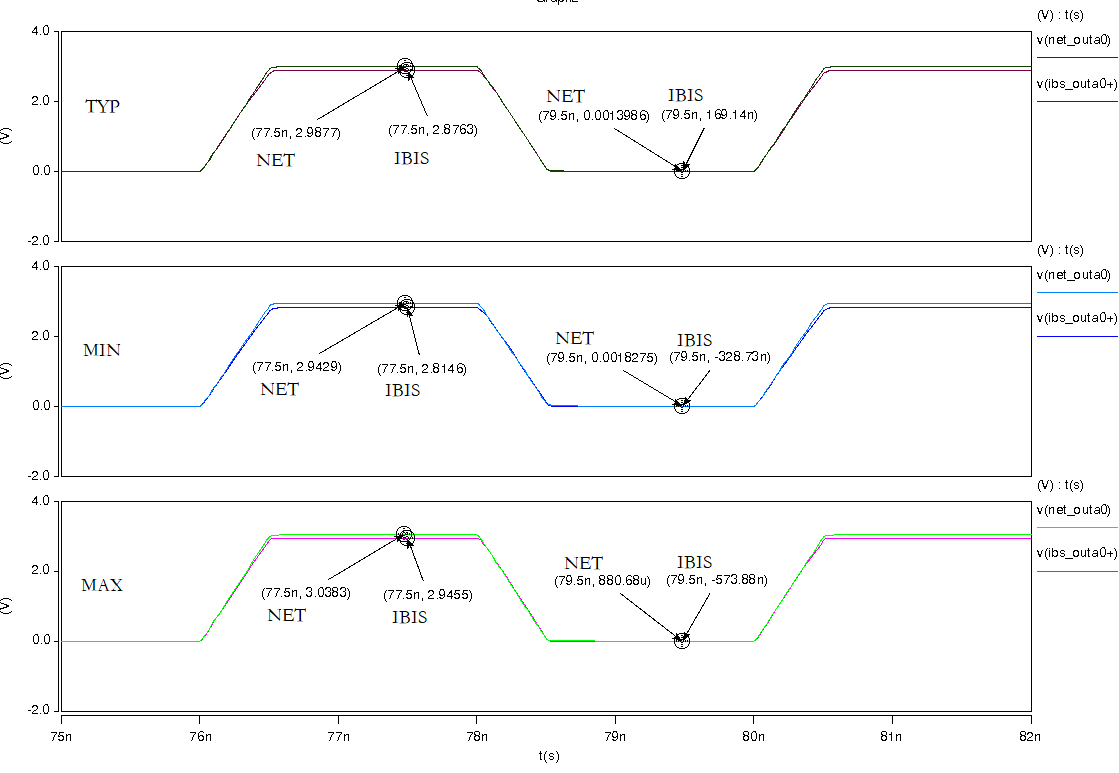
**……**

**……**

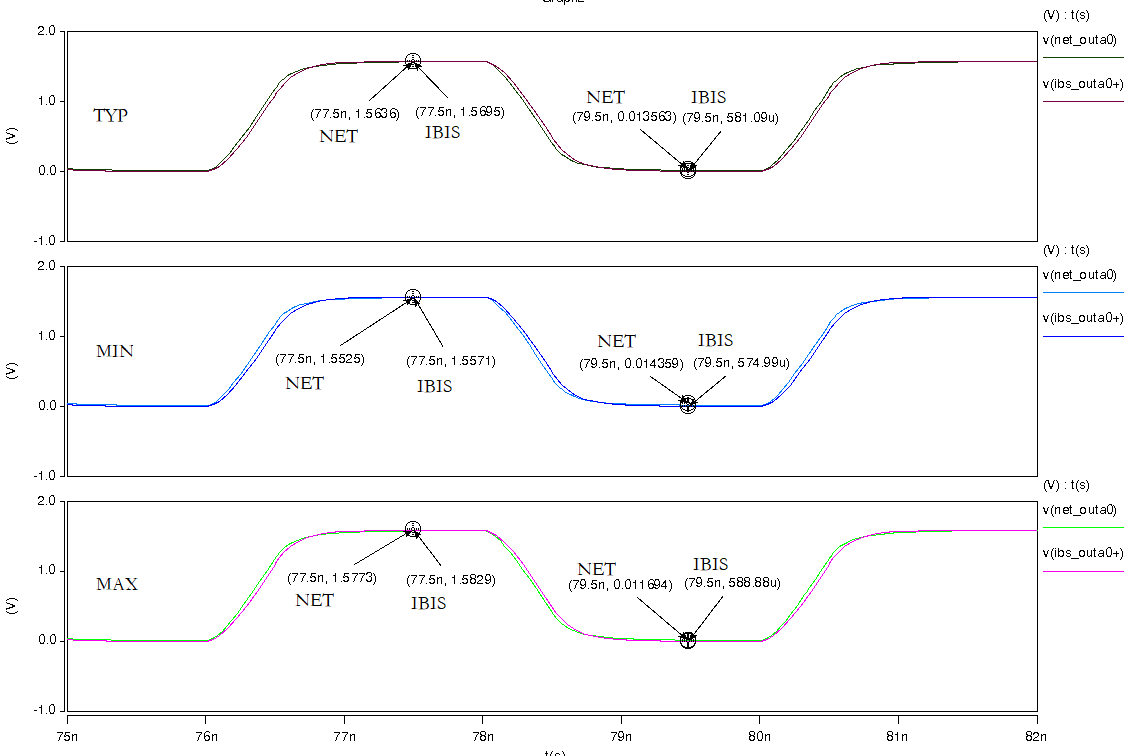
The frequency of signal is **250MHz**:

vin in0 0 pulse ( 0 3.3 0 0.5n 0.5n 1.5n 4n )

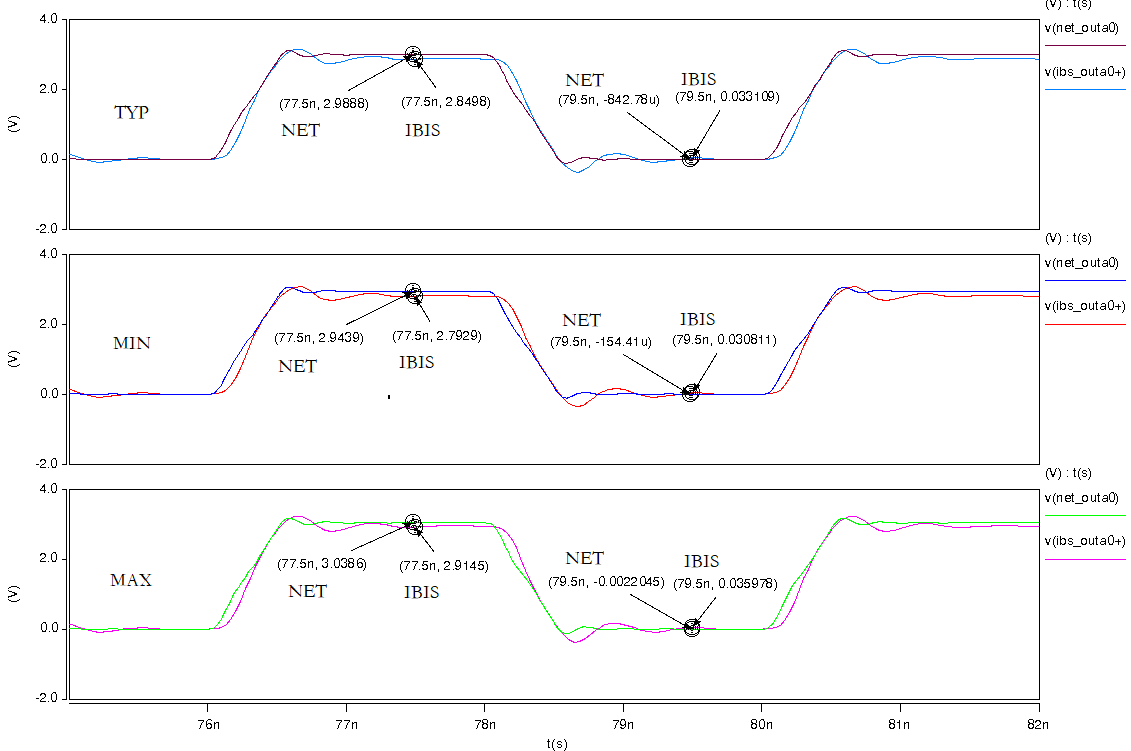
1. **Without** resistor between input signal and YA Pin, and add **50Ω pull-down** resistor and **2pF** pull-down capacitance to OUTPUT;
2. Add **50Ω** resistor between input signal and YA Pin, and add **50Ω pull-down** resistor and **2pF** pull-down capacitance to OUTPUT;
3. **Without** resistor between input signal and YA Pin, and add **50Ω pull-down** resistor and **2pF** pull-down capacitance to OUTPUT, add **package**.
4. **Simulation Result:**
5. Withoutresistor between input signal and YA Pin, and add **50Ω pull-down** resistor and **2pF** pull-down capacitance to OUTPUT;



1. Add **50Ω** resistor between input signal and YA Pin, and add **50Ω pull-down** resistor and **50pF** pull-down capacitance to OUTPUT;



1. **Without** resistor between input signal and YA Pin, and add **50Ω pull-down** resistor and **2pF** pull-down capacitance to OUTPUT, add **package**.



1. **Conclusion:**

For **SWITCH**, the simulation results of IBIS model can match well with the HSPICE model at different load conditions.