



Input Equalizer Configuration for Channel x (x is A or B)

SEL2_x	SEL1_x	SEL0_x	@1.5Ghz	@3.0Ghz
0	0	0	0.8dB	1.5dB
0	0	1	1.0dB	1.9dB
0	1	0	1.5dB	3.2dB
0	1	1	2.5dB	5.2dB
1	0	0	3.5dB	6.9dB
1	0	1	4.4dB	8.3dB
1	1	0	5.9dB	10.4dB
1	1	1	8.7dB	13.8dB

Output Emphasis Configuration for Channel x (x is A or B)

D2_x	D1_x	Emphasis
0	0	0dB
0	1	3.5dB
1	0	5.5dB
1	1	7.5dB

Output Swing Configuration for Channel x (x is A or B)

S1_x	S0_x	Swing(Differential)
0	0	1.1V
0	1	0.5V
1	0	0.8V
1	1	1.0V

These settings just can be selected in register BYTE5-12 by I2C interface

CONTROL PIN FUNCTION DESCRIPTION

PIN Name	PIN FUNCTION DESCRIPTION	Resistor
PD#	Input with internal 100K-Ohm pull-up resistor, High or open is Normal operation, Low is Disable the IC	R4=Open
Mode	Input with internal 100K-Ohm pull-up resistor, High or open is Disable I2C Operation, Low is Enable I2C operation	R10=0ohm
LB#	Input with internal 100K-Ohm pull-up resistor, High or open is Normal Operation, Low is Loopback connection, A_RX to A_TX and B_TX	R6=Open
A0, A1, A4	I2C programmable address bit A0, A1 and A4.	For I2C Register Address Selection
SIG_A, SIG_B	Signal detect output for CH A&B with 100kohm pull up SIG_A&B_High indicates a valid input signal which is > Vth at the differential inputs.	Signal Detect LED Indicator

Resistor Configuration for this Schematic

PERICOM
Enabling Smart Connectivity
Pericom Semiconductor Corporation

Size B	Document Name PI2EQX6874ZFE Reference Application Schematic	Rev A
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