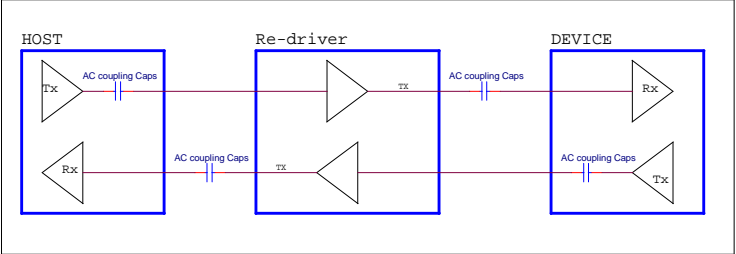
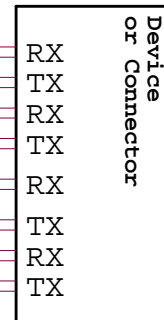
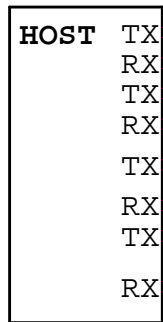
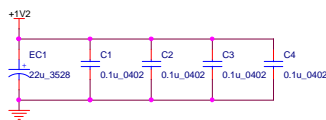


Revision History

Date	History
9/16/2010	First Released
3/02/2011	YT modified,add PD# 51pin

Block Diagram





Input Equaliser Configuration for Channel x (x is A or B)

SEL2_x	SEL1_x	SEL0_x	w1.25Ghz	w2.5Ghz
0	0	0	0.5dB	1.2dB
0	0	1	0.6dB	1.5dB
0	1	0	1.0dB	2.6dB
0	1	1	1.9dB	4.3dB
1	0	0	2.8dB	5.8dB
1	0	1	3.6dB	7.1dB
1	1	0	5.0dB	9.5dB
1	1	1	7.7dB	12.3dB

Output Emphasis Configuration for Channel x (x is A or B)

D0_x	D1_x	D0_x	De-Emphasis
0	0	0	0dB
0	0	1	-2.5dB
0	1	0	-3.5dB
0	1	1	-4.5dB
1	0	0	-5.5dB
1	0	1	-6.5dB
1	1	0	-7.5dB
1	1	1	-8.5dB

Output Swing Configuration for Channel x (x is A or B)

SL_x	SO_x	Swing(Differential)
0	0	1.1V
0	1	0.5V
1	0	0.8V
1	1	1.0V

These settings just can be selected in register BYTE8-9 by I2C interface

CONTROL PIN FUNCTION DESCRIPTION

PIN Name	PIN FUNCTION DESCRIPTION	Resistor Configuration
Mode	Input with internal 100K-Ohm pull-up resistor. High or open is Disable I2C Operation, Low is Enable I2C operation	R10=0ohm
A0, A1, A4	I2C programmable address bit A0, A1 and A4 with internal pull up RES	For I2C Register Address Selection
RESET#	Active low channel reset input for CH_A and CH_B with 100kohm pull-up resistor. RESET# = open indicates to use internal reset function	R (R11=Open)
LB#	Input with internal 100K-Ohm pull-up resistor. High or open is Normal Operation, Low is Loopback connection, A_RX to A_TX and B_TX	R (R6=Open)
RXD_A, RXD_B	Receiver Detect Enable input for CH_A and CH_B with 100kohm pull up	R (R4&R12=Open)
PD#	Power down pin, Active low for power down function, NC or high is P12EQX5964ZFE normal work, with 100k ohm internal pull up	R (R13=Open)

Note: R means the function is controlled by Register at I2C Operation