



PKG. DIMENSIONS(MM)		
SYMBOL	Min	Max
A	-	1.45
A1	0.00	0.15
A2	0.90	1.30
b	0.30	0.50
c	0.08	0.22
D	2.75	3.05
E	1.45	1.75
E1	2.60	3.00
e	0.95 BSC	
e1	1.90 BSC	
L	0.30	0.60
$\theta$	0°	8°

**Note:**

1. Ref: JEDEC MO-178C/AA
2. PACKAGE OUTLINE DIMENSIONS DO NOT INCLUDE MOLD FLASH AND METAL BUR



DATE: 03/29/16

DESCRIPTION: 5-Pin, Small Outline Transistor Plastic Package (SOT23)

PACKAGE CODE: TA (TA5)

DOCUMENT CONTROL #: PD-2144

REVISION: A