

Process Qualification Report

Reliability By Design

Qualification Description:

The information contained herein represents proof of Reliability and Performance of the baseline process technology listed below in accordance with the Qualification Plan and test methods referenced in Section 8.0, after exposure to a variety of environments (electrical, thermal, humidity, etc) and mechanical events that may occur during installation and operational lifetime of the product. Upon conclusion of the testing the product continued to operate within specification limits, demonstrating its capability of reliable operation throughout its lifetime.

The purpose of this report is to present Qualification Test results of the of referenced process technology. The Pericom product data presented in this report qualifies all products manufactured using the exact semiconductor materials and processing techniques used in the baseline process and its off-shoot processes. The report describes the qualification test program, procedures used, criteria enforced (at the time of product validation), and the resulting test data obtained during the Qualification Test. The materials and processing techniques used in the baseline process are incorporated into the off-shoot processes, so the quality/integrity of the baseline and off-shoots (i.e.: 2PxM, 1PxM) processes will be equivalent.

Lot Background Information:

Qual Test Date:	May-2009, updated Feb-2015
Process Technology:	0.13um 1P6M
Foundry & Code:	TSM6A (T)
Qual Test Number:	QDT06002

By Ext. Process:	0.13um 1PxM
Qual Vehicle:	PI7C9X111SLB

Pericom's Qualification Test Results:

Stress Test	Test Procedure	Test Conditions	Duration	# of Lots	Samples per Lot	Results Pass/Fail
Dynamic High Temp	JESD22-A108	1000 hrs 125°C 1.1V	168 hrs	2	57	57 / 0
Operating Life		1000 hrs 125°C 1.1V	500 hrs	2	57	57 / 0
(DHTOL)		1000 hrs 125°C 1.1V	1000 hrs	2	57	57 / 0
Dynamic High Temp	JESD22-A108	1000 hrs 125°C 4.62V 1.4V Core	168 hrs	3	100	100 / 0
Operating Life		1000 hrs 125°C 4.62V 1.4V Core	500 hrs	3	100	100 / 0
(WLR-TSMC)		1000 hrs 125°C 4.62V 1.4V Core	1000 hrs	3	100	100 / 0
ELFR based on 414 units	ELFR (55°C, 0.7 eV, 1.1V, 60% CL)	149.9				
	after 168 hr of DHTOL FIT Rate based on 414 units	FIT Rate (55°C, 0.7 eV, 1.1V, 60% CL)	25.2			
	FIT Rate based on 414 units					
	after 1000 hours of DHTOL	Calculated MTBF (hours)		39,720,189		
Temp Cycle Test	JESD22-A104	Calculated MTBF (hours) -65°C to 150°C, 500 cycles	100 cycles	39,720,189 2	70	70 / 0
Temp Cycle Test	JESD22-A104	Calculated MTBF (hours) -65°C to 150°C, 500 cycles -65°C to 150°C, 500 cycles	100 cycles 500 cycles	39,720,189 2 2	70 70	70 / 0 70 / 0
Temp Cycle Test High Temp Storage	JESD22-A104	Calculated MTBF (hours) -65°C to 150°C, 500 cycles -65°C to 150°C, 500 cycles 1000hrs, 0V, 150°C	100 cycles 500 cycles 168 hrs	39,720,189 2 2 2 2	70 70 70	70 / 0 70 / 0 70 / 0
Temp Cycle Test High Temp Storage (HTS)	JESD22-A103	Calculated MTBF (hours) -65°C to 150°C, 500 cycles -65°C to 150°C, 500 cycles 1000hrs, 0V, 150°C 1000hrs, 0V, 150°C	100 cycles 500 cycles 168 hrs 500 hrs	39,720,189 2 2 2 2 2 2 2 2	70 70 70 70 70	70 / 0 70 / 0 70 / 0 70 / 0
Temp Cycle Test High Temp Storage (HTS)	JESD22-A104 JESD22-A104	Calculated MTBF (hours) -65°C to 150°C, 500 cycles -65°C to 150°C, 500 cycles 1000hrs, 0V, 150°C 1000hrs, 0V, 150°C 1000hrs, 0V, 150°C	100 cycles 500 cycles 168 hrs 500 hrs 1000 hrs	39,720,189 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	70 70 70 70 70 70	70 / 0 70 / 0 70 / 0 70 / 0 70 / 0
Temp Cycle Test High Temp Storage (HTS) Latch Up Test	JESD22-A104 JESD22-A104 JESD22-A103 EIA JESD78	Calculated MTBF (hours) -65°C to 150°C, 500 cycles -65°C to 150°C, 500 cycles 1000hrs, 0V, 150°C 1000hrs, 0V, 150°C 1000hrs, 0V, 150°C Report available by Device	100 cycles 500 cycles 168 hrs 500 hrs 1000 hrs	39,720,189 2 2 2 2 2 2 2 2 2 2 1 1 2 2 2 2 2 2 2	70 70 70 70 70 70	70 / 0 70 / 0 70 / 0 70 / 0 70 / 0

Qualification by Extension Information:

It is valid to use the reliability data of a particular process technology and apply to all products within this process technology family. All parts within the same family are designed to the same rules (layout & electrical), and manufacturing is controlled by SPC. Within a product family, a device can only be fabricated on one process technology option.

If there are any questions about this qualification, please contact Quality Support at: <u>customerquestion@pericom.com</u>

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Date:	May-2009, updated Feb-2015
Subject:	Pericom Process Qualification Report
Mfg-Fab-Process:	TSM6A (T) 0.13um 1P6M
Qual Vehicle:	PI7C9X111SLB

By extension: Pericom active devices using the Fab/Process at the time of the Qualification:

PI7C9X111SLBFDE		
PI7C9X111SLBFDEX		
PI7C9X112SLFDE		
PI7C9X112SLFDEX		
PI7C9X119SLFDEX		
PI7C9X20303SLCFDE		
PI7C9X20303SLCFDEX		
PI7C9X20303ULAZPE		
PI7C9X20303ULAZPEX		
PI7C9X20404GPANBE		
PI7C9X20404GPBNBE		
PI7C9X20404SLCFDE		
PI7C9X20404SLCFDEX		
PI7C9X20505GPANDE		
PI7C9X20505GPBNDE		
PI7C9X20505SLFHEEX		
PI7C9X20508GPANDE		
PI7C9X20508GPBNDE		
PI7C9X20508SLFHEEX		

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