

Description

The AP65201 is a 500kHz switching frequency internal compensated synchronous DC/DC buck converter. It has integrated low $R_{DS(ON)}$ high and low side MOSFETs.

The AP65201 enables continuous load current of up to 2A with efficiency as high as 97%.

The AP65201 implements an automatic custom light load efficiency improvement algorithm.

The AP65201 features current mode control operation, which enables fast transient response times and easy loop stabilization.

The AP65201 simplifies board layout and reduces space requirements with its high level of integration and minimal need for external components, making it ideal for distributed power architectures.

The AP65201 is available in a standard Green TSOT26 package and is RoHS compliant.

- Gaming Consoles
- Flat Screen TV Sets and Monitors
- Set Top Boxes
- Distributed Power Systems
- Green Electronics
- Home Audio
- Consumer Electronics
- Network Systems
- FPGA, DSP and ASIC Supplies

Performance Spec of AP65201WU-EVM (Rev2)

Parameter	Conditions	Performance Value
Input Voltage	Range 4.5V to 16V	12V
Output Current		2A
Output Voltage		3.3V
Transient Response	Peak-to-peak load step from 1A to 2A	100mV _{P-P}
Switching Frequency		500kHz
Efficiency		91%

Figure 1. Evaluation Board (Rev1)



Figure 2. Load Transient 1 to 2A

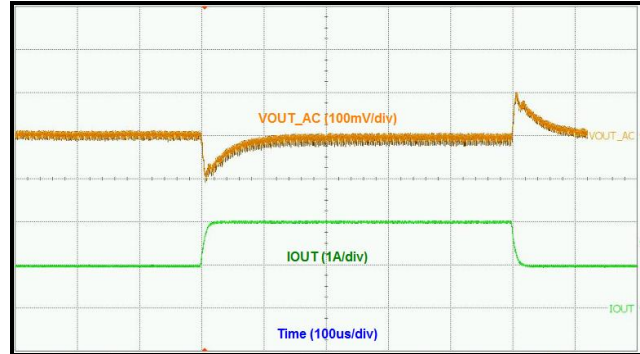


Figure 3. Efficiency (VOUT=3.3V)

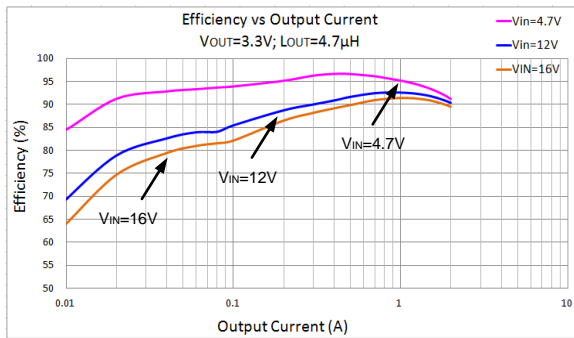
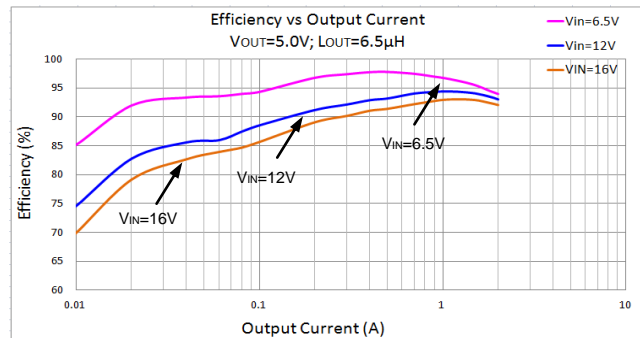
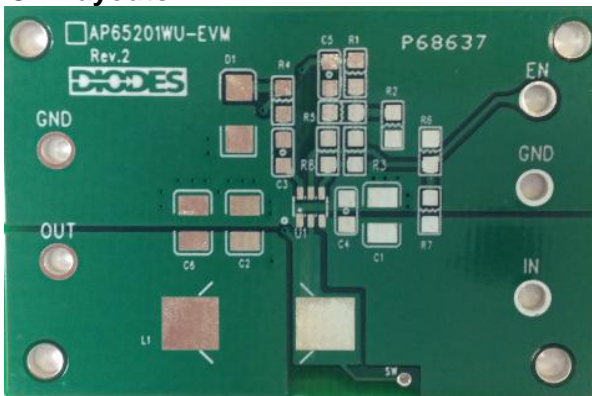


Figure 4. Efficiency (VOUT=5.0V)



PCB Layouts



Top Layer



Bottom Layer

Quick Start Guide

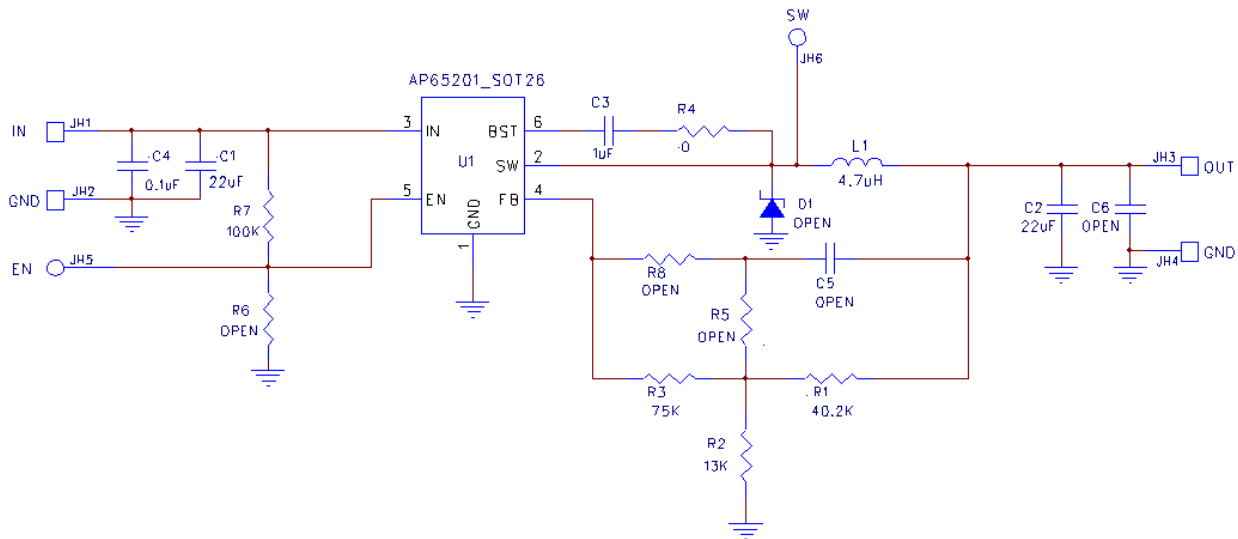
The AP65201WU-EVM has a simple layout and allows access to the appropriate signals through test points. To evaluate the performance of the AP65201, follow the procedure below:

1. Connect a power supply to the input terminals V_{IN} and GND. Set V_{IN} to 12V.
2. Connect the positive terminal of the electronic load to V_{OUT} and negative terminal to GND.
3. EN has a positive voltage through a 100K pull-up to V_{IN} . No supply input is required for EN.
4. The evaluation board should now power up with a 3.3V output voltage.
5. Check for the proper output voltage of 3.3V ($\pm 1\%$) at the output terminals V_{OUT} and GND. Measurement can also be done with a multimeter with the positive and negative leads between V_{OUT} and GND.
6. Set the load to 2A through the electronic load. Check for the stable operation of the SW signal on the oscilloscope. Measure the switching frequency.

Measurement/Performance Guidelines:

- 1) When measuring the output voltage ripple, maintain the shortest possible ground lengths on the oscilloscope probe. Long ground leads can erroneously inject high frequency noise into the measured ripple.
- 2) For efficiency measurements, connect an ammeter in series with the input supply to measure the input current. Connect an electronic load to the output for output current.

EVALUATION BOARD SCHEMATIC



BILL OF MATERIALS

Ref	Value	Description	Qty	Size	Vendor Name	Manufacturer PN
C1, C2	22µF	Ceramic Capacitor, 25V, X5R	1	1210	AVX	12103D226KAT2A
C3	1µF	Ceramic Capacitor, 16V, X7R, 10%	1	0805	Kemet	C0805C105K4RACTU
C4	0.1µF	Ceramic Capacitor, 25V, X7R, 10%	1	0805	Samsung	CL21B104KACNNNC
L1	4.7µH	DCR=11mΩ, Is=8.25A	1	12X12X6 mm	Würth Electronics	744771004
R1	40.2KΩ	Film Resistor, 1%	1	0805	Panasonic	ERJ-6ENF4022V
R2	13KΩ	Film Resistor, 1%	1	0805	Panasonic	ERJ-6ENF1302V
R3	75KΩ	Film Resistor, 1%	1	0805	Panasonic	ERJ-6ENF7502V
R4	0Ω	Film Resistor, 1%	1	0805	Panasonic	ERJ-6GEY0R00V
R7	100KΩ	Film Resistor, 1%	1	0805	Panasonic	ERJ-6ENF1003V
T1	1598	Terminal Turret Triple 0.094" L (Test Points)	5		Keystone Electronics	1598-1
U1		DC/DC converter	1	TSOT26	Diodes	AP65201WU

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