



DESCRIPTION

The AP64351 is 3.5A, synchronous buck converter with a wide input voltage range of 3.8V to 40V. The device fully integrates a $75 m\Omega$ high-side power MOSFET and a $45 m\Omega$ low-side power MOSFET to provide high-efficiency step-down DC-DC conversion.

The AP64351 device is easily used by minimizing the external component count due to its adoption of peak current mode control.

The AP64351 design is optimized for Electromagnetic Interference (EMI) reduction. The device has a proprietary

gate driver scheme to resist switching node ringing without sacrificing MOSFET turn-on and turn-off times, which reduces high-frequency radiated EMI noise caused by MOSFET switching. The AP64351 also features Frequency Spread Spectrum (FSS) with a switching frequency jitter of ±6%, which reduces EMI by not allowing emitted energy to stay in any one frequency for a significant period of time.

The device is available in the SO-8EP package.

FEATURES

- VIN 3.8V to 40V
- 3.5A Continuous Output Current
- 0.8V ±1% Reference Voltage
- 22µA Ultralow Quiescent Current (Pulse Frequency Modulation)
- 570kHz Switching Frequency
- Programmable Soft-Start Time
- Up to 85% Efficiency at 5mA Light Load
- Proprietary Gate Driver Design for Best EMI Reduction

- Frequency Spread Spectrum (FSS) to Reduce EMI
- Low-Dropout (LDO) Mode
- Precision Enable Threshold to Adjust UVLO
- Protection Circuitry
 - Undervoltage Lockout (UVLO)
 - Output Overvoltage Protection (OVP)
 - Cycle-by-Cycle Peak Current Limit
 - o Thermal Shutdown
- Totally Lead-Free & Fully RoHS Compliant
- Halogen and Antimony Free. "Green" Device



FEATURES

- 5V, 12V, and 24V Distributed Power Bus Supplies
- Power Tools and Laser Printers
- White Goods and Small Home Appliances
- Home Audio
- Network Systems
- Consumer Electronics
- General Purpose Point of Load

TYPICAL APPLICATIONS CIRCUIT

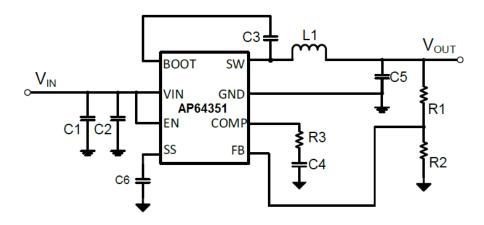


Figure 1. Typical Application Circuit

ABSOLUTE MAXIMUM RATINGS

| Symbol | Parameter | Rating | Unit | |
|-------------------|--------------------------|--|------|--|
| VIN | Supply Pin Voltage | -0.3 to +42.0 (DC) | V | |
| VIIN | Supply Fill Voltage | -0.3 to +45.0 (400ms) | □ | |
| V _{BST} | Bootstrap Pin Voltage | V _{SW} - 0.3 to V _{SW} + 6.0 | V | |
| V _{EN} | Enable/UVLO Pin Voltage | -0.3 to +42.0 | V | |
| V _{SS} | Soft-Start Pin Voltage | -0.3 to +6.0 | V | |
| V_{FB} | Feedback Voltage | -0.3V to +6.0 | V | |
| V _{COMP} | Compensation Pin Voltage | -0.3 to +6.0 | V | |
| V | Switch Node Voltage | -0.3 to VIN + 0.3 (DC) | V | |
| V_{sw} | Switch Node Voltage | -2.5 to VIN + 2.0 (20ns) | | |
| T _J | Junction Temperature | +160 | °C | |
| TL | Lead Temperature | +260 | °C | |



RECOMMENDED OPERATING CONDITIONS

| Symbol | Parameter | Min | Max | Unit |
|----------------|--------------------------------------|-----|------|------|
| VIN | Supply Voltage | 3.8 | 40 | V |
| VOUT | Output Voltage | 0.8 | 39 | V |
| T _A | Operating Ambient Temperature Range | -40 | +85 | °C |
| TJ | Operating Junction Temperature Range | -40 | +125 | °C |

EVALUATION BOARD

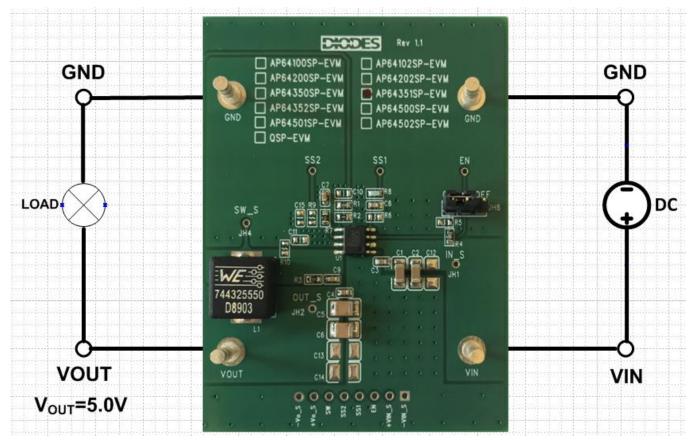


Figure 2. AP64351SP-EVM



QUICK START GUIDE

The AP64351SP-EVM has a simple layout and allows access to the appropriate signals through test points. To evaluate the performance of the AP64351SP, follow the procedure below:

- 1. Connect a power supply to the input terminals VIN and GND. Set VIN to 12V.
- 2. Connect the positive terminal of the electronic load to VOUT and negative terminal to GND.
- 3. For Enable, place a jumper at JH6 to "ON" position to connect EN pin to VIN through 100KΩ resistor to enable IC. Jump to "OFF" position to disable IC.
- 4. The evaluation board should now power up with a 5.0V output voltage.
- Check for the proper output voltage of 5.0V (±1%) at the output terminals VOUT and GND. Measurement can also be done with a multimeter with the positive and negative leads between VOUT and GND.
- 6. Set the load to 3.5A through the electronic load. Check for the stable operation of the SW signal on the oscilloscope. Measure the switching frequency.

MEASUREMENT/PERFORMANCE GUIDELINES:

- 1) When measuring the output voltage ripple, maintain the shortest possible ground lengths on the oscilloscope probe. Long ground leads can erroneously inject high frequency noise into the measured ripple.
- 2) For efficiency measurements, connect an ammeter in series with the input supply to measure the input current. Connect an electronic load to the output for output current.

SETTING OUTPUT VOLTAGE:

Table 1 shows a list of recommended component selections for common output voltages.

| VOUT | R1 | R2 | L1 | R7 | C 7 | C1, C2 | C5, C6 |
|------|--------|------|-------|--------|------------|--------|--------|
| 1.2V | 4.99ΚΩ | 10ΚΩ | 3.3µH | 3.32ΚΩ | 3.3nF | 2x10μF | 2x22µF |
| 1.5V | 8.66ΚΩ | 10ΚΩ | 3.3µH | 4.22ΚΩ | 3.3nF | 2x10μF | 2x22µF |
| 1.8V | 12.4ΚΩ | 10ΚΩ | 3.3µH | 4.99ΚΩ | 3.3nF | 2x10μF | 2x22µF |
| 2.5V | 21.5ΚΩ | 10ΚΩ | 4.7µH | 6.98ΚΩ | 3.3nF | 2x10μF | 2x22µF |
| 3.3V | 31.6ΚΩ | 10ΚΩ | 4.7µH | 9.31ΚΩ | 3.3nF | 2x10μF | 2x22µF |
| 5.0V | 52.3ΚΩ | 10ΚΩ | 5.5µH | 14ΚΩ | 3.3nF | 2x10μF | 2x22µF |
| 12V | 140ΚΩ | 10ΚΩ | 10μH | 33.2ΚΩ | 3.3nF | 2x10μF | 2x22µF |

Table 1. Common Output Voltages



EVALUATION BOARD SCHEMATIC

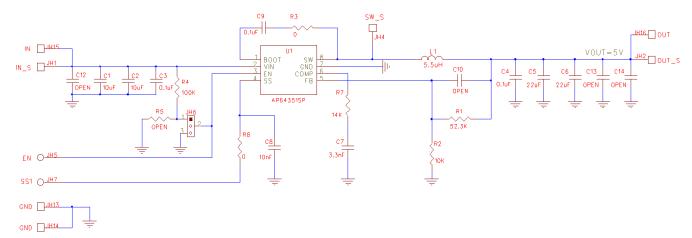


Figure 3. AP64351SP-EVM Schematic

PCB TOP LAYOUT

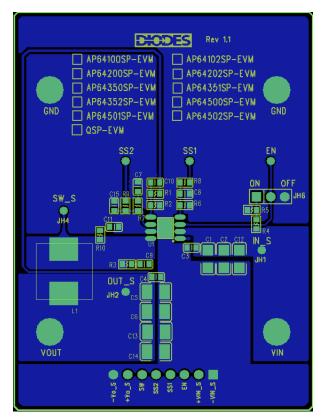


Figure 4. AP64351SP-EVM - Top Layer



PCB BOTTOM LAYOUT

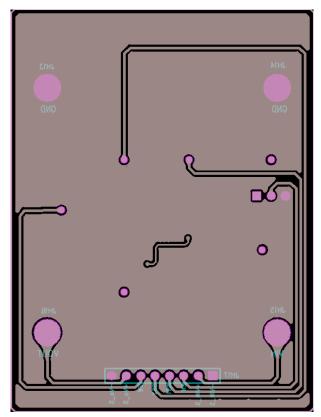


Figure 5. AP64351SP-EVM - Bottom Layer



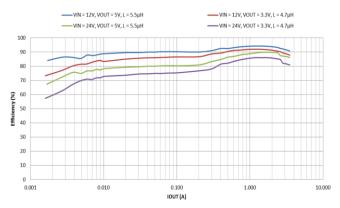


BILL OF MATERIALS for AP64351SP-EVM for Vout=5V

| Ref | Value | Description | Qty | Size | Vendor Name | Manufacturer PN | PCB Layer |
|---------------------|--------|---|-----|-----------------------|------------------------------------|--------------------|--------------|
| C1, C2 | 10µF | Ceramic Capacitor, 50V, X7R, 10% | 2 | 1206 | Samsung | CL31B106KBHNNNE | Тор |
| 01, 02 | ΤΟμί | Ceramic Capacitor, | | 1200 | Wurth | CECIBIOORDINANE | ТОР |
| C3, C4, C9 | 0.1µF | 50V, X7R, 10% | 3 | 0603 | Electronics | 885012206095 | Тор |
| C5, C6 | 22µF | Ceramic Capacitor, 16V, X7R | 2 | 1210 | Samsung | CL32B226KOJNNNE | Тор |
| C7 | 3.3nF | Ceramic Capacitor, 25V, X7R | 1 | 0603 | Wurth Electronics | 885012206062 | Тор |
| C8 | 10nF | Ceramic Capacitor, 25V, X7R | 1 | 0603 | Wurth Electronics | 885012206065 | Тор |
| R1 | 52.3ΚΩ | SMD Resistor, 1% | 1 | 0603 | Panasonic | ERJ-3EKF5232V | Тор |
| R2 | 10ΚΩ | SMD Resistor, 1% | 1 | 0603 | Panasonic | ERJ-3EKF1002V | Тор |
| R3 | 0Ω | RES SMD 1% 1/10W RES SMD 1% | 1 | 0603 | Vishay | CRCW06030000Z0EAC | Тор |
| R4 | 100ΚΩ | 1/10W | 1 | 0603 | Yageo | RC0603FR-07100KL | Тор |
| R7 | 14ΚΩ | RES SMD 1% 1/10W | 1 | 0603 | Bourns Inc | CR0603-FX-1402ELF | Тор |
| R8 | 0Ω | RES SMD 1% 1/10W | 1 | 0603 | Vishay | MCT06030Z0000ZP500 | Тор |
| L1 | 5.5µH | DCR=10.3mΩ, Ir=10A | 1 | 10.2x 10.2x 5mm | Wurth Electronics | 744325550 | Тор |
| JH6 | | PCB Header, 40 POS | 1 | 1X3 | 3M | 2340-6111TG | Тор |
| VIN, VOUT, GNDx2 | 1598 | Terminal Turret Triple 0.094" L (Test Points) | 4 | Through- Hole | Keystone Electronics | 1598-2 | Тор |
| U1 | AP6435 | Sync Buck DC-DC Converter | 1 | SO-8EP | Diodes Incorporated (Diodes) | AP64351SP | Тор |



TYPICAL PERFORMANCE CHARACTERISTICS



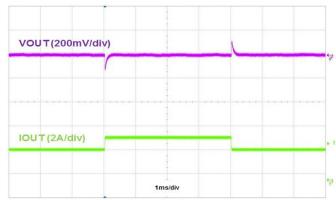
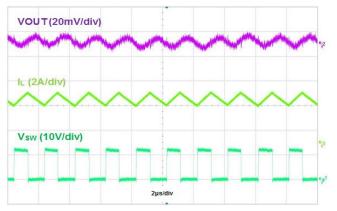


Figure 6. Efficiency vs Output Current

Figure 7. Load Transient 2A to 3.5A





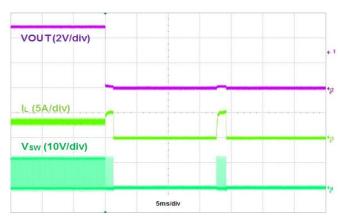


Figure 9. Output Short Protection, IOUT=3.5A

AP64351SP-EVM



3.8V TO 40V INPUT, 3.5A LOW IQ SYNCHRONOUS BUCK WITH PROGRAMMABLE SOFT-START TIME

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