



SDM1A30CSP

1A SCHOTTKY BARRIER RECTIFER CHIP SCALE PACKAGE

Product Summary

V _{RRM} (V)	lo (A)	VF MAX (mV)	Ir max (μA)
30	1	525	100

Description

The DIODES SDM1A30CSP is a 30V 1A Schottky barrier rectifier that is optimized for low forward voltage drop and low-leakage current, housed in a compact chip scale package (CSP) that occupies only 0.6mm² board space. The low thermal resistance enables designers to meet design challenges of increasing efficiency while also reducing board space.

Applications

The SDM1A30CSP are ideally suited for use in portable applications as:

- Blocking diodes
- Boost diodes
- Switching diodes
- · Reverse protection diodes

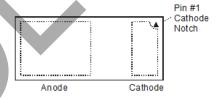
Features and Benefits

- Off Board Profile of 0.275mm More than 30% Thinner than DFN1006
- Low Forward Voltage (V_F) Minimizes Conduction Losses and Improves Efficiency
- Reduced High Temperature Reverse Leakage; Increased Reliability Against Thermal Runaway Failure in High Temperature Operation
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: X3-WLB1006-2
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: NiAu Bump. Solderable per MIL-STD-202, Method 208 (4)
- Polarity: Cathode Dot
- Weight: 0.001 grams (Approximate)





Ordering Information (Note 4)

Part Number	Peakers	Packing	
Part Number	Package	Qty.	Carrier
SDM1A30CSP-7	X3-WLB1006-2	5000	Reel

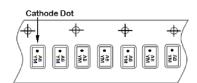
Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3, Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



XH = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: K = 2023) M = Month (ex: 9 = September) Dot Denotes Cathode Pin



Date Code Key

Year	2018	-	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032
Code	F	-	K	L	М	Ν	0	Р	R	S	Т	U
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	VRRM	30	V
Average Rectified Output Current	lo	1	Α
Repetitive Peak Forward Current (Pulse Wave = 1ms, Duty Cycle = 25%)	I _{FRM}	4	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	IFSM	15	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 5)	Reja	135	°C/W
Operating and Storage Temperature Range	TJ, Tsтg	-55 to +150	°C

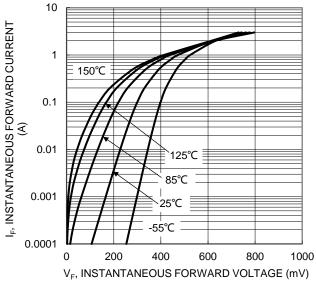
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
		_	395	440		IF = 0.5A, T _J = +25°C
Forward Voltage Drop	VF		475	525	mV	IF = 1.0A, T _J = +25°C
			425			IF = 1.0A, T _J = +125°C
		_	6	20		V _R = 10V, T _J = +25°C
Leakage Current (Note 6)	IR	_	20	100	μΑ	V _R = 30V, T _J = +25°C
		_	8	_	mA	V _R = 30V, T _J = +125°C
Junction Capacitance	Ст		40	_	pF	V _R = 4V, f = 1.0MHz

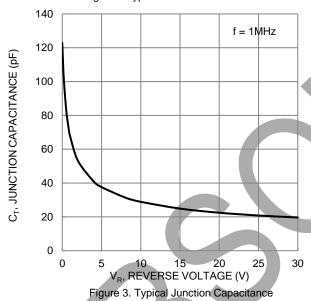
Notes:

- 5. Device mounted on FR-4 PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html. 6. Short duration pulse test used to minimize self-heating effect.









100000 150°C 10000 125℃ I_R, LEAKAGE CURRENT (μA) 1000 85°C 100 10 . 25℃ 0.1 -55℃ 0.01 0.001 10 15 25 30 V_R, REVERSE VOLTAGE (V)

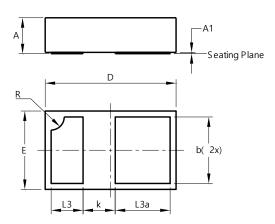
Figure 2. Typical Reverse Characteristics



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

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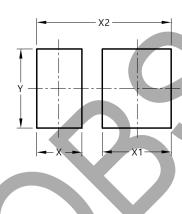


X3-WLB1006-2					
Dim	Min	Max	Тур		
A	0.25	0.30	0.275		
A1	0.00	0.01	-		
b	0.450	0.550	0.500		
D	0.95	1.05	1.000		
Б	0.55	0.65	0.600		
k	-	V-7	0.288		
L3	0.194	0.294	0.244		
L3a	0.350	0.450	0.400		
R	-	-	0.100		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X3-WLB1006-2



Dimensions	Value
Dimensions	(in mm)
Х	0.332
X1	0.507
X2	0.989
Υ	0.579



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