

NOT RECOMMENDED FOR NEW DESIGN USE SBR12U45LH1



SBR12E45LH1

12A SBR SUPER BARRIER RECTIFIER PowerDI5SP

Product Summary

V _{RRM} (V)	I _O	V _{F(TYP)} @ +125°C	I _{R(MAX)} @ V _{RRM}
	(A)	(V)	(mA)
45	12	0.40	0.3

Description

The SBR12E45LH1 uses SBR[®] patented technology that offers ultralow V_F to reduce forward power loss and improve efficiency. Encapsulated in the new PowerDl[®]5SP (Type B) package with a 0.75mm low height profile and protruding leads for easy soldering, it is especially suited for use as a bypass diode in solar panels.

Applications

Solar bypass diodes

Features

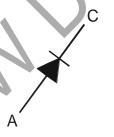
- Designed as bypass diodes for solar panels
- Low profile height (0.75mm) and 7.6mm protruding leads, enabling the package to be integrated within the solar glass panel
- Selectively rated for +200°C maximum junction temperature for high thermal reliability and excellent high temperature stability
- Patented Super Barrier Rectifier SBR technology
- Ultra low forward voltage drop to minimize forward power losses
- Very low reverse leakage to ensures maximum efficiency of solar panel
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: PowerDI5SP
- Package Material: Molded Plastic, UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Lead-Free Plating (Matte Tin Finish). Solderable per MIL-STD-202, Method 208 (23)
- Polarity: Cathode Bar Mark on Top and Cathode Notch on Lead
- Weight: 0.199 grams (Approximate)







Pin Configuration

Ordering Information (Note 4)

Part Number	Package	Packing		
Fart Number	Fackage	Qty.	Carrier	
SBR12E45LH1-13	PowerDI5SP (Type B)	3000	Tape & Reel	

Notes: 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/. Device is packed with marking code side down to the pocket of 32mm carrier tape and carrier tape is wound with device facing inside of reel.

Marking Information





Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	Vrrm Vrwm Vrm	45	٧
Average Rectified Output Current	O	12	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	IFSM	300	А

Thermal Characteristics

Characteristic		Symbol	Value	Unit	
Typical Thermal Resistance Junction to Ambient (Note 5)		$R_{ heta JA}$	66	°C/W	
Operating Temperature Range	V _R ≤ 80% V _{RRM}	Tu	-65 to +150	°C	
	DC Forward Mode (Note 6)	1.0	≤ 200	C	
Storage Temperature Range		Тѕтс	-55 to +175	°C	

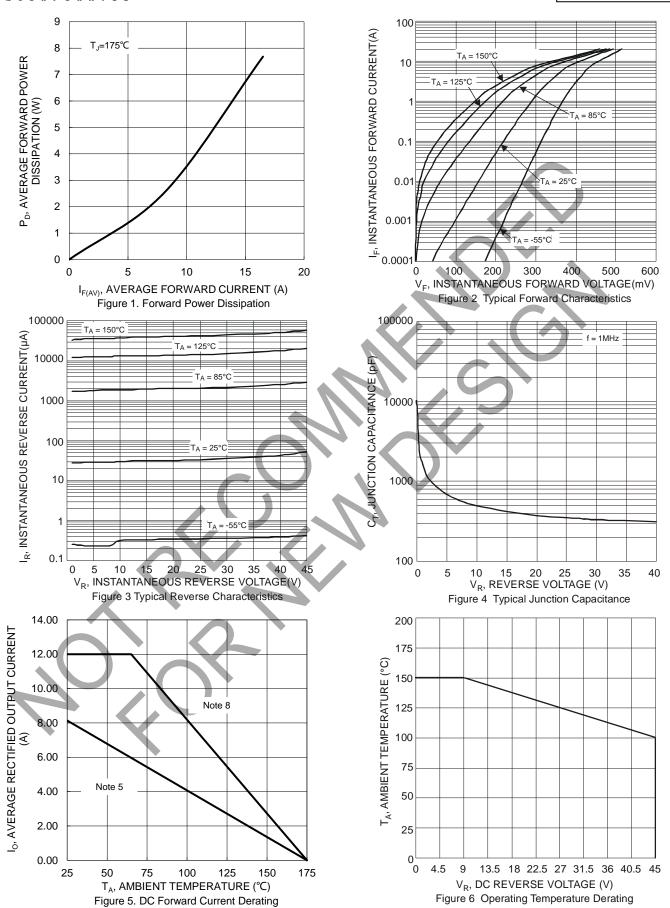
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characte	ristic	Symbol	Min	Тур	Max	Unit	Test Condition
	VF			0.42	0.50		I _F = 10A, T _J = +25°C
Forward Voltage Drop		VF	 -	0.44	0.52		I _F = 12A, T _J = +25°C
			ļ	0.40	0.47		I _F = 12A, T _J = +125°C
	IR		1	35	200	uA	V _R = 40V, T _J = +25°C
Leakage Current (Note 7)		I-	1	40	300		V _R = 45V, T _J = +25°C
Leakage Current (Note 1)		IR		15	_	mA	V _R = 45V, T _J = +125°C
			4	40	_	IIIA	V _R = 45V, T _J = +150°C

Notes:

- 5. FR-4 PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.
 6. Max junction temperature +200°C guaranteed for 2 hours at maximum output.
 7. Short duration pulse test used to minimize self-heating effect.





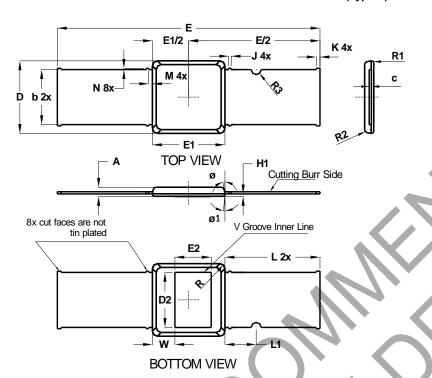
Note: 8. Device mounted on FR-4 substrate PCB with 10cm*10cm double-sided copper pad.



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5SP (Type B)

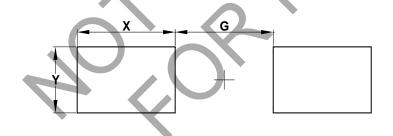


PowerDI5SP					
(Type B) Dim Min Max Typ					
A	- 141111	0.75	קעי		
b	4.30	4.50	4.40		
C	0.155	0.191	7.10		
D	5.70	5.90	5.80		
D2	4.40	-	-		
E	20.8	21.2	21.0		
E1	5.70	5.90	5.80		
E2	2.90	_	_		
H1	0.19	0.21	0.20		
7	- 4	_	0.20		
K	_	-	0.30		
L	-		7.60		
L1	- /		2.50		
М	-	/ -	0.30		
N	0	0.20	-		
R	-	_	0.40		
R1			0.15		
R2	_	_	0.25		
R3	-	_	0.40		
W	1.63	1.97	1.80		
Ø	8º	12º	-		
Ø1	3º	7º	-		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5SP (Type B)



Dimensions	Value (in mm)		
G	8.101		
X	8.100		
Y	5.100		



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