

PART OBSOLETE - USE [APX803L](#)

PT7M6418-6450 CL/CH/NL

Low Power Voltage Detector

Features

- High accuracy: $\pm 2\%$ (25°C)
- Low power consumption: 0.6 μ A @ 3V V_{CC}
- Detecting voltage range: 1.8 to 5V in 100mV increments
- Operating voltage range: 1.2V ~ 5.5V
- Operating temperature range: -40°C to +85°C
- Detecting voltage accuracy over temperature: $\pm 2.5\% \times$ TYP
- Output configuration: N-channel open drain or CMOS
- Reset timeout period at least 120ms

Description

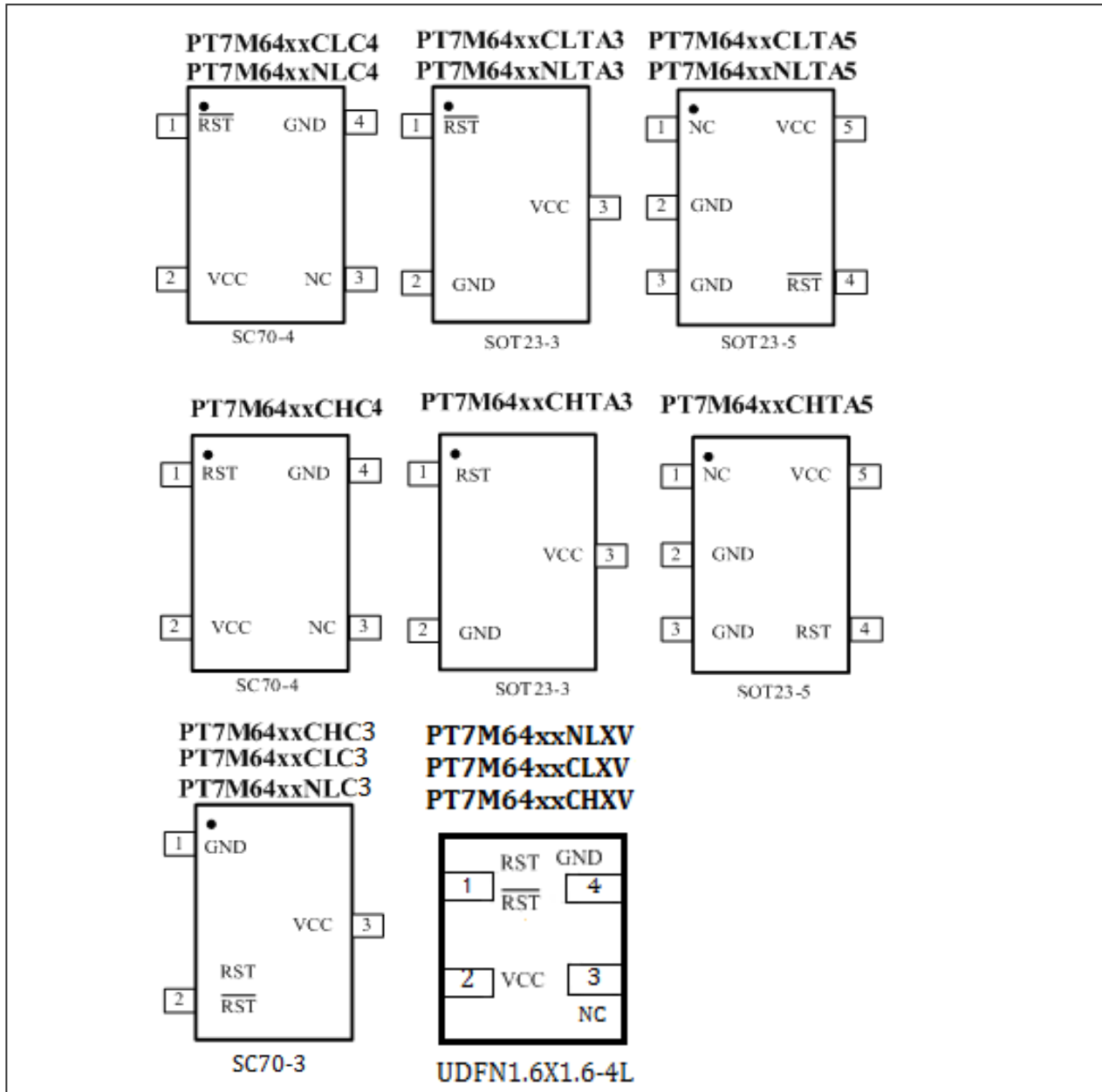
The PT7M64xx series are μ P supervisory circuits with a minimum reset timeout period of 120ms. Each circuit includes a precise bandgap reference; a comparator, a reset timeout circuit, internally trimmed resistor networks that set specified trip thresholds, and an internal 5% threshold hysteresis circuit (see the *Block Diagram*). Output is asserted when V_{CC} falls below the internal V_{TH} and remains asserted until V_{CC} rises above V_{TH+} (V_{TH+} = V_{TH} × 1.05) after a reset timeout period. These devices provide excellent circuit reliability and low cost by eliminating external components and adjustments when monitoring normal systems voltage from +1.8V to +5V in 100mV increments. The series are voltage detectors with a propagation delay of 35 μ s.

The family is available with four output stage options: push-pull with active-low output, push-pull with active-high output, open drain with active-low output and bidirection port with active-low output and pushbutton reset input. These devices specified over the -40°C to +85°C temperature range.

Table 1. Function comparison

Item	Part No.	Reset Output				Reset bi-direction	Threshold
		Open-Drain		Push-Pull			
		Active high	Active low	Active high	Active low		
1	PT7M64xxCL	-	-	-	√	-	1.8V to 5.0V in 100mV increments
2	PT7M64xxCH	-	-	√	-	-	
3	PT7M64xxNL	-	√	-	-	-	

Pin Configuration

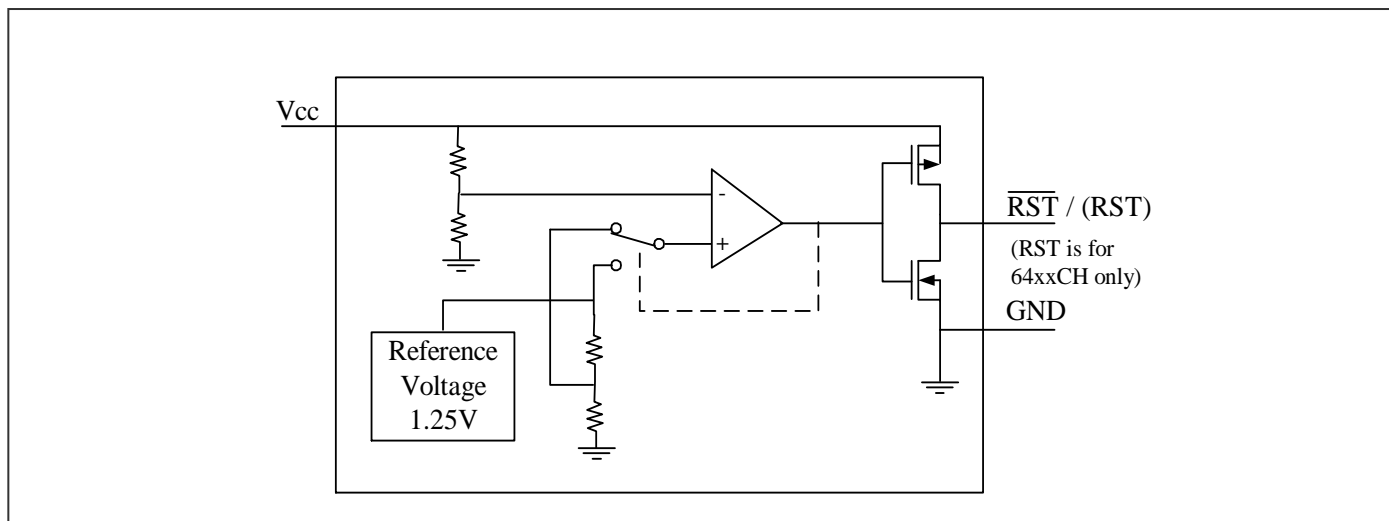


Pin Description

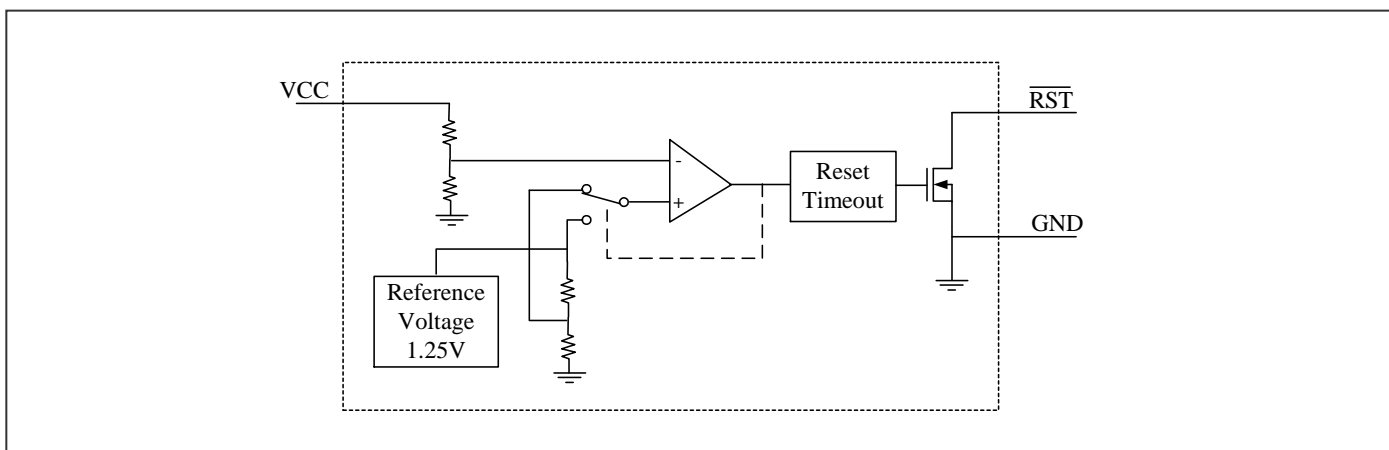
Name	Type	Description
$\overline{\text{RST}}$	O	Reset Output (PT7M64xxCL/NL/BL/NLL): $\overline{\text{RST}}$ is asserted when V_{CC} drops below voltage threshold V_{TH} : Active low. For PT7M64xxBL, RST is also pushbutton reset input
RST	O	Reset Output (PT7M64xxCH): RST is asserted when V_{CC} drops below voltage threshold V_{TH} : Active high.
GND	P	Ground
V_{CC}	P	Supply Voltage

Block Diagram

Block Diagram of PT7M64xxCL/CH



Block Diagram of PT7M64xxNL





Maximum Ratings

Storage Temperature	-65°C to +150°C
Ambient Temperature with Power Applied	-40°C to +85°C
Supply Voltage to Ground Potential (V _{CC} to GND)	-0.3V to +6.0V
DC Input Voltage (All inputs except V _{CC} and GND).....	-0.3V to V _{CC} +0.3V
DC Output Current (All outputs)	20mA
ESD(HBM) for 64xxNL/NLL.....	1KV
ESD(HBM) for 64xxCL/CH/BL.....	2KV
Power Dissipation	320mW
	(Depend on package)

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics

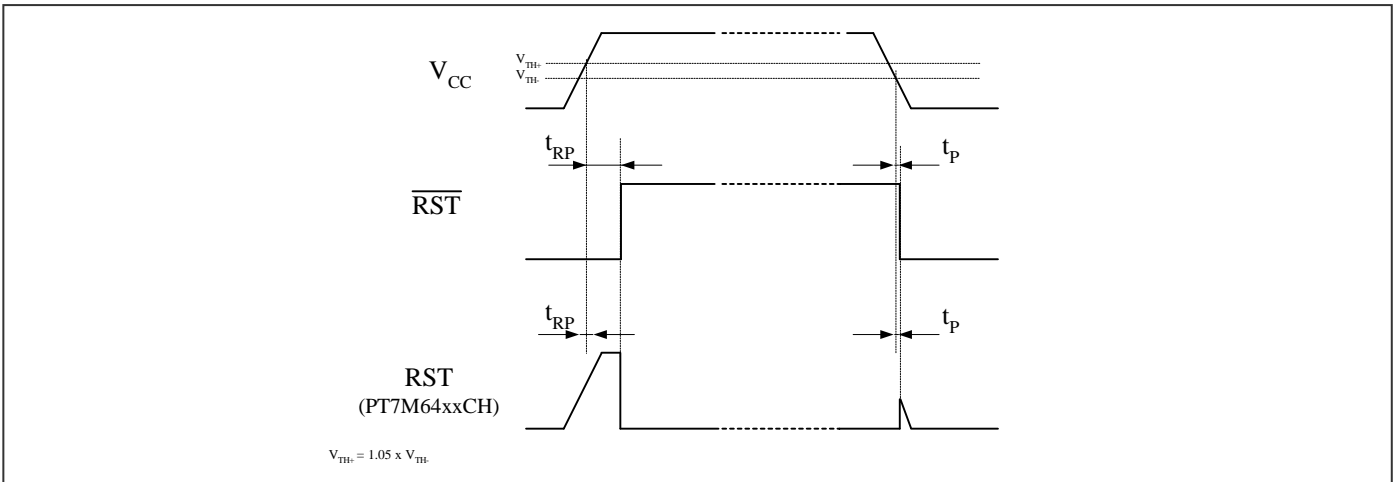
(V_{CC} = 1.2V to 5.5V, T_A = -40~85°C, unless otherwise noted. Typical values are at T_A = +25°C)

Description		Sym	Test Conditions	Min	Typ	Max	Unit
Supply Voltage		V _{CC}	T _A = 0~70°C	1.0	-	5.5	V
			T _A = -40~85°C	1.2	-	5.5	
Supply Current		I _{CC}	V _{CC} = 3V. No load.	-	0.6	1.2	μA
			V _{CC} = 5V. No load.	-	0.9	2.0	μA
Output Driving	Output high (Push-Pull only)	V _{OH}	V _{CC} ≥ 1.8V, I _{source} = 1mA	0.8×V _{CC}	-	-	V
			V _{CC} ≥ 2.5V, I _{source} = 3mA	0.8×V _{CC}	-	-	
			V _{CC} ≥ 4.5V, I _{source} = 8mA	0.8×V _{CC}	-	-	
	Output low	V _{OL}	V _{CC} ≥ 1.2V, I _{sink} = 1mA	-	-	0.3	V
			V _{CC} ≥ 2.5V, I _{sink} = 4mA	-	-	0.3	
			V _{CC} ≥ 4.5V, I _{sink} = 9mA	-	-	0.4	
Open-Drain Output Leakage Current		I _{LKG}	-	-	-	1	μA
Voltage Threshold*		V _{TH-}	+25°C	(V _{TH-}) ×0.985	V _{TH-}	(V _{TH-}) ×1.015	V
			-40°C~85°C	(V _{TH-}) ×0.975	V _{TH-}	(V _{TH-}) ×1.025	
		V _{TH+}	+25°C	(V _{TH+}) ×0.985	V _{TH+}	(V _{TH+}) ×1.015	
			-40°C~85°C	(V _{TH+}) ×0.975	V _{TH+}	(V _{TH+}) ×1.025	
Voltage threshold Hysteresis		V _{HYST}	V _{HYST} = [(V _{TH+})-(V _{TH-})]/(V _{TH-}) ×100%	3	4.5	6	%
Pushbutton Detect		P _{BDV}	-40°C~85°C, V _{CC} = 5V	0.7	-	1.2	V
Internal Pull-Up Resistor		R _P	-	3.75	5	6.25	kΩ

* V_{TH+} = 1.05 × V_{TH-}. V_{TH-} is V_{CC} dropping from high to low voltage. V_{TH+} is V_{CC} rising from low to high voltage.

AC Electrical Characteristics

Timing Diagram

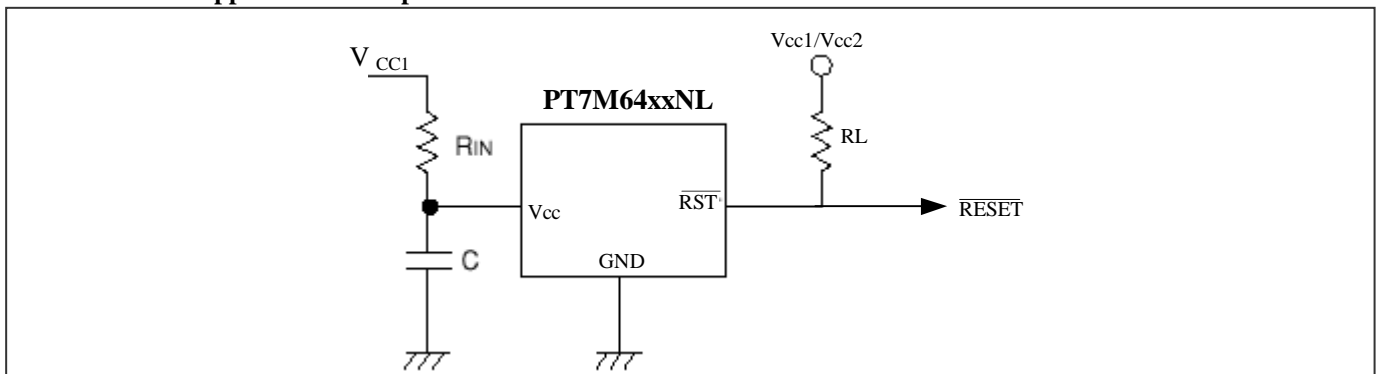


($V_{CC} = 1.2V$ to $5.5V$, $T_A = -40 \sim 85^\circ C$, unless otherwise noted. Typical values are at $T_A = +25^\circ C$)

Sym	Description	Test Conditions	Min	Typ	Max	Unit
t_{RP}	Timeout Period	$T_A = +25^\circ C$	120	260	430	ms
t_p	Delay	-	-	35	-	μs

Typical Operation Circuit

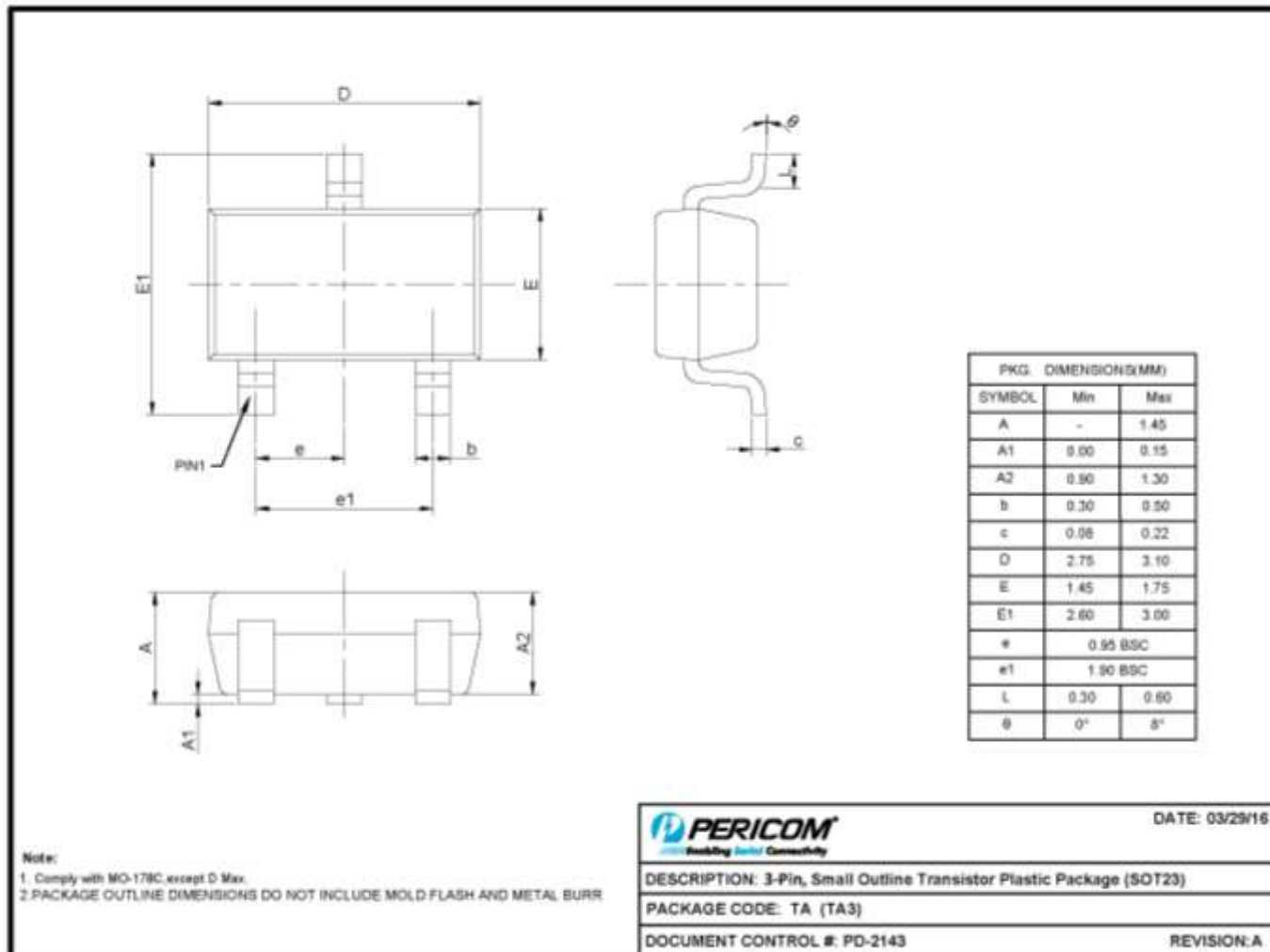
PT7M64xxNL Application Example



For typical application, R_L could be $100k\Omega$, R_{IN} less than $10k\Omega$ and that C more than $0.1\mu F$.

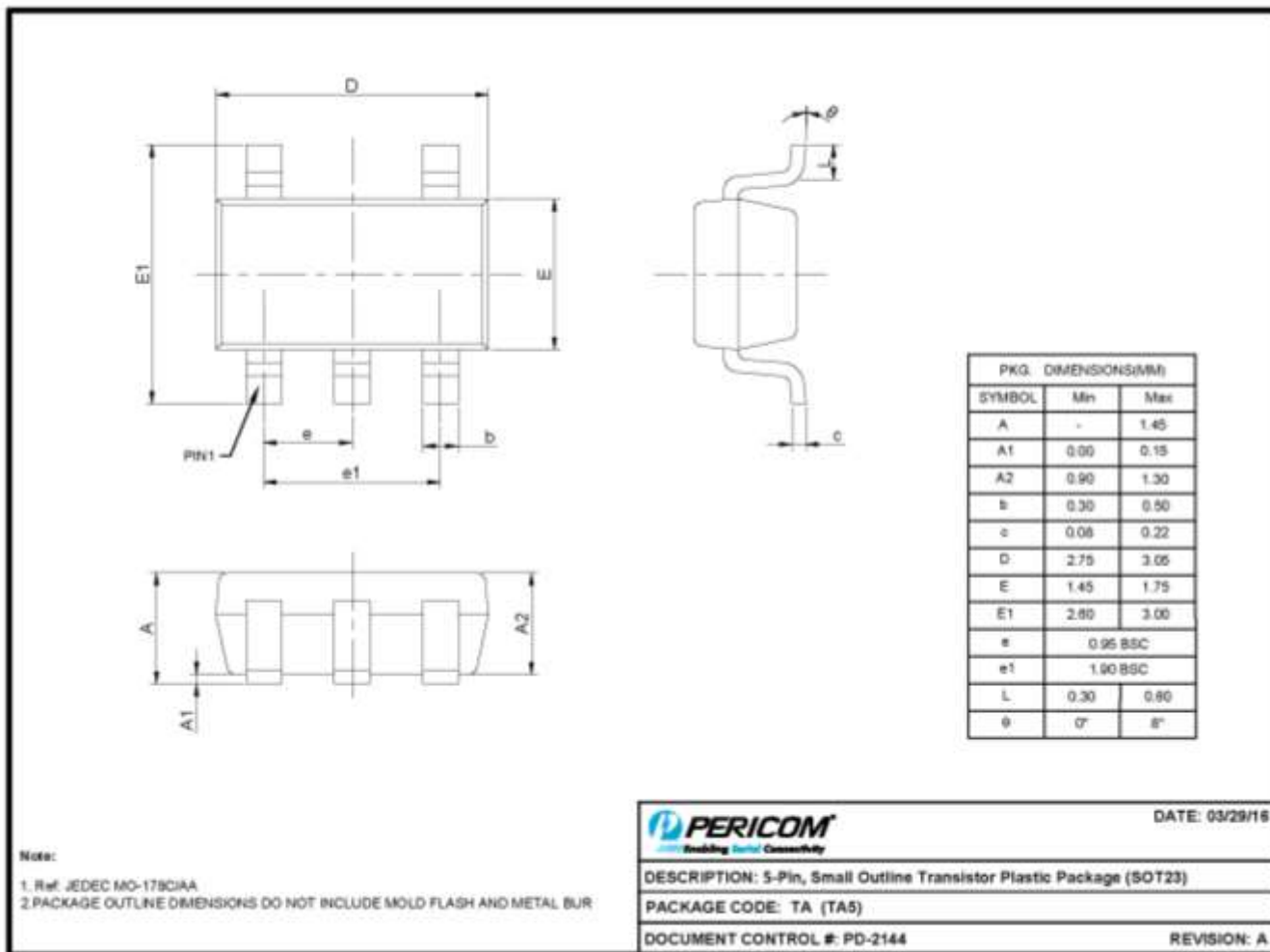
Mechanical Information

TA3 (SOT23)



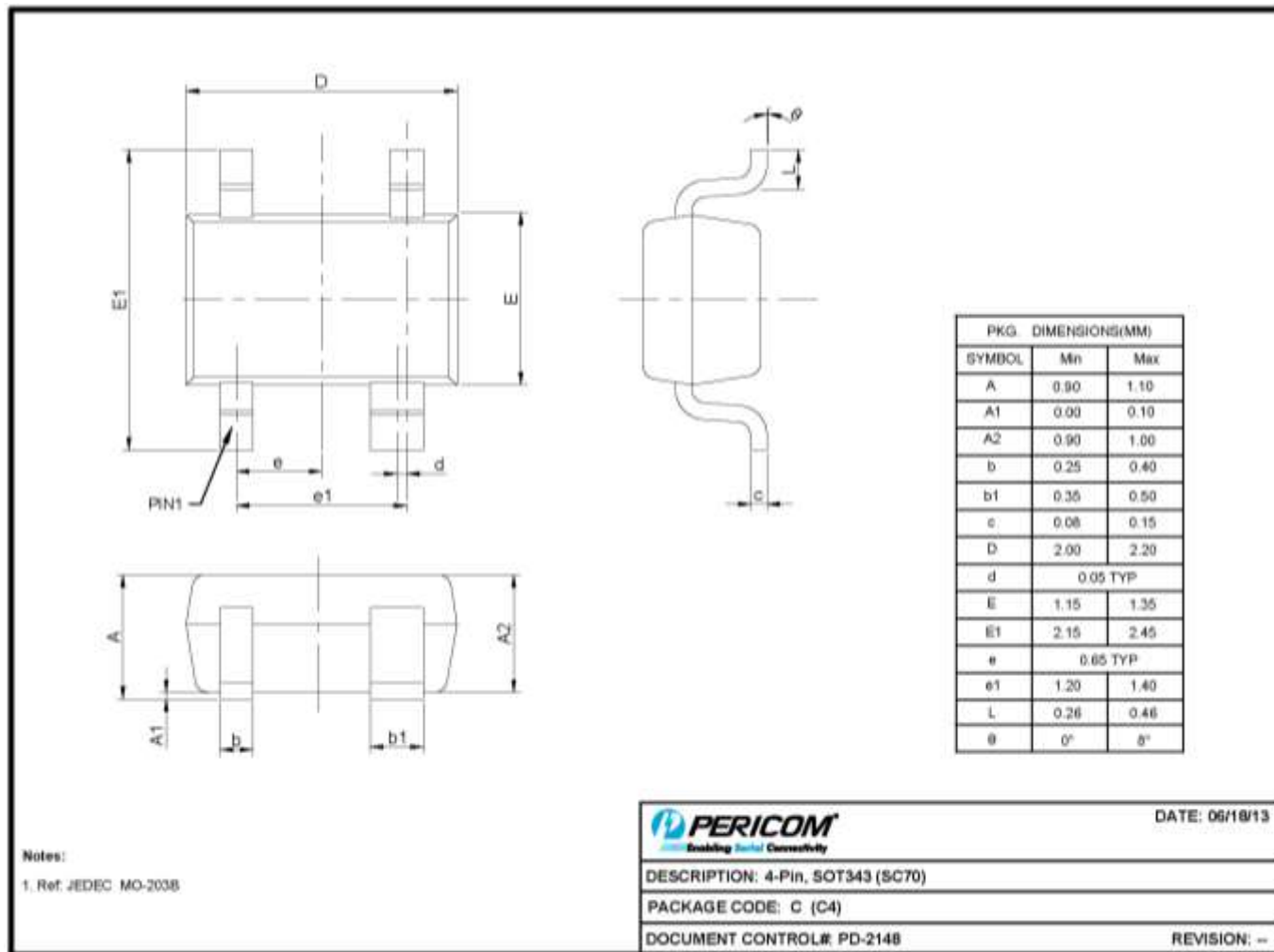
16-0080

TA5 (SOT23)



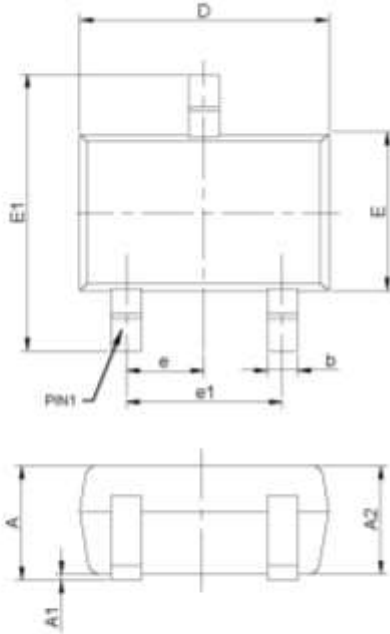
18-0081

C4 (SC70)



13-0187

C3 (SC70)



PKG. DIMENSIONS(MM)		
SYMBOL	Min	Max
A	-	1.10
A1	0.00	0.10
A2	0.70	1.00
b	0.15	0.40
c	0.08	0.22
D	1.80	2.20
E	1.10	1.40
E1	1.75	2.45
e	0.65 BSC	
e1	1.30 BSC	
L	0.25	0.40
Ø	0"	8"

Note:

1. Comply with MO-253C, except b, Max.D, Min.D, Max.E1, Min and E1 Max.
2. PACKAGE OUTLINE DIMENSIONS DO NOT INCLUDE MOLD FLASH AND METAL BURR



DATE: 03/29/16

DESCRIPTION: 3-Pin, SOT323 (SC70)

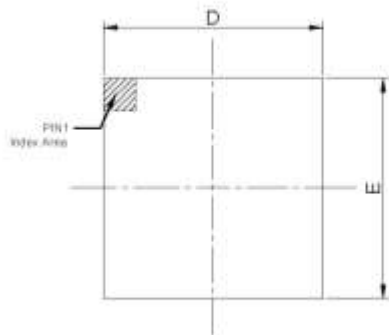
PACKAGE CODE: C (C3)

DOCUMENT CONTROL# PD-2147

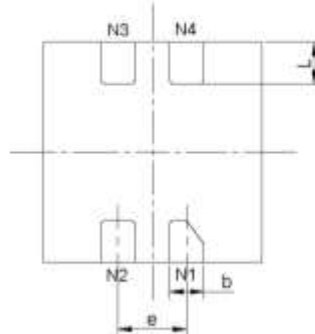
REVISION: A

16-0077

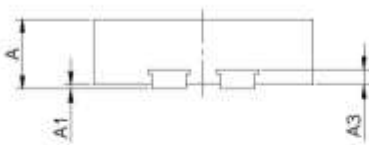
XV (UDFN1.6x1.6-4L)



TOP VIEW



BOTTOM VIEW



SIDE VIEW

PKG. DIMENSIONS(MM)		
SYMBOL	Min	Max
A	0.45	0.55
A1	0.00	0.05
A3	0.11 REF	
D	1.55	1.65
E	1.55	1.65
b	0.20	0.30
e	0.50 TYP	
L	0.25	0.35

Notes:
1. Ref: JEDEC MO-287A



DATE: 06/18/13

DESCRIPTION: 4-Pin, UDFN, 1.6X1.6, M1S

PACKAGE CODE: XV (XV4)

DOCUMENT CONTROL #: PD-2130

REVISION: --

13-0169



For latest package info.

please check: <http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/>

Ordering Information

Part Number	Package Code	Package
PT7M64xxCLTA3E	TA3	3-Pin, Small Outline Transistor Plastic Package (SOT23)
PT7M64xxCLTA5E	TA5	5-Pin, Small Outline Transistor Plastic Package (SOT23)
PT7M64xxCLC3E	C3	3-Pin, SOT323 (SC70)
PT7M64xxCLC4E	C4	4-Pin, SOT343 (SC70)
*PT7M64xxCHTA3E	TA3	3-Pin, Small Outline Transistor Plastic Package (SOT23)
*PT7M64xxCHTA5E	TA5	5-Pin, Small Outline Transistor Plastic Package (SOT23)
*PT7M64xxCHC3E	C3	3-Pin, SOT323 (SC70)
*PT7M64xxCHC4E	C4	4-Pin, SOT343 (SC70)
PT7M64xxNLTA3E	TA3	3-Pin, Small Outline Transistor Plastic Package (SOT23)
PT7M64xxNLTA5E	TA5	5-Pin, Small Outline Transistor Plastic Package (SOT23)
PT7M64xxNLC3E	C3	3-Pin, SOT323 (SC70)
PT7M64xxNLC4E	C4	4-Pin, SOT343 (SC70)
PT7M64xxNLXV4E	XV	4-Pin, 1.6x1.6, MIS (UDFN)
PT7M64xxCLXV4E	XV	4-Pin, 1.6x1.6, MIS (UDFN)
*PT7M64xxCHXV4E	XV	4-Pin, 1.6x1.6, MIS (UDFN)

Note:

- “xx” refer to voltage range, see below table 1.
- E = Pb-free or Pb-free and Green
- Adding X Suffix= Tape/Reel
- Contact Pericom for availability.
- “*” for CH part, please check the storage with related sales.

Table.2 Suffix “xx” definition of PT7M64xx

Suffix xx	V _{TH} (V)	Suffix xx	V _{TH} (V)	Suffix xx	V _{TH} (V)	Suffix xx	V _{TH} (V)	Suffix xx	V _{TH} (V)
18	1.8	25	2.5	32	3.2	39	3.9	46	4.6
19	1.9	26	2.6	33	3.3	40	4.0	47	4.7
20	2.0	27	2.7	34	3.4	41	4.1	48	4.8
21	2.1	28	2.8	35	3.5	42	4.2	49	4.9
22	2.2	29	2.9	36	3.6	43	4.3	50	5.0
23	2.3	30	3.0	37	3.7	44	4.4		
24	2.4	31	3.1	38	3.8	45	4.5		

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SOT-23/SC-70 Package Top Marking Instruction

