

## PI3HDMI412FT-B

## 2:1 Mux/DeMux High Bandwidth HDMI™ 1.4 Compliant Signal Switch

### **Features**

→ 4-Differential Channel 2:1 Mux/DeMux

→ HDMI<sup>TM</sup> 1.4 compatible

→ High Bandwidth: 2.7GHz @-3dB BW

→ Supports both AC coupled and DC coupled signals

→ Switching speed: 4ns

→ Isolation: -40dB @ 2.0 Gbps

→ Crosstalk: -31dB @ 2.0 Gbps

→ ESD: Data bits @ 8kV contact, select bit @ 2kV HBM

→ Low bit-to-bit skew

→ Enable/Disable Time: 9ns

→ Bidirectional

→ Packaging (Pb-free & Green):

— 42-pin TQFN (ZH42)

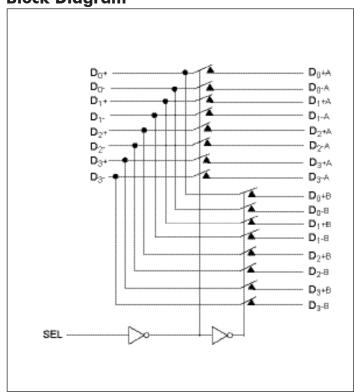
### **Description**

Pericom Semiconductor's PI3HDMI series of switch circuits are targeted for high-resolution video networks that are based on DVI/HDMI<sup>TM</sup> standards, and TMDS signal processing. The PI3HDMI412FT-B is an 8- to 4-Channel Mux/DeMux Switch. The device multiplexes differential signals to one of two corresponding outputs. The switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs. It is designed for low bit-to-bit skew and high channel-to-channel noise isolation.

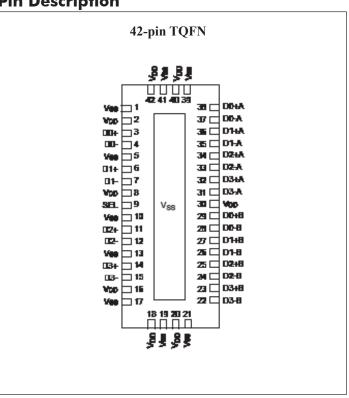
The high bandwidth provides the resolution required by the next generation HDTV and PC graphics. Three differential channels are used for data (video signals for DVI or audio/video signals for HDMI), and one differential channel is used for Clock for decoding the TMDS signals at the outputs.

Because of its passive bidirectional feature, this switch can be used either at the video drivers side or at the receiver side. For PC graphics applications, the device sits at the drivers side to switch between multiple display units such as LCD monitor, projector, TV, etc. For consumer video applications, the device sits at the receiver end to switch between the source components such as DVD, D-VHS, STB, etc.

**Block Diagram** 



Pin Description





### **Pin Description**

Pin # (TQFN)	Pin Name	Description
2, 8, 16, 18, 20, 30, 40, 42	V <sub>DD</sub>	+ Power supply
1, 5, 10, 13, 17, 19, 21, 39, 41	V <sub>SS</sub>	- Power supply
9	SEL	Select pin, see truth table
3, 4, 6, 7, 11, 12, 14, 15, 22-29, 31-38	$Dx^{+/-x} + CLK^{+/-x}$	Data + Clk bits for TMDS signal

### Truth Table(1)

Function	SEL
Dx to Dx <sup>A</sup>	L
Dx to Dx <sup>B</sup>	Н

#### Note:

1. x=0,1,2,3

### Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	65°C to +150°C
Supply Voltage to V <sub>SS</sub> Potential	0.5V to +2.5V
DC Input Voltage	0V to V <sub>DD</sub> +0.5V
DC Output Current	120mA

**Note:** Stresses greater than those listed under MAXIMUM RAT-INGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

## **DC Power Supply Characteristics**

Parameter	Description	Min.	Max.	Units
V <sub>DD -</sub> V <sub>SS</sub>	Positive to Negative Power Supply	1.5	2.0	V
$V_{SS}$	Negative Power Supply	0	1.6	V

## DC Electrical Characteristics for Switching over Operating Range (TA = $-40^{\circ}$ C to $+85^{\circ}$ C)

Parameter	Description	Test Conditions (1)	Min.	<b>Typ.</b> <sup>(2)</sup>	Max.	Units
$V_{IH}^{(3)}$	Input HIGH Voltage	Guaranteed HIGH level	$0.65(V_{DD}-V_{SS})+V_{SS}$			
$V_{\mathrm{IL}}^{\scriptscriptstyle{(3)}}$	Input LOW Voltage	Guaranteed LOW level	-0.5+V <sub>SS</sub>		$0.35(V_{DD}-V_{SS})+V_{SS}$	V
$V_{IK}$	Clamp Diode Voltage	V <sub>DD</sub> =Max., I <sub>IN</sub> =-18mA		-0.7+V <sub>SS</sub>	-1.2+V <sub>SS</sub>	
$I_{IH}$	Input HIGH Current	V <sub>DD</sub> =Max., V <sub>IN</sub> =V <sub>DD</sub>			±5	
$I_{IL}$	Input LOW Current	V <sub>DD</sub> =Max., V <sub>IN</sub> =V <sub>SS</sub>			±5	μΑ

### Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at  $T_A = 25$ °C ambient and maximum loading.
- 3.  $\rm V_{\hbox{\scriptsize IH}}$  &  $\rm V_{\hbox{\scriptsize IL}}$  are for SEL input only.



## **Power Supply Characteristics**

Parameters	Description	Test Conditions(1)	Min.	<b>Typ.</b> <sup>(2)</sup>	Max.	Units
$I_{CC}$	Quiescent Power Supply Current	$V_{DD}$ = Max., $V_{IN}$ = $V_{DD}$ or $V_{SS}$		300	400	μΑ

#### Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at  $T_A = 25$ °C ambient and maximum loading.

## Switching Characteristics ( $T_A = -40^{\circ} \text{ to } +85^{\circ}\text{C}$ , $V_{SS} = 0\text{V}$ , $V_{DD} = 1.8\text{V} \pm 10\%$ )

Parameter	Description	Min.	Max.	Units
tpZH, tpZL	Line Enable Time - SEL to N <sup>A</sup> , N <sup>B(1)</sup>	0.5	8.0	
tpHZ, tPLZ	Line Disable Time - SEL to N <sup>A</sup> , N <sup>B(1)</sup>	0.5	4.0	ns

#### Notes:

# Dynamic Electrical Characteristics Over the Operating Range $(T_A = -40^{\circ} \text{ to } +85^{\circ}\text{C})$

Parameter	Description	<b>Test Conditions</b>	Min.	Тур.	Max.	Units
V	Crosstalk	See Fig. 1 for Measurement Setup,		21		
$X_{TALK}$	Crosstaik	@ 2.0 Gbps		-31	1D	
O <sub>IRR</sub>	OFF Isolation	See Fig. 2 for Measurement Setup,		40		dB
		@ 2.0 Gbps		-40		
BW	-3dB BW			2.7		GHz
		@ 1.65 Gbps		-0.87		
Insertion Loss		@ 2.0 Gbps		-0.95		dB
		@ 2.5 Gbps		-1.18		u D
		@ 3.3 Gbps		-1.85		

<sup>1.</sup> For measurement setup, please see "Test Circuit For Electrical Characteristic" on page 8, and "Switching waveforms" on page 9



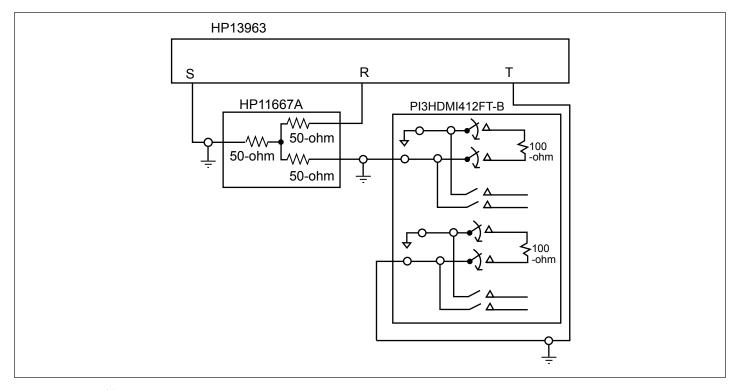


Fig 1. Crosstalk Setup

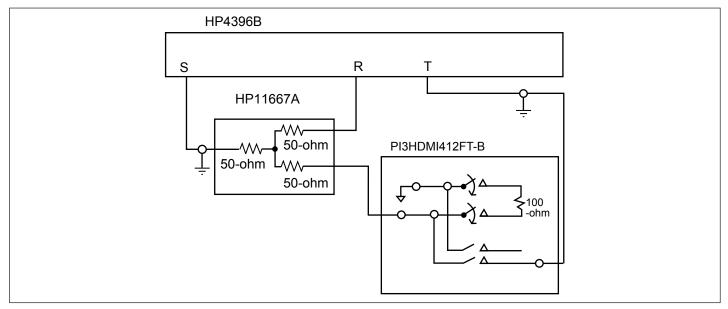


Fig 2. Off-isolation setup



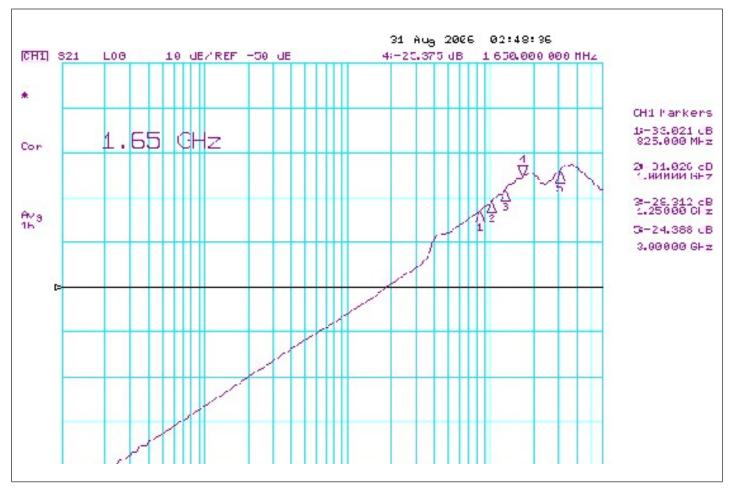


Fig 3. Crosstalk



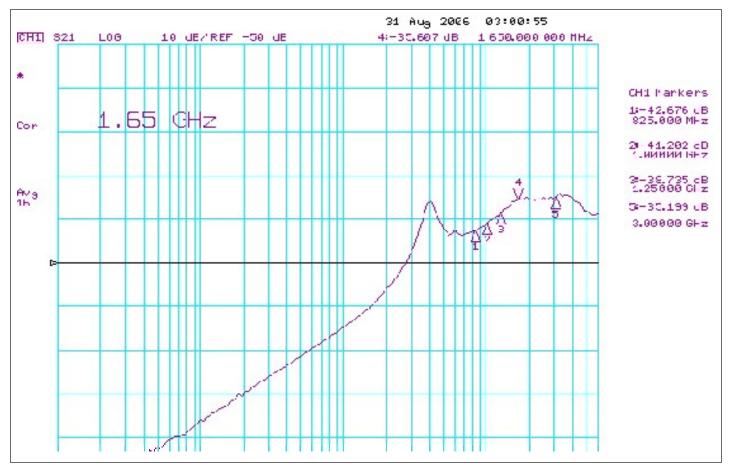


Fig 4. Off Isolation



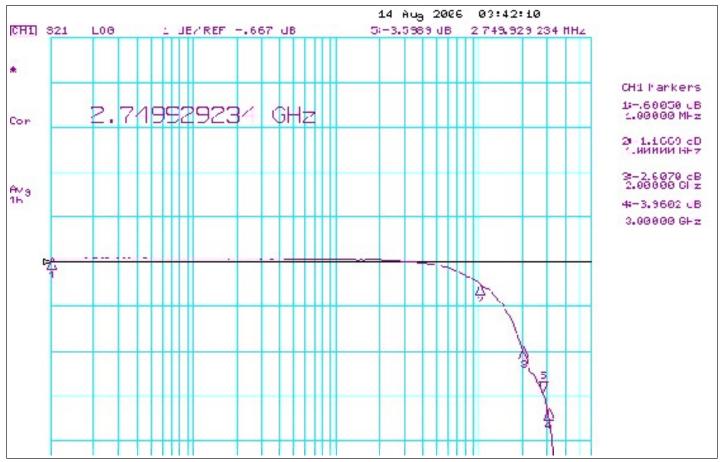


Fig 5. Insertion Loss



## **Application Measurements**

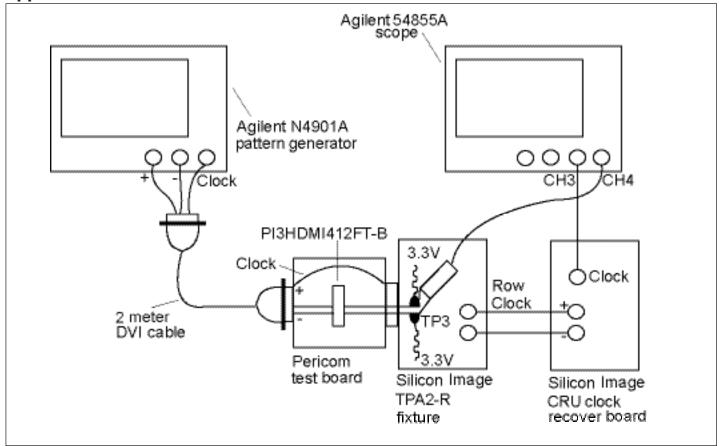


Fig 6. TMDS TP3 Rx eye compliance Test Setup



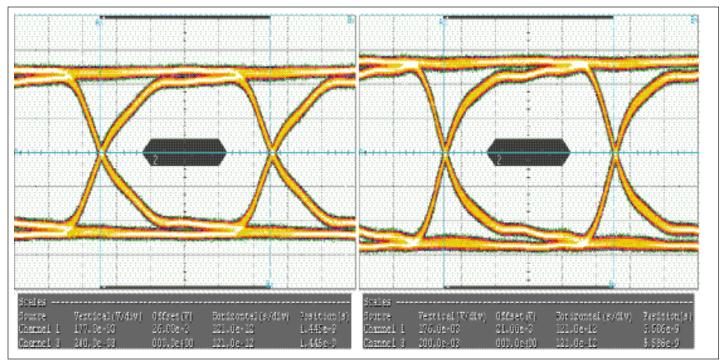
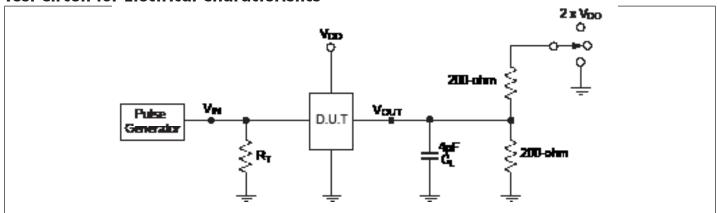


Fig 7. TP3 (Rx) with 2-meter cable eye measurements (Right eye is with no switch. Left eye is with switch.)

### **Test Circuit for Electrical Characteristics**



#### Notes:

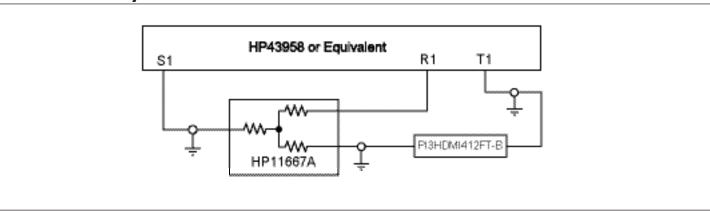
- 1. CL = Load capacitance: includes jig and probe capacitance.
- 2. RT = Termination resistance: should be equal to ZOUT of the Pulse Generator
- 3. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- 4. All input impulses are supplied by generators having the following characteristics:  $PRR \le MHz$ ,  $ZO = 50\Omega$ ,  $tR \le 2.5ns$ ,  $tF \le 2.5ns$ .
- 5. The outputs are measured one at a time with one transition per measurement.



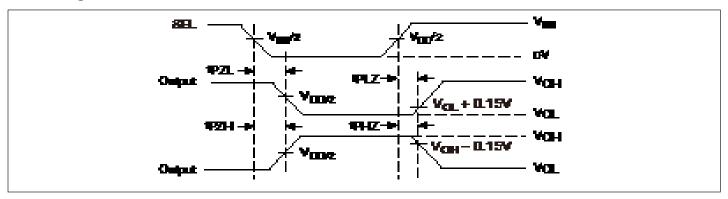
### **Switch Positions**

Test	Switch
$t_{\rm PLZ}, t_{\rm PZL}$	2 x (V <sub>DD</sub> - V <sub>SS</sub> )
t <sub>PHZ</sub> , t <sub>PZH</sub>	V <sub>SS</sub>
Prop Delay	Open

## **Test Circuit for Dynamic Electrical Characteristics**

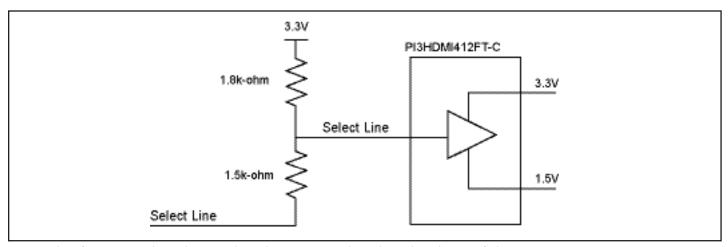


## **Switching Waveforms**



**Voltage Waveforms Enable and Disable Times** 

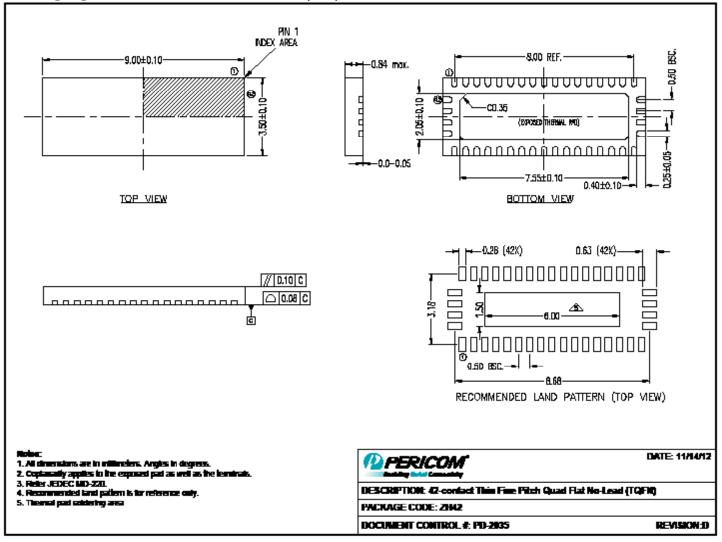




Example of a circuit that also needs to be connected to the select lines of the PI3HDMI412FT-B



## Packaging Mechanical: 42-Pin TQFN (ZH)



### 12-(529

### Note:

For latest package info, please check: http://www.pericom.com/products/packaging/mechanicals.php

### **Ordering Information**

Ordering Code	Package Code	Package Description
PI3HDMI412FT-BZHE	ZH	42-contact, Thin Fine Pitch Quad Flat No-Lead (TQFN)

#### Notes:

- $\bullet \ Thermal\ characteristics\ can\ be\ found\ on\ the\ company\ web\ site\ at\ www.pericom.com/packaging/$
- E = Pb-free and Green
- Adding X suffix = Tape/Reel



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