PART OBSOLETE - DISCONTINUED





PI2DBS6212

6.5 Gbps SAS2, SATA3, XAUI 2 Differential Channel, 2:1 Mux/DeMux Switch

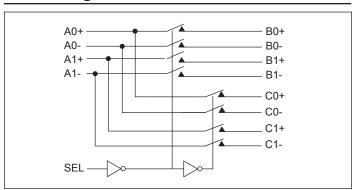
Description

The DIODES PI2DBS6212 is a 4 to 2 bi-directional differential channel multiplexer/demultiplexer switch supporting 6.5 Gbps applications. Due to its low bit-to-bit skew, high channel-to-channel noise isolation and high bandwidth, this product is ideal for switching two sources to a single receiver, or alternatively, one source to two receivers.

Application(s)

- SAS2, SATA3, XAUI, Infiniband, Hype Transport, Rapid I/O
- Computers, Servers, Storage, Instrumentation, Telecom, Networking.

Block Diagram



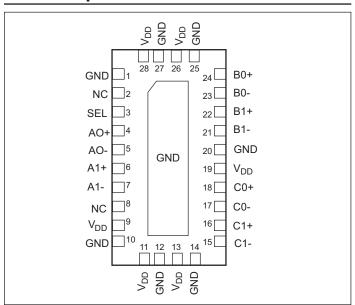
Truth Table

Function	SEL
A to B	L
A to C	Н

Features

- SAS2, SATA3, XAUI switch
- 2 Differential Channel, 2:1 Mux/DeMux
- 6.5 Gbps performance
- Bi-directional operation
- Low Bit-to-Bit Skew, 10ps max
- Low Insertion Loss: -2.3dB@3GHz(6Gbps)
- Low Crosstalk: -43dB@3GHz (6.0Gbps)
- Low Off Isolation: -21dB@3GHz (6.0Gbps)
- V_{DD} Operating Range: 1.5V to 1.8V $\pm 10\%$
- ESD Tolerance 2KV HBM I/OTotally
- Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/
- Packaging (Pb-free & Green):
 - 28 contact TQFN (ZH, 3.5 × 5.5mm)

Pin Description



Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- $2. \ See \ https://www.diodes.com/quality/lead-free/\ for\ more\ information\ about\ Diodes\ Incorporated's\ definitions\ of\ Halogen-\ and\ Antimony-free,\ "Green"\ and\ Lead-free.$
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.





Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	65°C to +150°C
Supply Voltage to Ground Potential	0.5V to +2.5V
DC Input Voltage	0.5V to +V _{DD}
DC Output Current	120mA
Power Dissipation	0.5W

Note: Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. (2)	Max.	Units
I_{DD}	Quiescent Power Supply Current	V_{DD} = Max., V_{IN} = GND or V_{DD}			400	μΑ

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at $V_{DD} = 1.8V$, $T_A = 25$ °C ambient and maximum loading.

DC Electrical Characteristics for Switching over Operating Range

 $(T_A = -40^{\circ}C \text{ to } +85^{\circ}C, V_{DD} = 1.5V \text{ to } 1.8V \pm 10\%)$

	, 55	<u> </u>				
Parameter	Description	Test Conditions	Min.	Тур.	Max.	Units
V_{IH}	Input HIGH Voltage, SEL input	Guaranteed HIGH level	0.65 x V _{DD}	-	V_{DD}	
V_{IL}	Input LOW Voltage, SEL input	Guaranteed LOW level	-0.5	-	$0.35 \times V_{DD}$	V
V_{IK}	Clamp Diode Voltage, SEL input	$V_{\mathrm{DD}} = \mathrm{Max.}, I_{\mathrm{IN}} = -18\mathrm{mA}$	-	-0.7	-1.2	
I_{IH}	Input HIGH Current for SEL	$V_{DD} = Max., V_{IN} = V_{DD}$	-10	-	+10	
I_{OZ}	Channel Leakage Current	$V_{\mathrm{DD}} = \mathrm{Max.}, V_{\mathrm{IN}} = 1.8\mathrm{V}$	-10	-	+10	μΑ
${ m I}_{ m IL}$	Input LOW Current	$V_{DD} = Max., V_{IN} = GND$	-20	-	+10	
17	DC C:1 V-14 D	$V_{\rm O}/V_{\rm I} > 95\%, R_{\rm L} = 10K$	-0.5		2.5	V
V_{IDC}	DC Signal Voltage Range	$V_{O}/V_{I} > 80\%$, $R_{L} = 50$ -Ohms	-0.4		1.2	ľ

Switching Characteristics

 $(T_A = -40^{\circ} \text{ to } +85^{\circ}\text{C}, V_{DD} = 1.5\text{V to } 1.8\text{V} \pm 10\%)$

Parameter	Description	Min.	Тур.	Max.	Units
tPZH, tPZL	Line Enable Time - SEL to AN, BN, CN	0.5	-	9.0	
tPHZ, tPLZ	Line Disable Time - SEL to AN, BN, CN	0.5	-	9.0	ns
tb-b	Bit-to-bit skew within the same differential pair			10	
tch-ch	Channel-to-channel skew			15	ps
tdiff	Differential delay - AN to BN or CN			20	

Notes:

1. For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.





Dynamic Electrical Characteristics Over the Operating Range

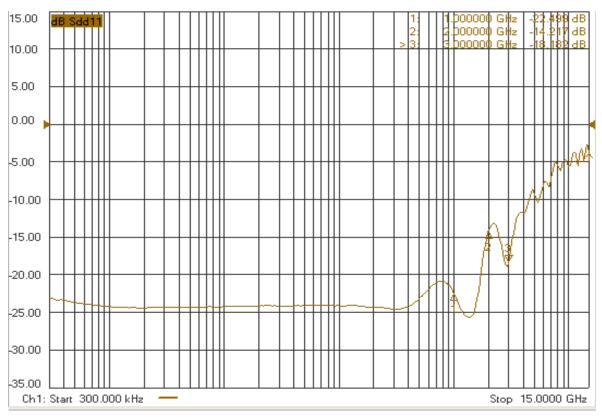
 $(T_A = -40^{\circ} \text{ to } +85^{\circ}\text{C}, V_{DD} = 1.5\text{V to } 1.8\text{V} \pm 10\%)$

Parameter	Description	Test Conditions	Min.	Тур.	Max.	Units	
BW	Bandwidth -3dB			4.1			
V _{IF}	Max Signal Frequency Range	Insertion loss 1.5dB, V _{IN} = 0.8Vpp, DC = 0V	2.5				
		Insertion loss 1.5dB, V_{IN} = 0.6Vpp, DC = 0.9V	2.5			GHz	
			4.0				
			4.0				
P-1dB	1 dB Compression Input Signal	$R_L = 50$, $f = 375$ MHz, sin wave, $DC = 0$ V	1.2				
		$R_L = 50$, $f = 375$ MHz, sin wave, $DC = 0.45$ V	2.0			Vpp	
		$R_L = 50$, $f = 375$ MHz, sin wave, $DC = 0.9$ V	2.4				
R _{LOSS}	Return Loss	f = 3 GHz		-18			
X _{TALK}	Crosstalk	f = 3.0 GHz		-43			
O _{IRR}	OFF Isolation	f = 3.0 GHz		-21		dB	
I _{LOSS}	Differential Insertion Loss	f = 3 GHz		-2.3			

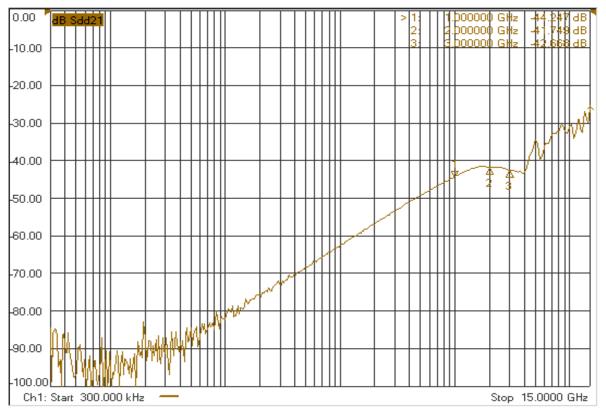
Notes:

1. Guaranteed by design.



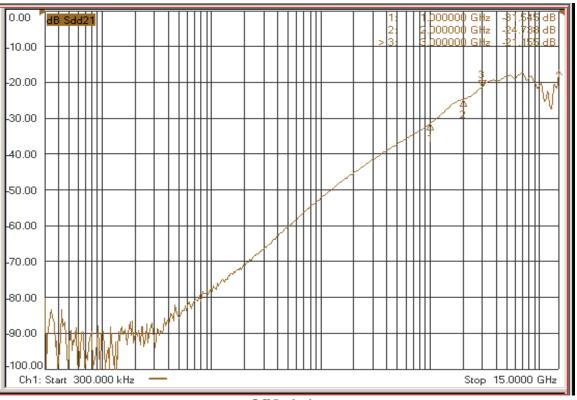


Return Loss

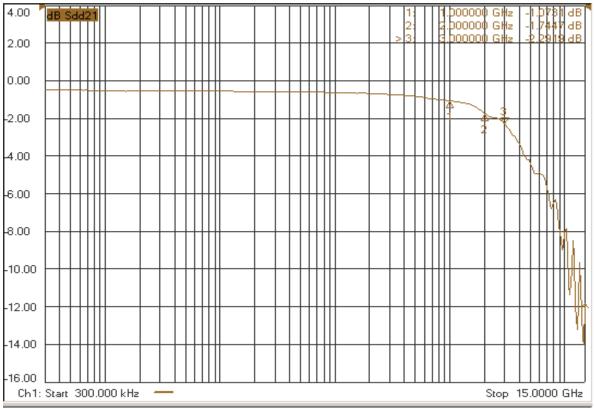






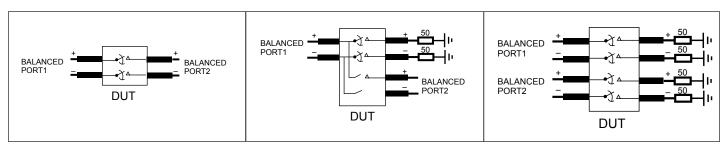


Off Isolation







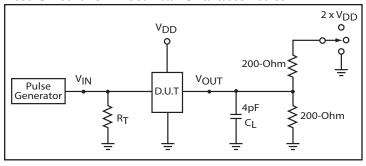


Diff. Insertion Loss and Return Test Circuit

Diff. Off Isolation Test Circuit

Diff. Near End Xtalk Test Circuit

Test Circuit for Electrical Characteristics (1-5)



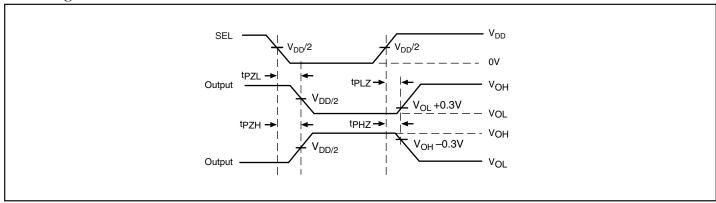
Notes:

- 1. $C_L = Load$ capacitance: includes jig and probe capacitance.
- 2. R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator
- 3. Output 1 is for an output with internal conditions such that the output is low except when disabled by the output control. output 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- 4. All input impulses are supplied by generators having the following characteristics: PRR \leq MHz, $Z_O = 50\Omega$, $t_R \leq$ 2.5ns, $t_F \leq$ 2.5ns.
- 5. The outputs are measured one at a time with one transition per measurement.

Switch Positions

Test	Switch
t _{PLZ} , t _{PZL}	2 x V _{DD}
t _{PHZ} , t _{PZH}	GND
Prop Delay	Open

Switching Waveforms



Voltage Waveforms Enable and Disable Times





Part Marking

ZH Package - Cu Version

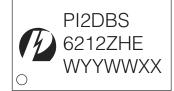


W: Die Rev YY: Year

WW: Workweek

1st X: Assembly Code 2nd X: Fab Code

ZH Package - 2017 Au Version



W: Die Rev YY: Year

WW: Workweek

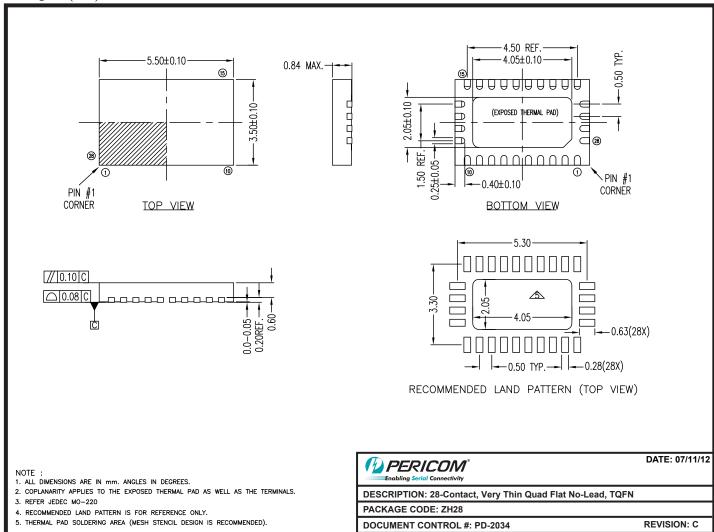
1st X: Assembly Code 2nd X: Fab Code





Packaging Mechanical

28-TQFN (ZH)



12-0419

For latest package info.

 $please\ check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/please. The property of th$

Ordering Information

Ordering Numbers	Package Code	Package Decription
PI2DBS6212ZHEX	ZH	28-contact, Very Thin Quad Flat No-Lead (TQFN)

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. E = Pb-free and Green
- 5. X suffix = Tape/Reel





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