

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
-40V	25mΩ @ V _{GS} = -10V	-8.0A
	45mΩ @ V _{GS} = -4.5V	-6.0A

Description and Applications

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

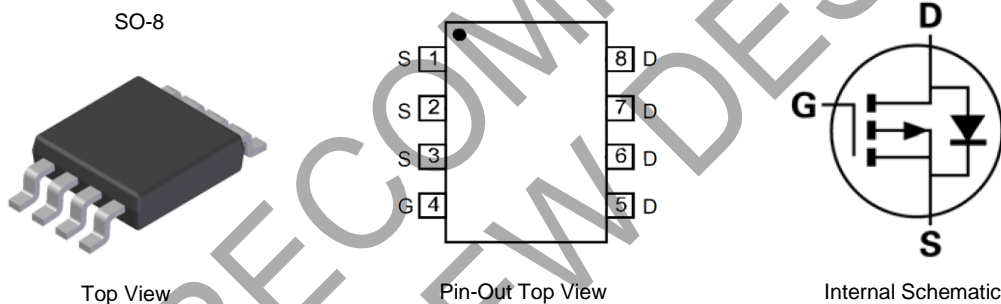
- Motor controls
- Backlighting
- DC-DC converters
- Printer equipment

Features and Benefits

- Low R_{DS(ON)} – Minimizes Conduction Losses
- Fast Switching Speed – Minimizes Switching Losses
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- The DIODES™ DMP4025LSSQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF16949 certified facilities.
<https://www.diodes.com/quality/product-definitions/>

Mechanical Data

- Package: SO-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 Ⓢ
- Weight: 0.074 grams (Approximate)

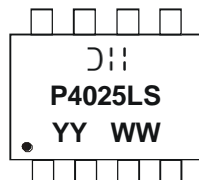


Ordering Information (Note 4)

Part Number	Package	Packing	
		Qty.	Carrier
DMP4025LSSQ-13	SO-8	2,500	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



D = Manufacturer's Marking
P4025LS = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 22 = 2022)
WW = Week (01 to 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	-40	V
Gate-Source Voltage			V _{GSS}	±20	
Continuous Drain Current	V _{GS} = -10V	(Note 6)	I _D	-8.0	A
		T _A = +70°C (Note 6)		-6.9	
		(Note 5)		-6.0	
Pulsed Drain Current	V _{GS} = -10V	(Note 7)	I _{DM}	-30	
Continuous Source Current (Body Diode)			(Note 7)	I _S	
Pulsed Source Current (Body Diode)			(Note 7)	I _{SM}	-30

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Power Dissipation	(Note 5)	P _D	1.52	W
	(Note 6)		2.4	
Thermal Resistance, Junction to Ambient	(Note 5)	R _{θJA}	82	°C/W
	(Note 6)		52	
Thermal Resistance, Junction to Lead	(Note 8)	R _{θJL}	48.85	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 9)						
Drain-Source Breakdown Voltage	BV _{DSS}	-40	—	—	V	I _D = -250μA, V _{GS} = 0V
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-1.0	μA	V _{DS} = -40V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 9)						
Gate Threshold Voltage	V _{GS(TH)}	-0.8	-1.3	-1.8	V	I _D = -250μA, V _{DS} = V _{GS}
Static Drain-Source On-Resistance	R _{DS(ON)}	—	18	25	mΩ	V _{GS} = -10V, I _D = -3A
			30	45		V _{GS} = -4.5V, I _D = -3A
Forward Transconductance	g _{FS}	—	16.6	—	S	V _{DS} = -5V, I _D = -3A
Diode Forward Voltage	V _{SD}	—	-0.7	-1.0	V	I _S = -1A, V _{GS} = 0V
DYNAMIC CHARACTERISTICS (Note 10)						
Input Capacitance	C _{iss}	—	1,640	—	pF	V _{DS} = -20V, V _{GS} = 0V f = 1MHz
Output Capacitance	C _{oss}	—	179	—		
Reverse Transfer Capacitance	C _{rss}	—	128	—		
Gate Resistance	R _G	—	6.43	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _g	—	14.0	—	nC	V _{GS} = -4.5V V _{GS} = -10V V _{DS} = -20V I _D = -3A
Total Gate Charge	Q _g	—	33.7	—		
Gate-Source Charge	Q _{gs}	—	5.5	—		
Gate-Drain Charge	Q _{gd}	—	7.3	—		
Turn-On Delay Time	t _{D(ON)}	—	6.9	—	ns	V _{DD} = -20V, V _{GS} = -10V I _D = -3A
Turn-On Rise Time	t _R	—	14.7	—		
Turn-Off Delay Time	t _{D(OFF)}	—	53.7	—		
Turn-Off Fall Time	t _F	—	30.9	—		

- Notes:
- For a device surface mounted on minimum recommended FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
 - Same as Note 5, except the device is surface mounted on 25mm x 25mm x 1.6mm FR4 PCB.
 - Repetitive rating on 25mm x 25mm FR4 PCB, D = 0.02, pulse width 300μs – pulse width by maximum junction temperature.
 - Thermal resistance from junction to solder-point (at the end of the drain lead).
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to production testing.

Thermal Characteristics

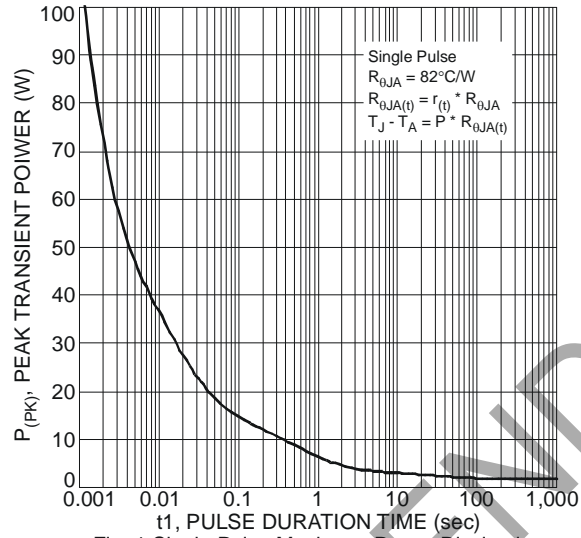


Fig. 1 Single Pulse Maximum Power Dissipation

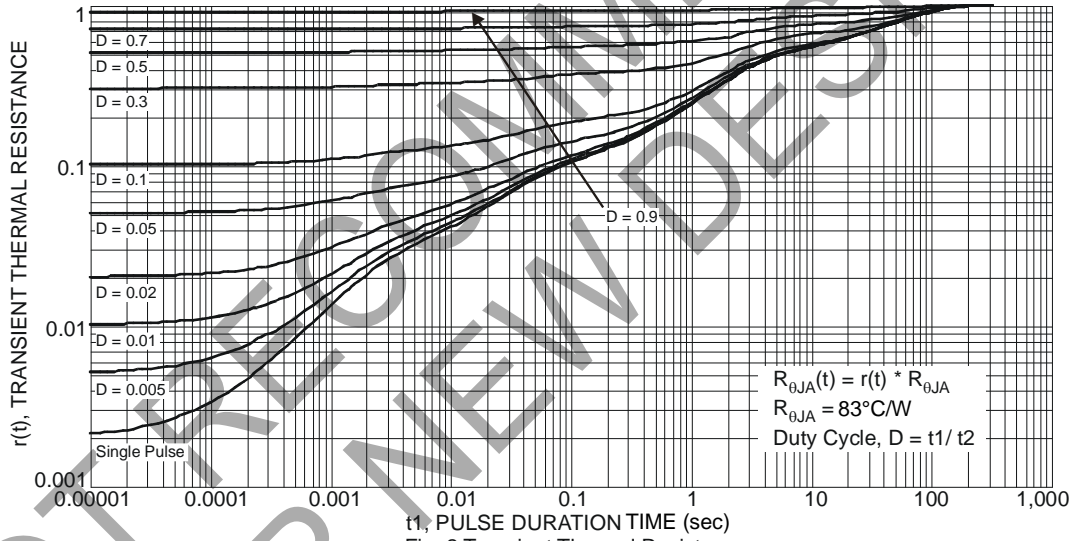


Fig. 2 Transient Thermal Resistance

Typical Characteristics

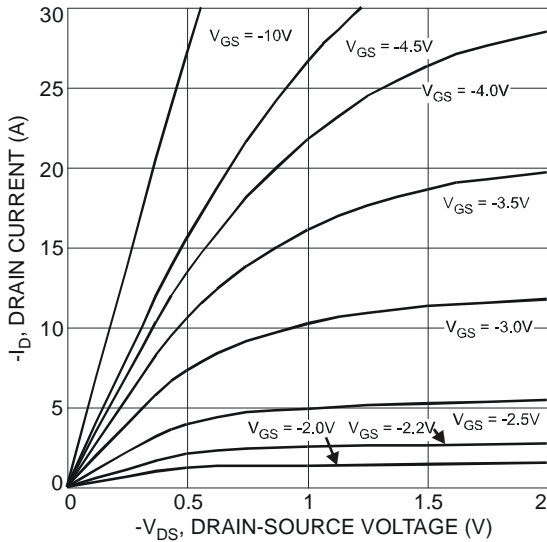


Fig. 3 Typical Output Characteristic

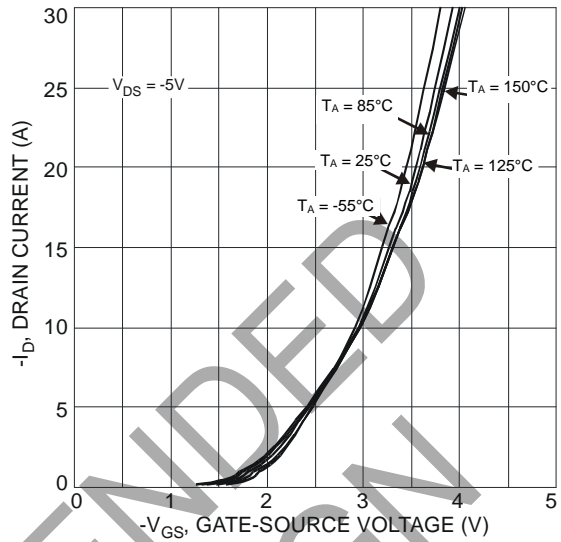


Fig. 4 Typical Transfer Characteristic

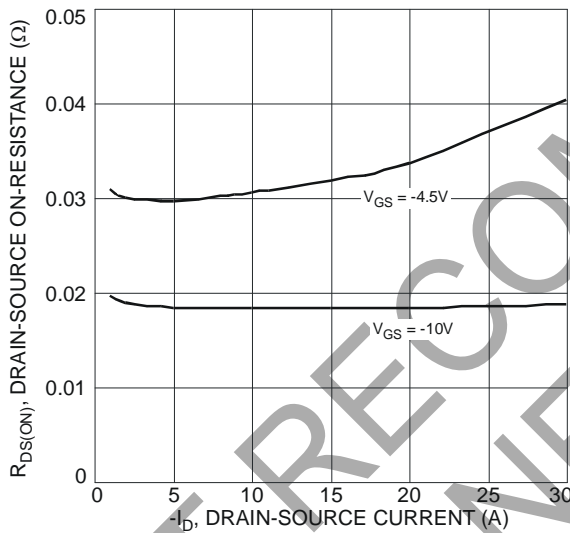


Fig. 5 Typical On-Resistance vs. Drain Current and Gate Voltage

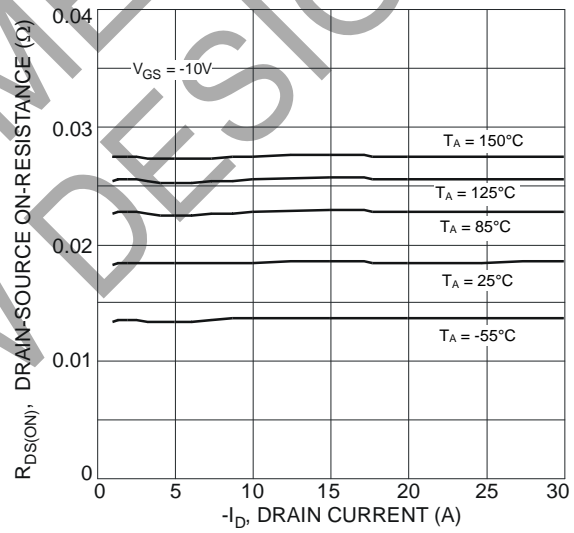


Fig. 6 Typical On-Resistance vs. Drain Current and Temperature

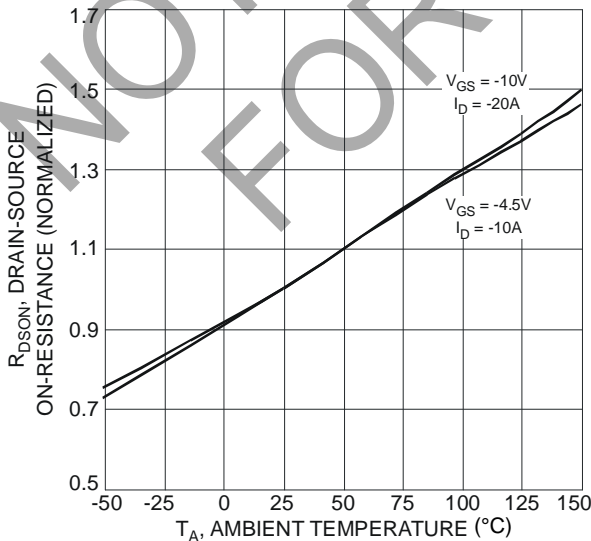


Fig. 7 On-Resistance Variation with Temperature

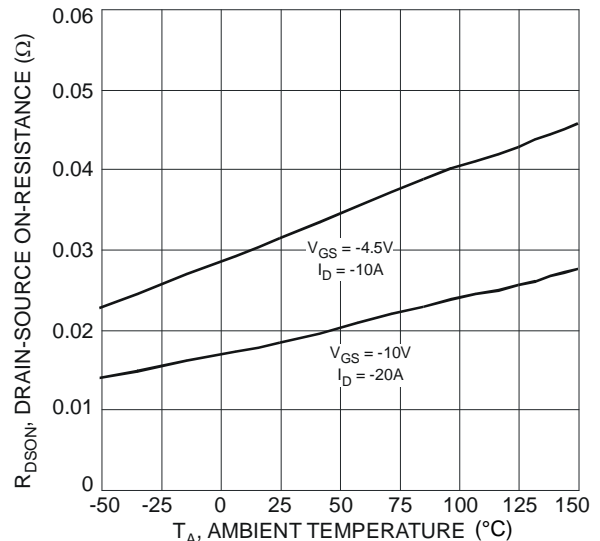


Fig. 8 On-Resistance Variation with Temperature

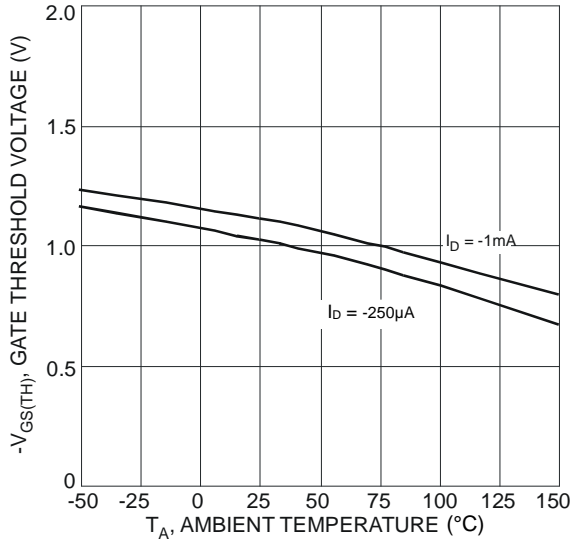


Fig. 9 Gate Threshold Variation vs. Ambient Temperature

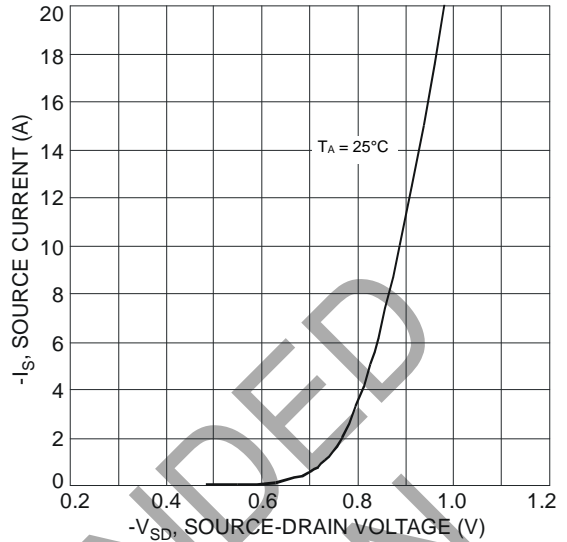


Fig. 10 Diode Forward Voltage vs. Current

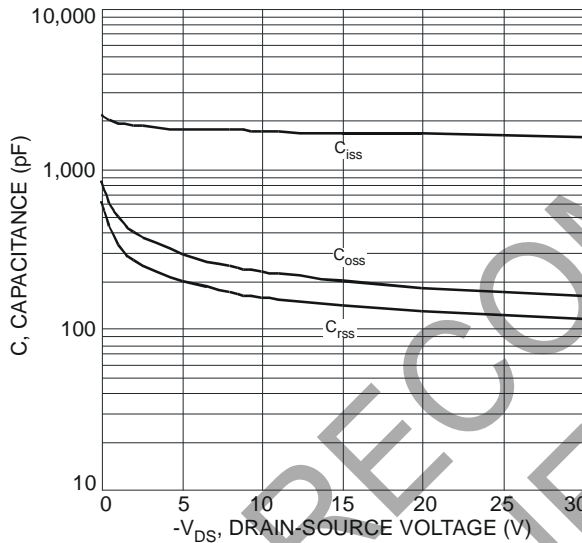


Fig. 11 Typical Total Capacitance

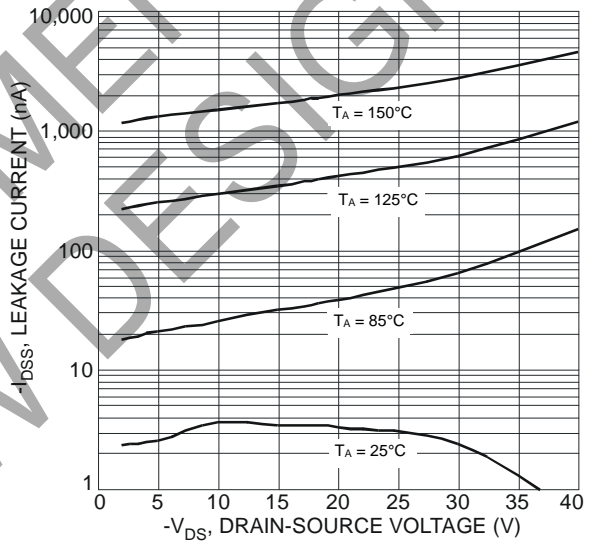


Fig. 12 Typical Leakage Current vs. Drain-Source Voltage

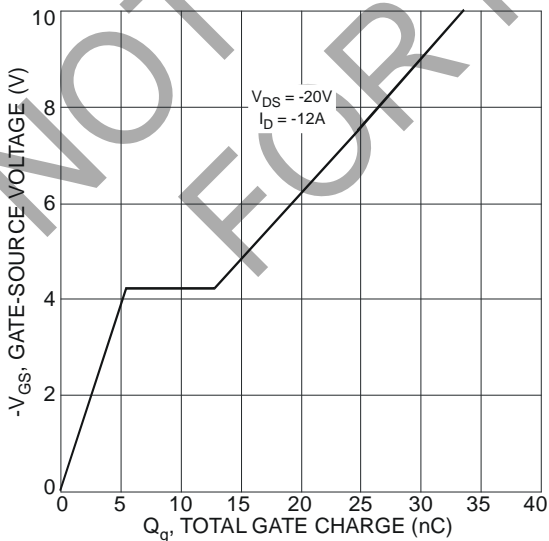
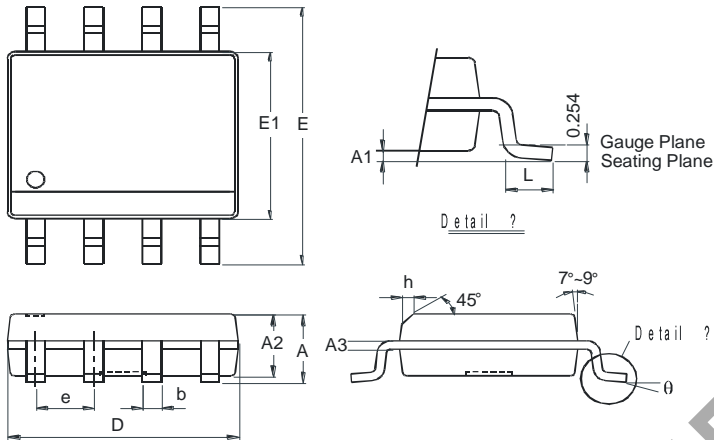


Fig. 13 Gate-Charge Characteristics

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SO-8

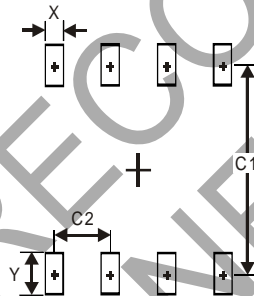


SO-8		
Dim	Min	Max
A	-	1.75
A1	0.10	0.20
A2	1.30	1.50
A3	0.15	0.25
b	0.3	0.5
D	4.85	4.95
E	5.90	6.10
E1	3.85	3.95
e	1.27 Typ	
h	-	0.35
L	0.62	0.82
θ	0°	8°
All Dimensions in mm		

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

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Dimensions	Value (in mm)
X	0.60
Y	1.55
C1	5.4
C2	1.27

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