

**DUAL P-CHANNEL ENHANCEMENT MODE MOSFET**

**Product Summary** (Typ. @  $V_{GS} = -4.5V$ ,  $T_A = +25^{\circ}C$ )

$BV_{DSS}$	$R_{DS(ON)}$	$Q_g$	$Q_{gd}$	$I_D$
-20V	80m $\Omega$	3.3nC	0.6nC	-4A

**Description**

This new generation MOSFET is designed to minimize the on-state resistance ( $R_{D1D2(ON)}$ ) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

**Applications**

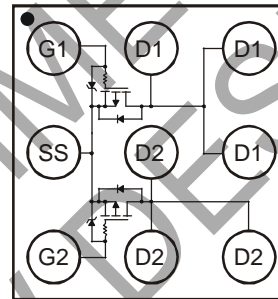
- Battery Management
- Load Switch
- Battery Protection

**Features and Benefits**

- LD-MOS Technology with the Lowest Figure of Merit:  
 $R_{DS(ON)} = 80m\Omega$  to Minimize On-State Losses  
 $Q_g = 3.3nC$  for Ultra-Fast Switching
- $V_{gs(th)} = -0.7V$  Typ. for a Low Turn-On Potential
- CSP with Footprint 1.5mm x 1.5mm
- Height = 0.62mm for Low Profile
- ESD = 3kV HBM Protection of Gate
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

**Mechanical Data**

- Case: U-WLB1515-9
- Terminal Connections: See Diagram Below
- Weight: 0.0018 grams (Approximate)



Top View

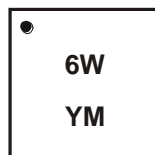
**Ordering Information** (Note 4)

Part Number	Case	Packaging
DMP2100UCB9-7	U-WLB1515-9	3000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

**Marking Information**

U-WLB1515-9



6W = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: F = 2018)  
 M = Month (ex: 9 = September)

Date Code Key

Year	2015	2016	2017	2018	2019	2020	2021
Code	C	D	E	F	G	H	I

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V <sub>D1D2</sub>	-20	V	
Gate-Source Voltage	V <sub>GS</sub>	-6	V	
Continuous Drain Current (Note 5) V <sub>GS</sub> = -4.5V	I <sub>D1D2</sub>	T <sub>C</sub> = +25°C T <sub>C</sub> = +70°C	-3.0 -2.1	A
Continuous Drain Current (Note 6) V <sub>GS</sub> = -4.5V		T <sub>C</sub> = +25°C T <sub>C</sub> = +70°C	-4.0 -3.0	A
Continuous Source Pin Current (Note 6)	I <sub>S</sub>	-2.0	A	
Continuous Gate Clamp Current (Note 6)	I <sub>G</sub>	-0.4	A	
Pulsed Source Pin Current (Pulse Duration 10μs, Duty Cycle ≤ 1%)	I <sub>SM</sub>	-15	A	
Pulsed Drain Current (Pulse Duration 10μs, Duty Cycle ≤ 1%)	I <sub>DM</sub>	-28	A	
Pulsed Gate Clamp Current (Pulse Duration 10μs, Duty Cycle ≤ 1%)	I <sub>GM</sub>	-6	A	

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	0.8	W
Total Power Dissipation (Note 6)	P <sub>D</sub>	1.6	W
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	152	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θJA</sub>	65	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>D1D2</sub>	-20	—	—	V	V <sub>GS</sub> = 0V, I <sub>D1D2</sub> = -250μA
Gate-Source Breakdown Voltage	BV <sub>GSS</sub>	-6.1	—	—	V	I <sub>GS</sub> = -250μA, V <sub>D1D2</sub> = 0V
Zero Gate Voltage Drain Current @T <sub>C</sub> = +25°C	I <sub>DDS</sub>	—	—	-1	μA	V <sub>D1D2</sub> = -16V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	-100	nA	V <sub>GS</sub> = -6V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.4	-0.7	-0.9	V	V <sub>D1D2</sub> = V <sub>GS</sub> , I <sub>DS</sub> = -250μA
Static Drain-Source On-Resistance	R <sub>D1D2(ON)</sub>	—	80	100	mΩ	V <sub>GS</sub> = -4.5V, I <sub>D1D2</sub> = -1A
		—	105	130		V <sub>GS</sub> = -2.5V, I <sub>D1D2</sub> = -1A
		—	140	175		V <sub>GS</sub> = -1.8V, I <sub>D1D2</sub> = -1A
Forward Transfer Admittance	Y <sub>fs</sub>	—	5.3	—	S	V <sub>D1D2</sub> = -10V, I <sub>D1D2</sub> = -1A
<b>DIODE CHARACTERISTICS</b>						
Diode Forward Voltage (Note 6)	V <sub>SD</sub>	—	-0.7	-1	V	V <sub>GS</sub> = 0V, I <sub>D1D2</sub> = -1A
Reverse Recovery Charge	Q <sub>rr</sub>	—	18	—	nC	V <sub>dd</sub> = -9.5V, I <sub>F</sub> = -1A,
Reverse Recovery Time	t <sub>rr</sub>	—	34	—	ns	di/dt = 200A/μs
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iss</sub>	—	232	310	pF	V <sub>D1D2</sub> = -10V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	107	150	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	43.5	55	pF	
Total Gate Charge	Q <sub>g</sub>	—	3.3	4.2	nC	V <sub>GS</sub> = -4.5V, V <sub>D1D2</sub> = -10V, I <sub>D1D2</sub> = -1A
Gate-Source Charge	Q <sub>gs</sub>	—	0.3	—	nC	
Gate-Drain Charge	Q <sub>gd</sub>	—	0.6	—	nC	
Gate Charge at V <sub>th</sub>	Q <sub>g(th)</sub>	—	0.2	—	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	—	8.5	—	ns	V <sub>D1D2</sub> = -10V, V <sub>GS</sub> = -4.5V, I <sub>D1D2</sub> = -1A, R <sub>G</sub> = 30Ω
Turn-On Rise Time	t <sub>R</sub>	—	7.0	—	ns	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	47	—	ns	
Turn-Off Fall Time	t <sub>F</sub>	—	28	—	ns	

- Notes:
- Device mounted on FR-4 PCB with minimum recommended pad layout.
  - Device mounted on FR-4 material with 1-inch<sup>2</sup> (6.45-cm<sup>2</sup>), 2-oz. (0.071-mm thick) Cu.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to production testing.

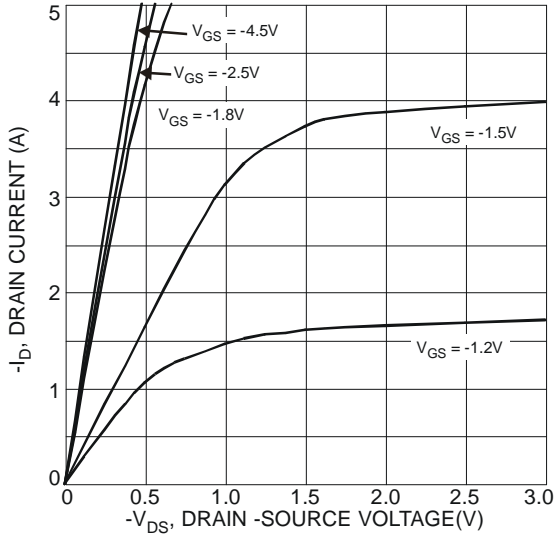


Fig. 1 Typical Output Characteristics

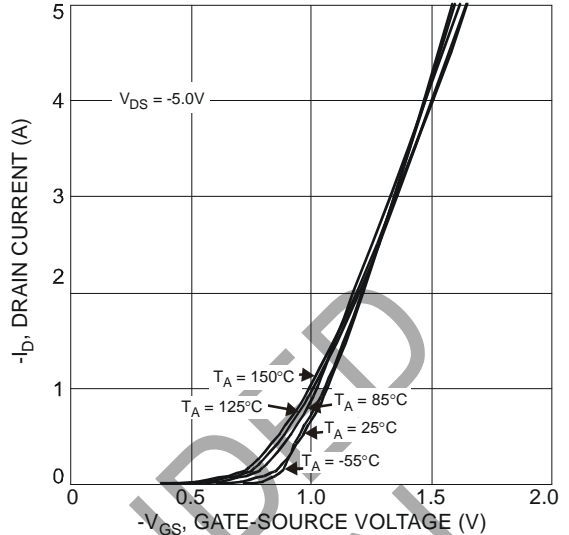


Fig. 2 Typical Transfer Characteristics

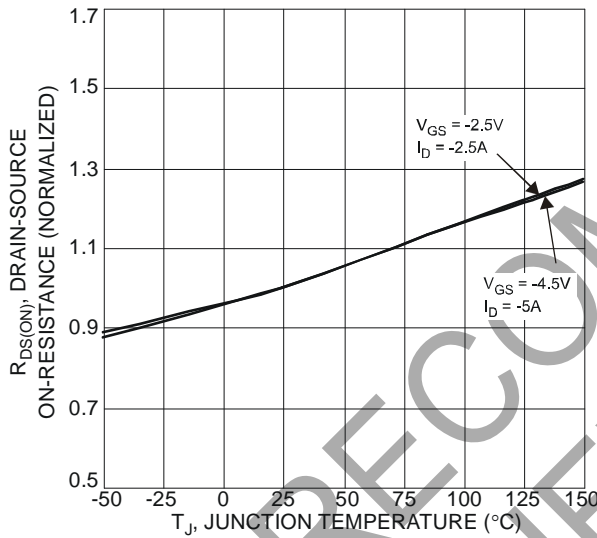


Fig. 3 On-Resistance Variation with Temperature

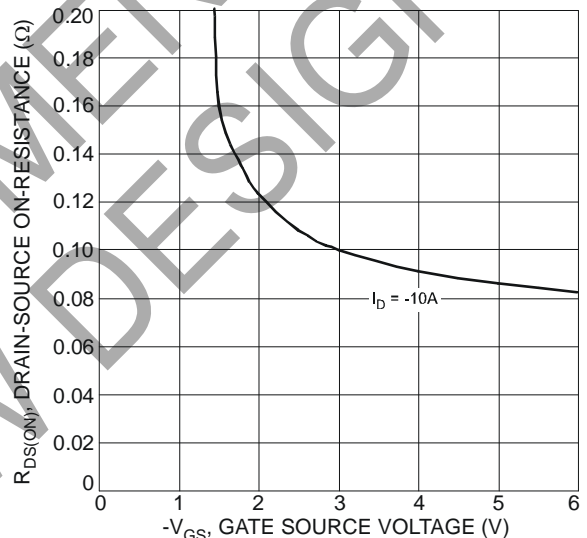


Fig. 4 Typical On-Resistance vs. Drain Current and Gate Voltage

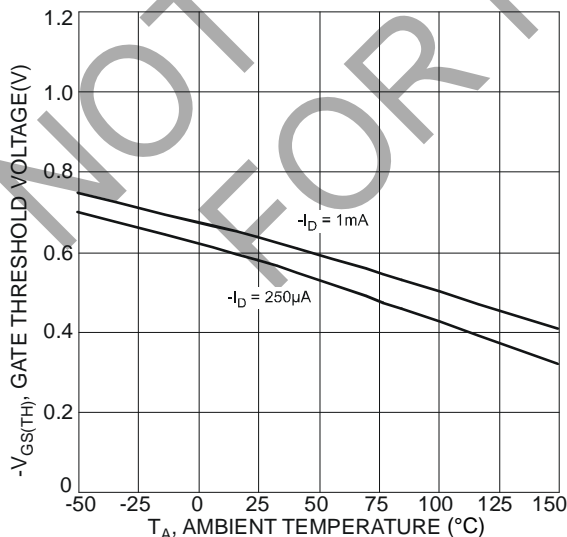


Fig. 5 Gate Threshold Variation vs. Ambient Temperature

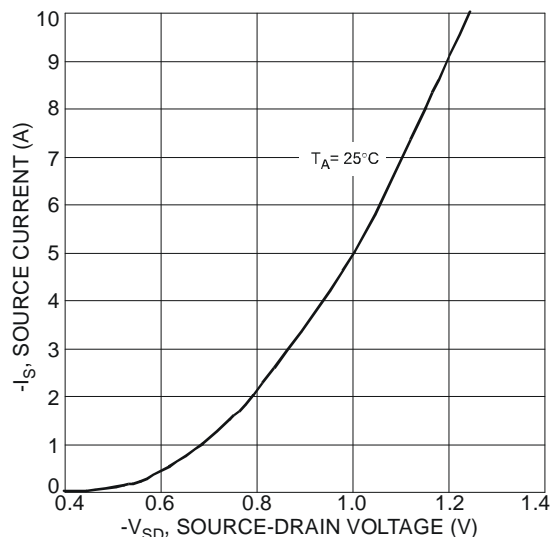


Fig. 6 Diode Forward Voltage vs. Current

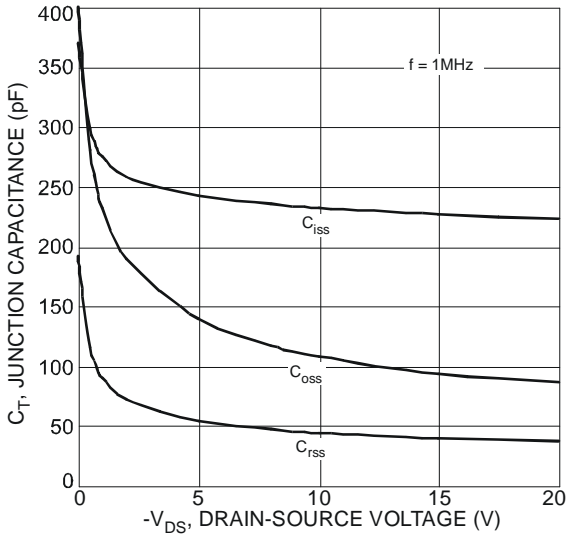


Fig. 7 Typical Junction Capacitance

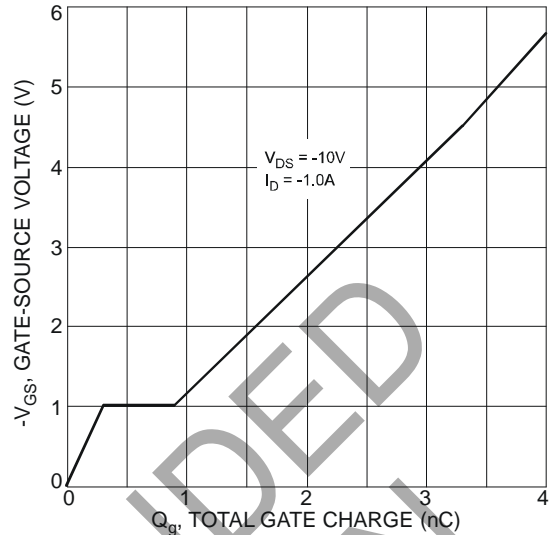


Fig. 8 Gate-Charge Characteristics

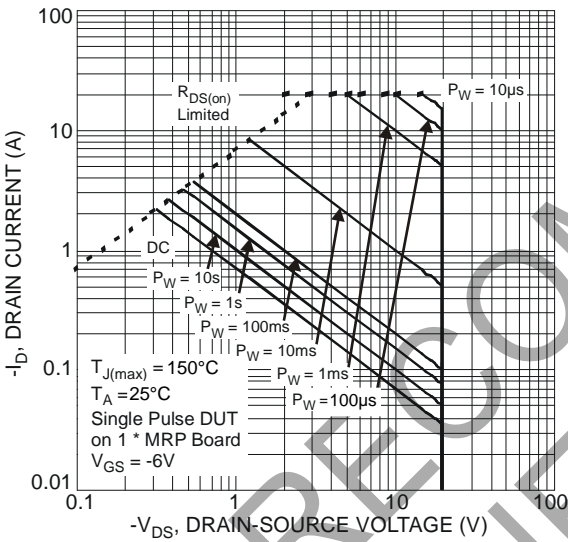


Fig. 9 SOA, Safe Operation Area

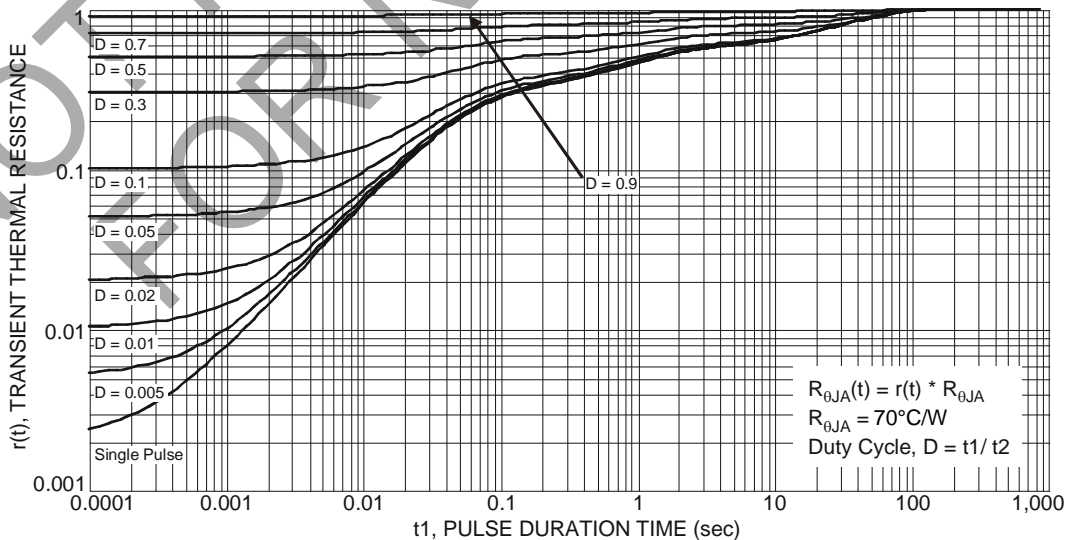
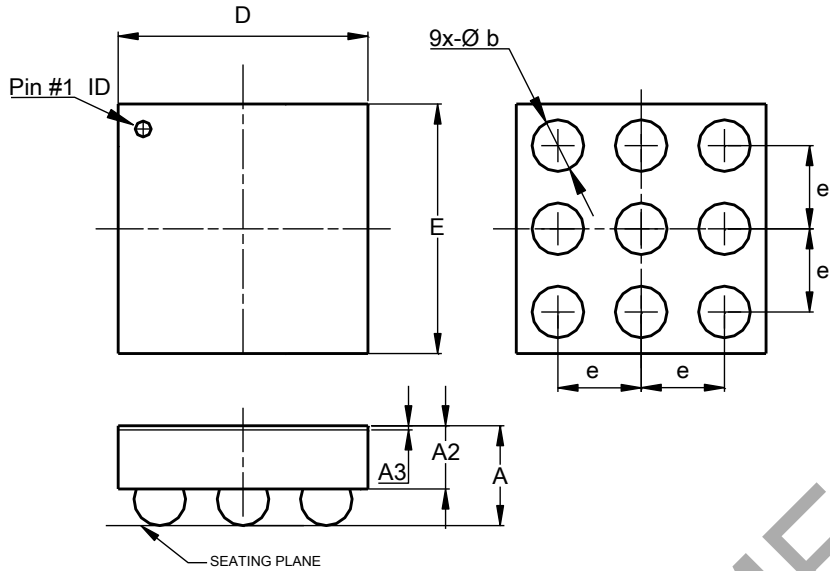


Fig. 10 Transient Thermal Resistance

**Package Outline Dimensions**

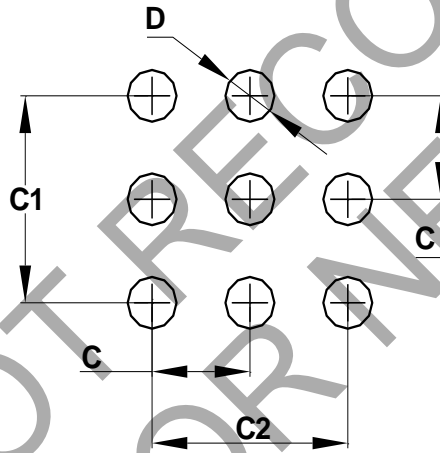
Please see <http://www.diodes.com/package-outlines.html> for the latest version.



U-WLB1515-9			
Dim	Min	Max	Typ
A	--	0.62	--
A2	--	0.36	0.36
A3	0.020	0.030	0.025
b	0.27	0.37	0.32
D	1.47	1.50	1.49
E	1.47	1.50	1.49
e	--	--	0.50
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



Dimensions	Value (in mm)
C	0.50
C1	1.00
C2	1.00
D	0.25

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