



### DMJ70H1D5SV3

#### N-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

BV <sub>DSS</sub>	Rds(on) max	I <sub>D MAX</sub> T <sub>C</sub> = +25°C
700V	1.5Ω @ V <sub>GS</sub> = 10V	5.0A

### **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

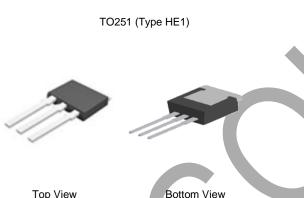
- Adaptors
- LCDs & PDP TVs
- Lighting

### **Features and Benefits**

- Low On-Resistance
- High BV<sub>DSS</sub> Rating for Power Application
- Low Input Capacitance
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

#### Mechanical Data

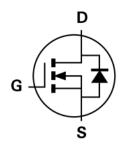
- Package: TO251
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.33 grams (Approximate)







Top View Pin Configuration



Internal Schematic

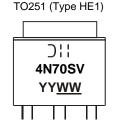
### Ordering Information (Note 4)

Part Number	Paakaga	Packing		
Part Number	Package	Qty.	Carrier	
DMJ70H1D5SV3	TO251 (Type HE1)	75 pieces	Tube	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## **Marking Information**



☐ : = Manufacturer's Marking 4N70SV = Product Type Marking Code YYWW = Date Code Marking YY or YY= Last Two Digits of Year (ex: 22 = 2022) WW or  $\overline{WW}$  = Week Code (01 to 53)



# **Maximum Ratings** (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	VDSS	700	V	
Gate-Source Voltage		Vgss	±30	V
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	$T_C = +25$ °C $T_C = +100$ °C	I <sub>D</sub>	5.0 3.2	А
Maximum Body Diode Forward Current (Note 6)	•	Is	3.0	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I <sub>DM</sub>	6.0	А
Avalanche Current (Note 7)	L = 60mH	I <sub>AS</sub>	0.5	А
Avalanche Energy (Note 7)	L = 60mH	Eas	7.5	mJ
Peak Diode Recovery dv/dt (Note 7)		dv/dt	5.2	V/ns

### Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	PD	78 31	W
Thermal Resistance, Junction to Ambient (Note 6)		Reja	80	°C/W
Thermal Resistance, Junction to Case (Note 5)		Reлc	1.8	C/VV
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

### Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BVDSS	700		_	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	-		1	μA	V <sub>DS</sub> = 700V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	Igss	_	_	100	nA	$V_{GS} = \pm 30V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)				•			
Gate Threshold Voltage	VGS(TH)	2	3.4	4	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>		1.25	1.5	Ω	$V_{GS} = 10V, I_D = 1A$	
Diode Forward Voltage	VsD		0.85	1.3	V	V <sub>G</sub> S = 0V, I <sub>S</sub> = 1A	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	$C_{iss}$	_	316	_		V <sub>DS</sub> = 50V, f = 1MHz, V <sub>GS</sub> = 0V	
Output Capacitance	Coss	_	124	_	pF		
Reverse Transfer Capacitance	Crss	_	3.9	_			
Gate Resistance	Rg	_	2.2	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge	Qg	_	9.8	_		V <sub>DD</sub> = 560V, I <sub>D</sub> = 3.2A, V <sub>GS</sub> = 10V	
Gate-Source Charge	Qgs	_	1.3	_	nC		
Gate-Drain Charge	Qgd	_	5.3	_			
Turn-On Delay Time	t <sub>D</sub> (ON)	_	7.5	_		$V_{DD} = 350V$ , $V_{GS} = 10V$ , $R_g = 4.7\Omega$ , $I_D = 3.2A$	
Turn-On Rise Time	t <sub>R</sub>	_	10	_			
Turn-Off Delay Time	tD(OFF)	_	21	_	ns		
Turn-Off Fall Time	tF	_	5	_			
Body Diode Reverse Recovery Time	trr	_	190	_	ns		
Body Diode Reverse Recovery Time (T <sub>J</sub> = +150°C)	trr	_	255	_	ns	1- 2.24 41/45 4.004/5-	
Body Diode Reverse Recovery Charge	Qrr	_	1.4	_	μC	Is = 3.2A, dl/dt = 100A/µs	
Body Diode Reverse Recovery Charge (T <sub>J</sub> = +150°C)	Qrr	_	2.0	_	μC	]	

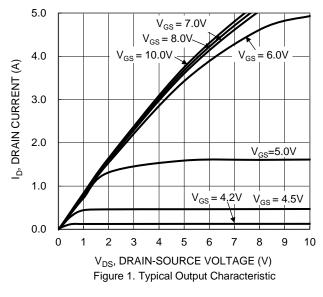
Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout

6. Device mounted on FR-4 substrate PC board, 2oz. copper, with minimum recommended pad layout.

7. Guaranteed by design. Not subject to production testing.

8. Short duration pulse test used to minimize self-heating effect.





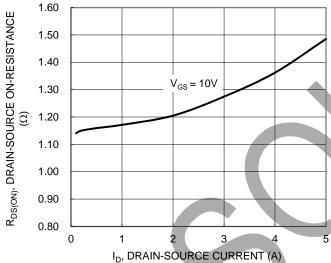


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

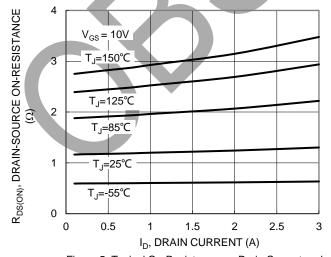
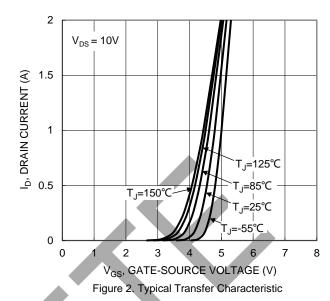


Figure 5. Typical On-Resistance vs. Drain Current and Temperature



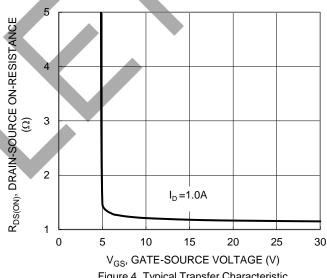


Figure 4. Typical Transfer Characteristic

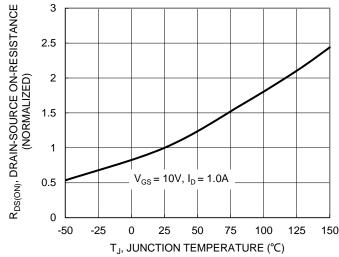


Figure 6. On-Resistance Variation with Temperature



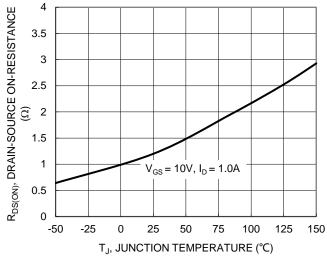
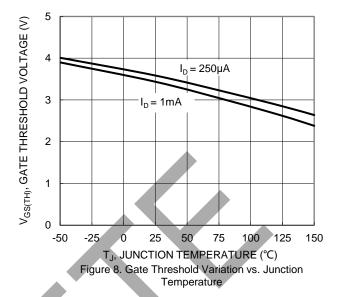
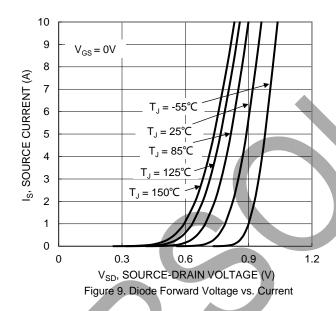


Figure 7. On-Resistance Variation with Temperature





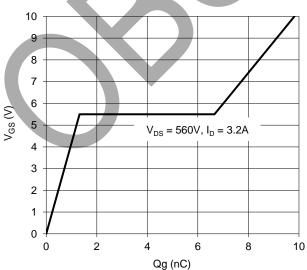
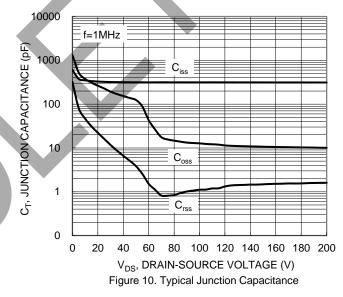
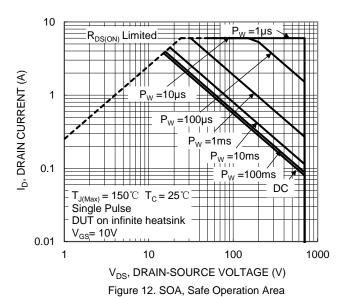
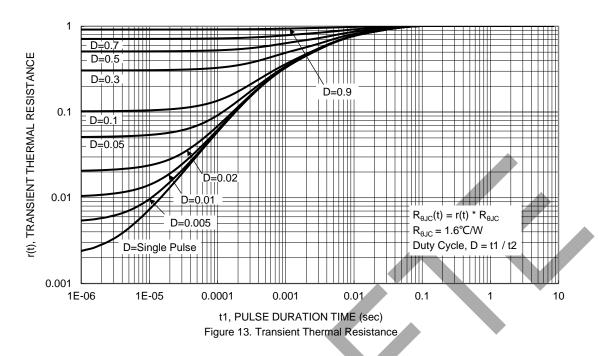


Figure 11. Gate Charge



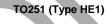


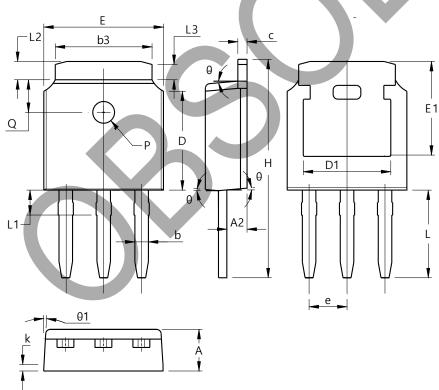




## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.





TO251 (Type HE1)						
Dim	Min	Max	Тур			
Α	2.20	2.40	2.30			
A2	0.97	1.17	1.07			
b	0.68	0.90	0.78			
b3	5.20	5.50	5.33			
С	0.43	0.63	0.53			
D	5.98	6.22	6.10			
D1	5.30 REF					
е	2.286 BSC					
Е	6.40	6.80	6.60			
E1	4.63	5.03	4.83			
Н	10.00	11.44	11.22			
k	C	).40REF				
L	3.90	4.30	4.10			
L1	0.85	1.25	1.05			
L2	0.88	1.28	1.02			
L3	0.75 REF					
Q	1.65	1.95	1.80			
PØ	1.20					
θ	5°	5° 9°				
θ1	5° 9° 7°					
All Dimensions in mm						



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