

Product Summary

BV_{DSS}	R_{DS(ON)} Max	I_D T_C = +25°C
700V	1.3Ω @ V _{GS} = 10V	4.6A

Features and Benefits

- Low On-Resistance
- High BV_{DSS} Rating for Power Application
- Low Input Capacitance
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

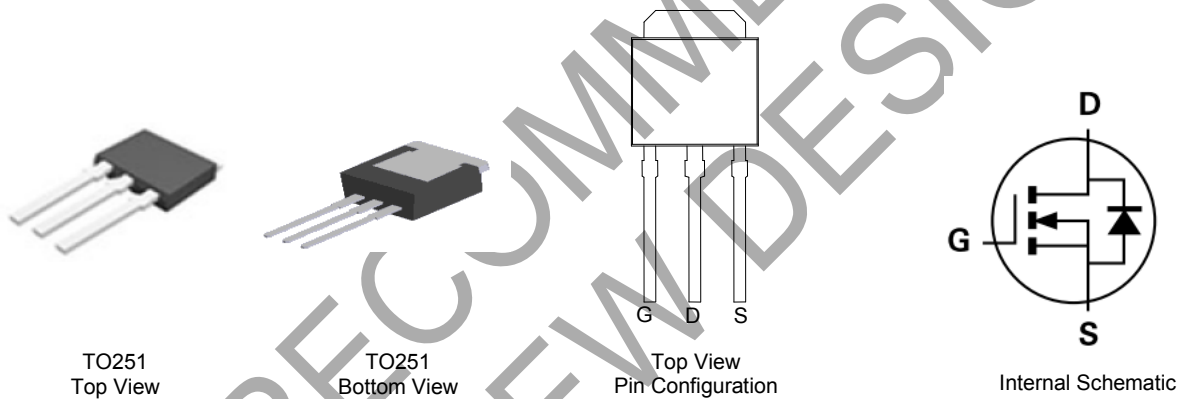
Description and Applications

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}), yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Motor Control
- Backlighting
- DC-DC Converters
- Power Management Functions

Mechanical Data

- Case: TO251
- Case Material: Molded Plastic, "Green" Molding Compound.
UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin Annealed over Copper Leadframe.
Solderable per MIL-STD-202, Method 208
- Weight: 0.33 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMJ70H1D3SJ3	TO251	75 Pieces/Tube

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



= Manufacturer's Marking
 7N70SJ = Product Type Marking Code
 YYWW = Date Code Marking
 YY or YY = Last Digit of Year (ex: 16 = 2016)
 WW or WW = Week Code (01 to 53)

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V_{DSS}	700	V
Gate-Source Voltage	V_{GSS}	± 30	V
Continuous Drain Current (Note 5) $V_{GS} = 10\text{V}$	I_D	$T_C = +25^\circ\text{C}$ 4.6	A
		$T_C = +100^\circ\text{C}$ 2.9	
Maximum Body Diode Forward Current (Note 6)	I_S	3.0	A
Pulsed Drain Current (10 μs Pulse, Duty Cycle = 1%)	I_{DM}	5.4	A
Avalanche Current (Note 7)	I_{AS}	L = 60mH 1.1	A
Avalanche Energy (Note 7)		L = 60mH 40	
Peak Diode Recovery dv/dt (Note 7)	dv/dt	5	V/ns

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P_D	$T_C = +25^\circ\text{C}$ 41	W
		$T_C = +100^\circ\text{C}$ 16	
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	79	$^\circ\text{C/W}$
Thermal Resistance, Junction to Case (Note 5)	$R_{\theta JC}$	3.0	
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV_{DSS}	700	—	—	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1	μA	$V_{DS} = 700\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	100	nA	$V_{GS} = \pm 30\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(TH)}$	2	2.9	4	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	1.0	1.3	Ω	$V_{GS} = 10\text{V}, I_D = 2.5\text{A}$
Diode Forward Voltage	V_{SD}	—	0.9	1.3	V	$V_{GS} = 0\text{V}, I_S = 5\text{A}$
DYNAMIC CHARACTERISTICS (Note 7)						
Input Capacitance	C_{ISS}	—	351	—	pF	$V_{DS} = 50\text{V}, f = 1\text{MHz}, V_{GS} = 0\text{V}$
Output Capacitance	C_{OSS}	—	66	—		
Reverse Transfer Capacitance	C_{RSS}	—	1.1	—		
Gate Resistance	R_G	—	3.5	—	Ω	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Total Gate Charge	Q_G	—	13.9	—	nC	$V_{DD} = 560\text{V}, I_D = 5\text{A}, V_{GS} = 10\text{V}$
Gate-Source Charge	Q_{GS}	—	1.9	—		
Gate-Drain Charge	Q_{GD}	—	8.5	—		
Turn-On Delay Time	$t_{D(ON)}$	—	8.5	—	ns	$V_{DD} = 350\text{V}, V_{GS} = 10\text{V}, R_G = 4.7\Omega, I_D = 2.5\text{A}$
Turn-On Rise Time	t_R	—	11.6	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	24.5	—		
Turn-Off Fall Time	t_F	—	10	—		
Body Diode Reverse Recovery Time	t_{RR}	—	212	—	ns	$I_S = 5\text{A}, di/dt = 100\text{A}/\mu\text{s}$
Body Diode Reverse Recovery Time ($T_J = +150^\circ\text{C}$)	t_{RR}	—	251	—	ns	
Body Diode Reverse Recovery Charge	Q_{RR}	—	1.8	—	μC	
Body Diode Reverse Recovery Charge ($T_J = +150^\circ\text{C}$)	Q_{RR}	—	2.3	—	μC	

- Notes:
- Device mounted on infinite heatsink.
 - Device mounted on FR-4 substrate PC board, 2oz. copper, with minimum recommended pad layout.
 - Guaranteed by design. Not subject to production testing.
 - Short duration pulse test used to minimize self-heating effect.

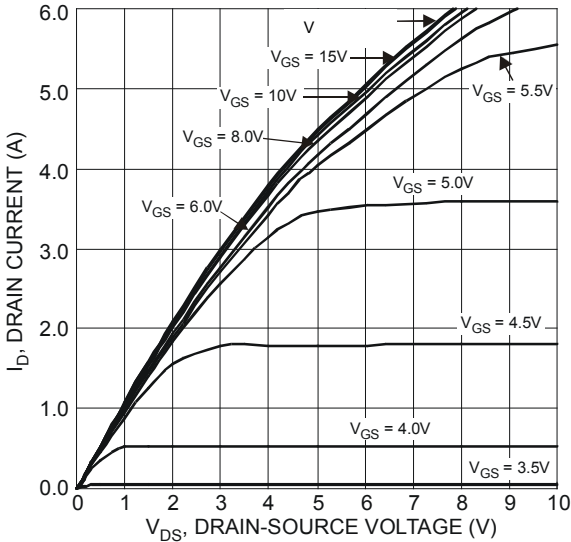


Figure 1 Typical Output Characteristics

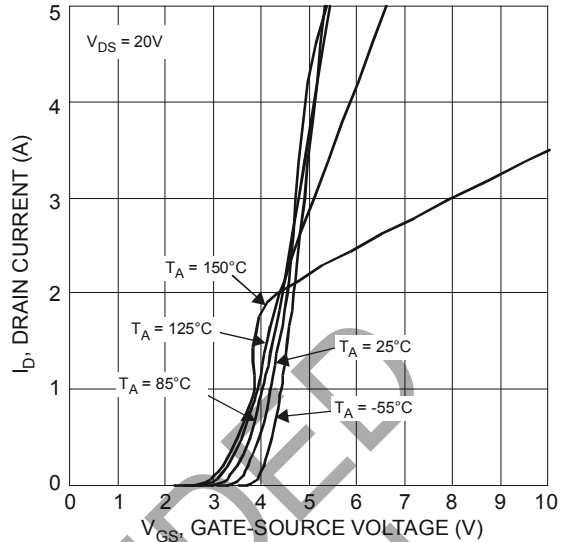


Figure 2 Typical Transfer Characteristics

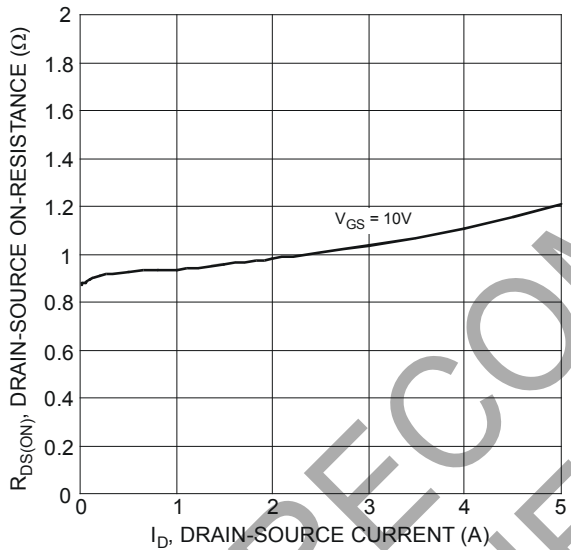


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

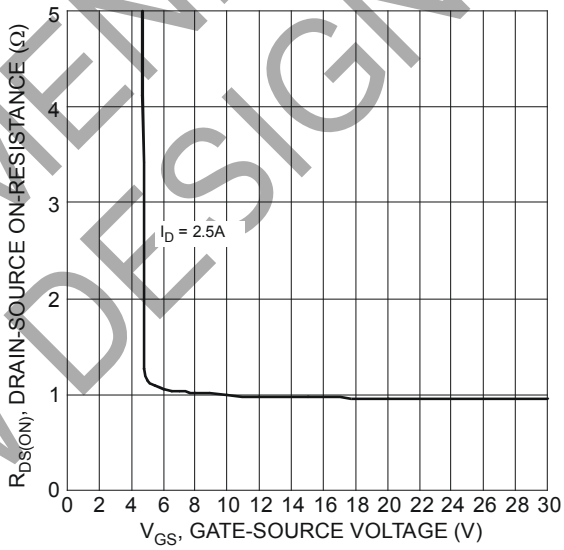


Figure 4 Typical Transfer Characteristics

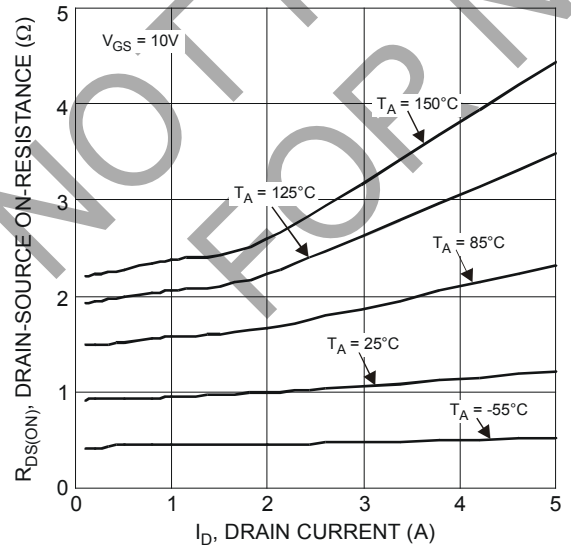


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

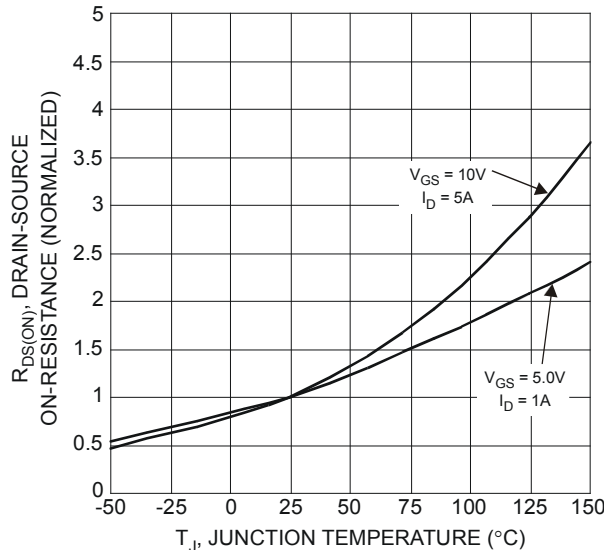


Figure 6 On-Resistance Variation with Temperature

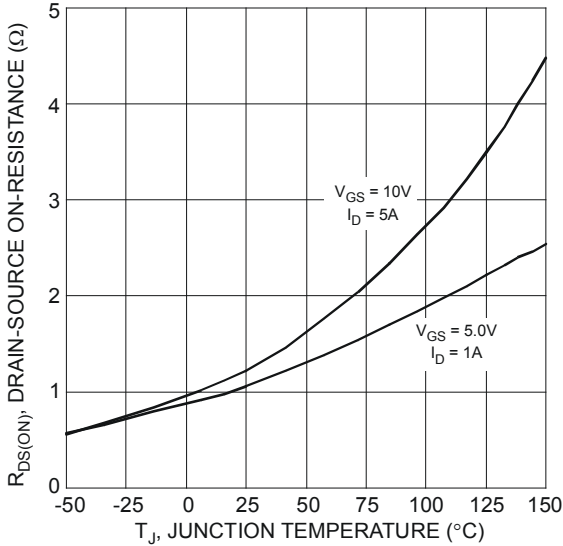


Figure 7 On-Resistance Variation with Temperature

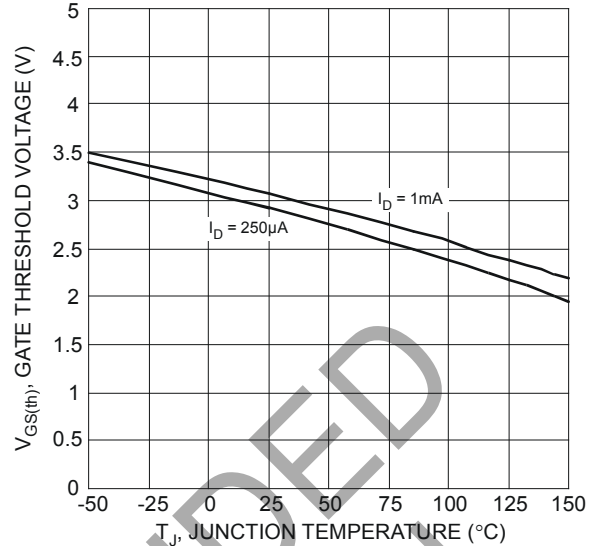


Figure 8 Gate Threshold Variation vs. Ambient Temperature

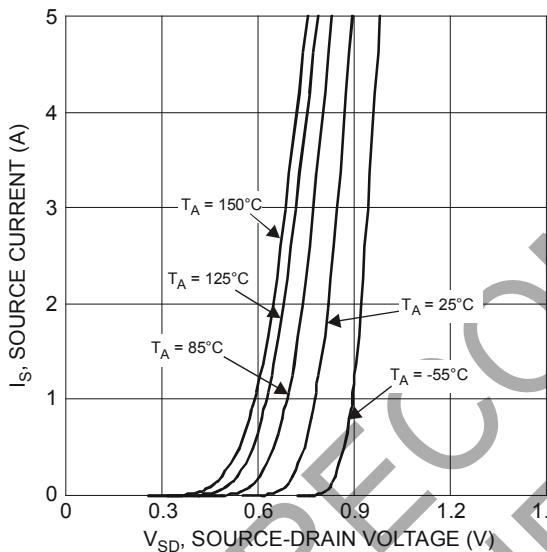


Figure 9 Diode Forward Voltage vs. Current

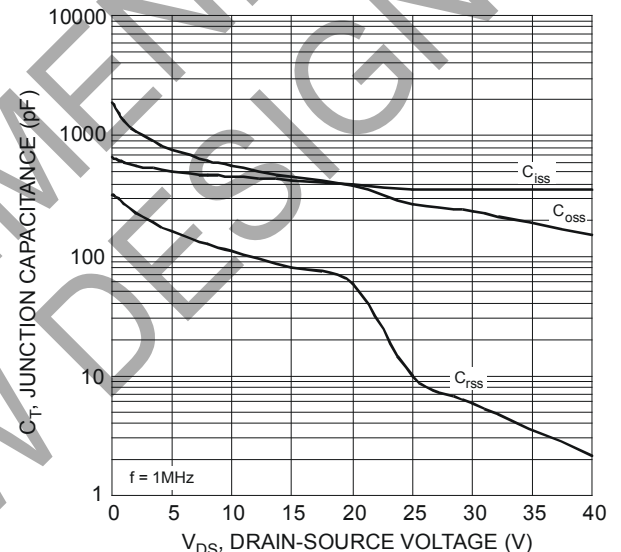


Figure 10 Typical Junction Capacitance

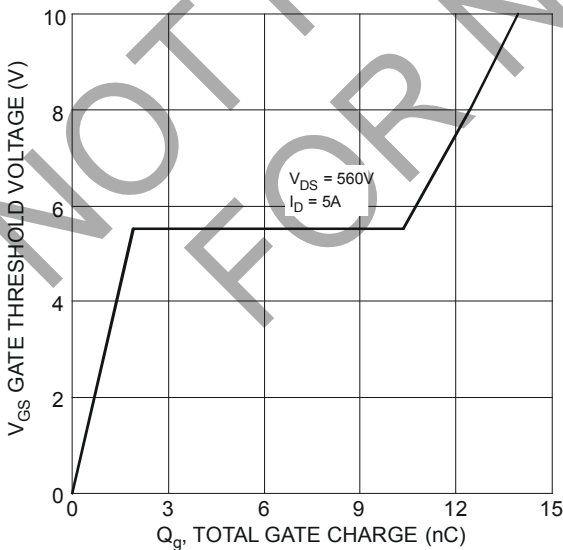


Figure 11 Gate Charge

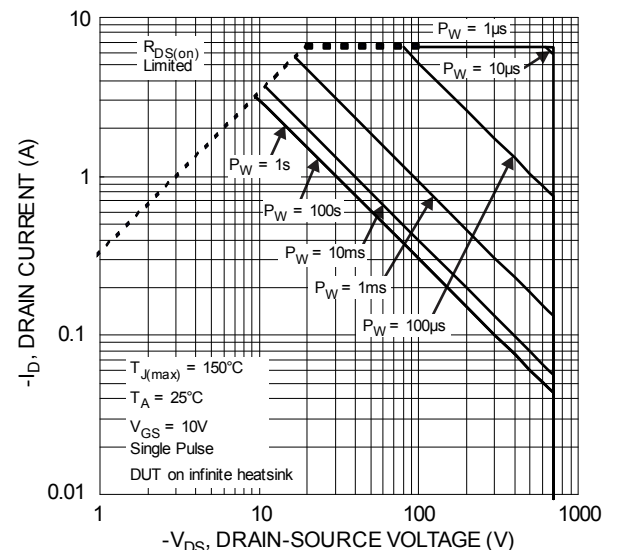
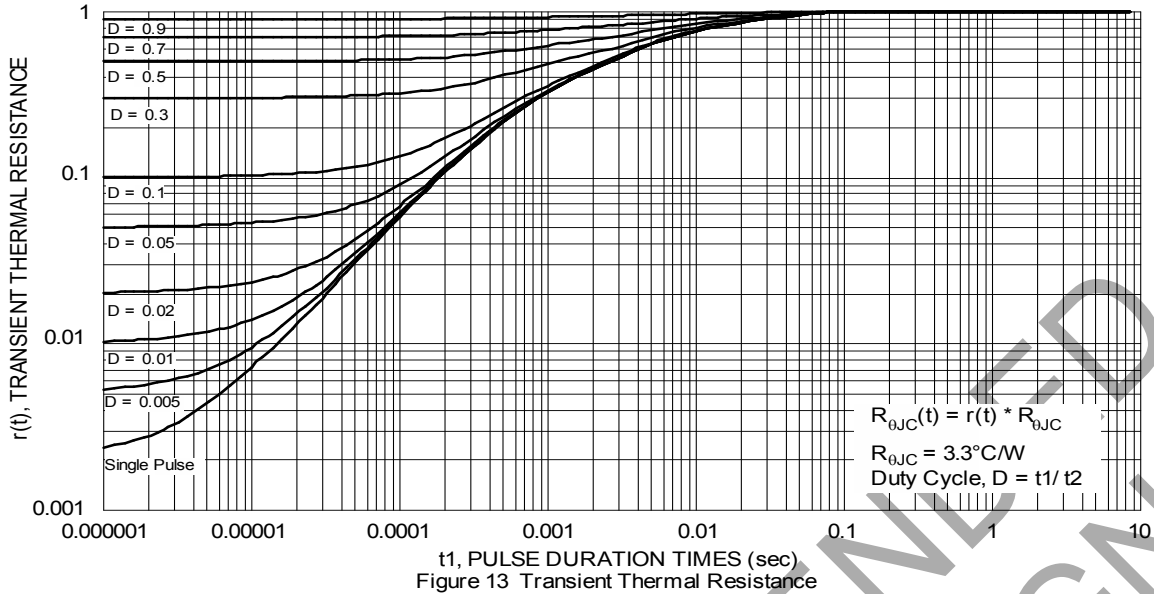


Figure 12 SOA, Safe Operation Area



NOT RECOMMENDED FOR NEW DESIGN

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