



AH9485/86



## SINGLE PHASE LOW NOISE SMART MOTOR DRIVER

## Description

The AH9485 and AH9486 are single chip solutions for driving a single phase (coil) brushless DC motors. It integrates the motor driver and the high sensitivity Hall effect sensor to simplify the system circuit and PCB design for simpler solutions.

Rotor lock-detect, shutdown and auto-restart functions help to prevent the motor from over-heating when rotor is locked and restarts the motor after the lock is removed.

The AH9485 and AH9486 have built-in open drain tachometer frequency generator (FG) and rotation detection (RD) outputs to allow external interface to monitor motor rotation, speed or rotor locks conditions easily. The FG provides magnetic change frequency while the RD provides the rotor status.

The full bridge output stage uses "soft-switching" technique to minimize the audible switching noise (vibration and acoustic noise) and electromagnetic interference (EMI) providing a low noise solution.

Over-temperature shutdown provides thermal protection for the device. An external DC voltage or a PWM (pulse-width modulation) signal on the PWM pin can be used to control the motor speed.

The AH9485 and AH9486 are packaged in TSOT26F.

## Features

- Internal Full Bridge Driver
- High Sensitivity Integrated Hall Sensor
- Low and Wide Supply Voltage Range of 2V to 6V
- Soft Switching for Low Noise
- Rotor Lock Protection and Auto-Restart
- DC Voltage or PWM Signal Speed Control
- Thermal Protection
- Tachometer FG Output(AH9485) or Rotor Lock Alarm RD Output (AH9486)
- Operating Temperature Range of -40°C to +105°C
- Flat Pin TSOT26F Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

# Pin Assignments

## (Top View)



# Applications

- 5V Low Noise BLDC Cooling Fans
- Low Voltage / Low Power BLDC Motors
- Notebook / VGA DC Fans / Blowers
  - Micro-Motors



# Typical Applications Circuit (Note 4)



Note 4: Must use C1 capacitor for the decoupling between V<sub>DD</sub> and GND and place the capacitor as close to the IC as possible. ZD1: V<sub>DD</sub> over-voltage stress protection diode.

R2: current limit resistor for Open Drain RD output. R3: de-coupling resistor for long-distance signal transmission.

## **Pin Descriptions**

Pin Number	Pin Name	Function
1	FG/RD	FG(AH9485) or RD(AH9486)
2	GND	Ground pin
3	OUT1	Output pin 1
4	OUT2	Output pin 2
5	PWM	DC voltage or Pulse width modulation signal speed control pin
6	V <sub>DD</sub>	Power supply pin

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# **Functional Block Diagram**





### Absolute Maximum Ratings (Notes 5 and 6) (@TA=+25°C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit
V <sub>DD</sub>	Supply Voltage	7	V
I <sub>OUTP</sub>	Peak Output Current	500	mA
I <sub>OUTC</sub>	Continuous Output Current	300	mA
V <sub>FG/RD</sub>	FG/RD Pull-up Voltage	7 or V <sub>DD</sub>	V
I <sub>FG/RD</sub>	FG/RD Current	20	mA
P <sub>D</sub>	Power Dissipation	520	mW
$\theta_{JA}$	Thermal Resistance (Junction to Ambient)	141	°C/W
T <sub>STG</sub>	Storage Temperature	-55 to +150	٥C
ESD	ESD (Human Body Model) (Note 7)	2000	V

Note: 5. Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

6. The absolute maximum V<sub>DD</sub> of 7V is a transient stress rating and is not meant as a functional operating condition. It is not recommended to operate the device at the absolute maximum rated conditions for any period of time.

7. Electronic semiconductor products are sensitive to Electro Static Discharge (ESD). Always observe Electro Static Discharge control procedures whenever handling semiconductor products.

## **Recommended Operating Conditions**

Symbol	Parameter	Min	Мах	Unit
V <sub>DD</sub>	Supply Voltage	2	6	V
T <sub>A</sub>	Ambient Temperature	-40	+105	°C

## Electrical Characteristics (@V<sub>DD</sub> = 5V, T<sub>A</sub>=+25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
I <sub>DD</sub>	Supply Current	_	-	1.8	3	mA
R <sub>DSON1</sub>	ON Resistance (R <sub>PMOS</sub> +R <sub>NMOS</sub> )	I <sub>OUT</sub> = 300mA	-	2	_	Ω
R <sub>DSON2</sub>	OUT1, OUT2 ON Resistance	T <sub>J</sub> =+125°C, I <sub>OUT</sub> =300mA	_	3	_	Ω
V <sub>OL</sub>	FG/RD Output Low Voltage	I <sub>OL</sub> = 10mA	_	0.33	0.5	V
I <sub>LEAK</sub>	FG/RD Output Leakage Current	$V_{FG}(V_{RD}) = 5V$	-	0.15	10	μA
t <sub>ON</sub>	Looked Poter Period	_	_	0.25	_	s
t <sub>OFF</sub>		_	-	1.5	-	s
PWM <sub>HIGH</sub>	DW/M Voltage Pange	_	>0.5×V <sub>DD</sub>	_	V <sub>DD</sub>	V
PWMLOW	r www.vollage Nalige	_	0	_	<0.14×V <sub>DD</sub>	V



## Magnetic Characteristics (@V<sub>DD</sub> =5V, TA=+25°C, unless otherwise specified.)

Symbol	Parameter	Min	Тур	Max	Unit
B <sub>OP</sub>	Operating Point	0	25	50	Gauss
B <sub>RP</sub>	Releasing Point	-50	-25	0	Gauss
B <sub>HYS</sub>	Hysteresis	_	50	-	Gauss

# **Magnetic Hysteresis Characteristics**



# Output Waveforms Description (Notes 8 and 9)



Notes: 8. RD and FG are open drain outputs and the high level of the signal depends on the external supply voltage. 9. The on torque time of motor is same as the re-start time.



AH9485/86

# **Test Circuit**





### Application Note (Notes 10, 11 and 12)

#### **DC Supply Voltage Speed Control**

Motor speed can be controlled by varying the V<sub>DD</sub> supply voltage while PWM pin is tied to V<sub>DD</sub> pin.

### **PWM Pin Speed Control**

PWM (pulse width modulation) pin can be used to control the motor speed. There are two types of speed control using PWM pin: a) DC Voltage Mode

b) Pulse Width Mode

#### a) DC Voltage Mode PWM:

DC voltage signal ( $V_{PWM}$ ) on the PWM pin can be used to control the motor speed. Applying a DC voltage to the PWM pin will generate an internal PWM pulses with the "ON" duty cycle based on the applied DC voltage according to the diagram below. The internal duty ratio generated will change from 0% duty at  $V_{PWM} = 0.14V_{DD}$  typically to 100% at  $V_{PWM} = 0.5V_{DD}$ .



Notes: 10. While using pulse mode PWM, the "High" value of the input pulse should be higher than  $0.5 \times V_{DD}$  and the "Low" value should be lower than  $0.14 \times V_{DD}$ .

- 11. The lower-limit for the PWM pulse frequency is 200Hz, and the recommended frequency range is higher than 30kHz where the PWM input pulses will not generate acoustic noise.
- 12. The "PWM" pin contains an internal pull-up resistor, so the AH9485/86 will rotate at full-speed (100% ON) when this pin is left un-connected (floating).

#### b) Pulse Mode PWM:

External PWM pulses can be applied directly to the PWM pin to control the speed. The AH9485/86 will pass this pulse to the coil driver with the original duty-cycle ratio. While using pulse mode PWM, the "High" value of the input pulse should be higher than  $0.5 \times V_{DD}$  and the "Low" value should be lower than  $0.14 \times V_{DD}$ . The lower-limit for the PWM pulse frequency is 200Hz, and the recommended frequency range is higher than 30kHz to help prevent acoustic noise related to the switching.

### Soft Switching

AH9485/86 use soft switching of the motor coil current during commutation to minimize audible switching noise and electromagnetic Interference (EMI) to provide a low noise solution.



## **Performance Characteristics**



### Operating Switch Points BOP, BRP & BHYS VS. Supply Voltage VDD



High+Low Side RDS(ON) vs. Supply Voltage VDD



Supply Current IDD vs. Operating Temperature TA



Operating Switch Points BOP, BRP & BHYS vs. Operating Temperature TA



High+Low Side RDS(ON) vs. Operating Temperature TA



AH9485/86 Document number: DS38108 Rev. 2 - 3



# Ordering Information

		<u>AH948X</u> – <u>XX</u>	<u>x</u> - <u>x</u>			
	[		_ L			
	Product Version	n Pa	ackage	Packing	g	
	5: AH9485	WUF:	F: TSOT26F 7: 7" Tape & Reel			
	6: AH9486					
				7" Tape and F	Reel	
Part Number	Package Code	Packaging	Quantity		Part Number Suffix	
AH9485-WUF-7	WUF	TSOT26F	3000/Tape & R	leel	-7	
AH9486-WUF-7	WUF	TSOT26F	3000/Tape & R	Reel	-7	
				5		
Image: Warking mormation   (Top View)   6 5 4   XX Y XX : Identification Code   Y: Year 0~9 W: Week : A~Z : 1~26 Week; a~z : 27~52 Week; z Represents 52 and 53 Week   1 2 3 X : Internal Code						
	Part Number	Package	ldentifica	ation Code		
	AH9485-WUF-	7 TSOT26F	<u>- н</u>	45		
	AH9486-WUF-	7 TSOT26F		16		
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## Package Outline Dimensions (All dimensions in mm.)

Please see http://www.diodes.com/package-outlines.html for the latest version.

### (1) Package Type: TSOT26F



**Sensor Location** 



## **Mechanical Structure**



Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: TSOT26F



X1



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