

# NOT RECOMMENDED FOR NEW DESIGN USE DMN63D1L



2N7002KX

#### N-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C
60V	2Ω @ V <sub>GS</sub> = 10V	380mA
60 V	$3\Omega$ @ $V_{GS} = 5V$	300mA

#### **Description**

This MOSFET has been designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

#### **Applications**

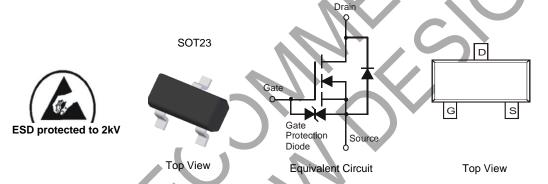
- Motor Control
- Power Management Functions
- Backlighting

#### **Features and Benefits**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected up to 2kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding
   Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Alloy 42
   leadframe. Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.008 grams (approximate)



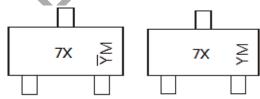
### Ordering Information (Note 4)

Part Number	Case	Packaging
2N7002KX-7	SOT23	3000/Tape & Reel
2N7002KX-13	SOT23	10000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant
- See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- <1000ppm antimony compounds.
  4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

### **Marking Information**



7X= Product Type Marking Code

YM = Date Code Marking for SAT (Shanghai Assembly/ Test site)  $\overline{Y}$ M = Date Code Marking for CAT (Chengdu Assembly/ Test site) Y or  $\overline{Y}$  = Year (ex: A = 2013)

M = Month (ex: 9 = September)

Chengdu A/T Site Shanghai A/T Site

Date Code Key

Year	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017
Code	Т	U	V	W	Χ	Υ	Z	Α	В	С	D	Е
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Drain-Source Voltage	$V_{DSS}$	60	V		
Gate-Source Voltage			$V_{GSS}$	±20	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	380 300	mA
Continuous Drain Current (Note 6) $V_{GS} = 5V$ Steady $T_A = +25^{\circ}C$ State $T_A = +70^{\circ}C$			I <sub>D</sub>	300 240	mA
Maximum Continuous Body Diode Forward Current (Note 6)			Is	0.5	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	(Note 6)		I <sub>DM</sub>	700	mA

### Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)		PD	370	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{ heta JA}$	335	°C/W
Total Power Dissipation (Note 6)		PD	475	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>0</sub> JA	261	°C/W
Thermal Resistance, Junction to Case (Note 6)		R <sub>eJC</sub>	106	- C/VV
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

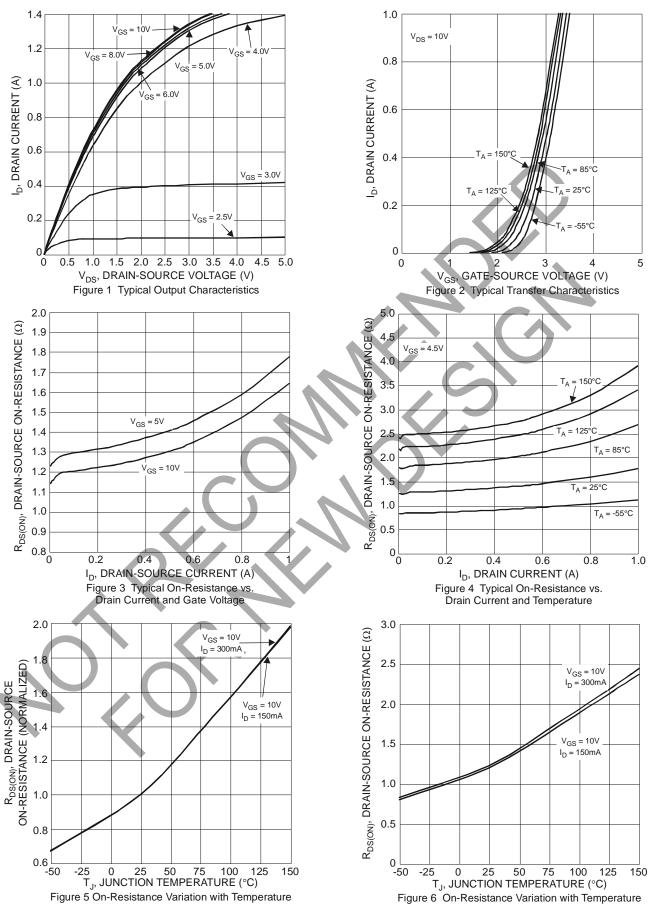
### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60		_	V	$V_{GS} = 0V, I_D = 10\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub> 4	1-1	4	1.0	μA	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	-11	_	±10	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	1.0		2.5	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$
Static Drain-Source On-Resistance	P			2	Ω	$V_{GS} = 10V, I_D = 0.5A$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_		3	12	$V_{GS} = 5V, I_D = 0.05A$
Forward Transfer Admittance	Y <sub>fs</sub>	80			ms	$V_{DS} = 10V, I_D = 0.2A$
Diode Forward Voltage	V <sub>SD</sub>	_	0.75	1.1	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C <sub>iss</sub>	_	56		pF	
Output Capacitance	Coss	_	4.4		pF	$V_{DS} = 25V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C <sub>rss</sub>	_	3.1		pF	1 - 1.00112
Total Gate Charge	Qg	_	0.6	_	nC	451/1/ 401/
Gate-Source Charge	$Q_{gs}$	_	0.2	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$ $I_{D} = 250mA$
Gate-Drain Charge	$Q_{gd}$	_	0.1	_	nC	1D = 23011A
Turn-On Delay Time	t <sub>D(on)</sub>		3.0		ns	
Turn-On Rise Time	t <sub>r</sub>	_	2.8	_	ns	V <sub>DD</sub> = 30V, V <sub>GS</sub> = 10V,
Turn-Off Delay Time	t <sub>D(off)</sub>	_	17.4	_	ns	$R_G = 25\Omega$ , $I_D = 200mA$
Turn-Off Fall Time	t <sub>f</sub>	_	8.1	_	ns	

Notes:

- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout
- Device mounted on 1" x 1" FR-4 PCB, with minimum recommended pad layout
   Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
   Short duration pulse test used to minimize self-heating effect.
   Guaranteed by design. Not subject to product testing.







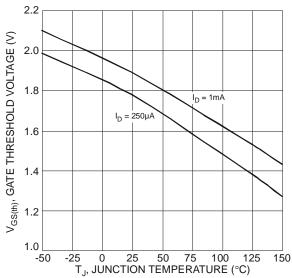
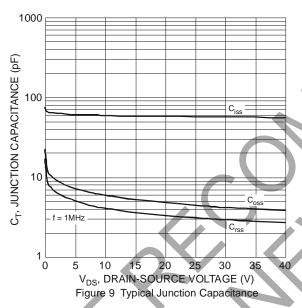
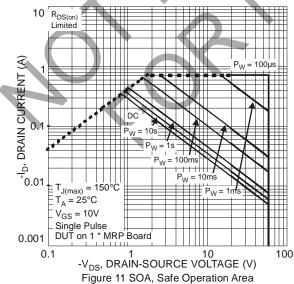
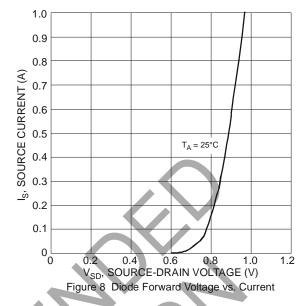
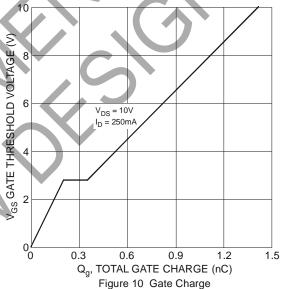


Figure 7 Gate Threshold Variation vs. Ambient Temperature





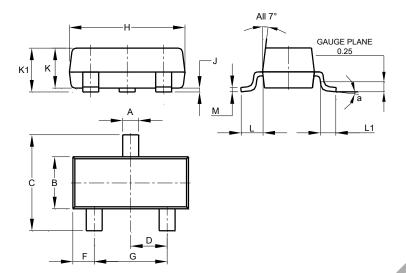






## **Package Outline Dimensions**

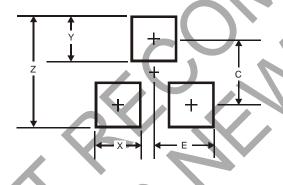
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Н	2.80	3.00	2.90			
J	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K1	0.903	1.10	1.025			
L	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
α		8°				
All Dimensions in mm						

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Υ	0.9
С	2.0
F	1.35



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