



#### **VOLTAGE REFERENCE ARRAY**

#### **Features**

- **Epitaxial Planar Die Construction**
- Ideally Suited for Automated Assembly Processes
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

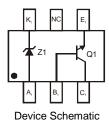
# **Mechanical Data**

- Case: SOT26 (SC74R)
- Case Material: Molded Plastic, "Green" Molding Compound. • UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Matte Tin Finish Annealed over Copper Leadframe • (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.008 grams (Approximate)

#### SOT26 (SC74R)



Top View



#### Ordering Information (Note 4)

Part Number	Case	Packaging
DVRN6056-7-F	SOT26 (SC74R)	3,000/Tape & Reel

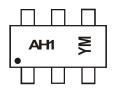
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3).compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony free, "Green" and

Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

### **Marking Information**



AH1 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: F = 2018)M = Month (ex: 9 = September)

Date Code Kev

Bale Coucilla	1								-				
Year	2003	2004	2005		2012	2013	2014	201	5 201	6 2017	2018	2019	2020
Code	Р	R	S		Z	А	В	С	D	E	F	G	Н
Month	Jan	Feb	Mar	Apr	Ma	y Jı	ın	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	3	7	8	9	0	Ν	D



# Maximum Ratings, NPN Transistor Element (Q1) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	60	V
Collector-Emitter Voltage	V <sub>CEO</sub>	40	V
Emitter-Base Voltage	V <sub>EBO</sub>	6.0	V
Collector Current - Continuous (Note 5	lc	600	mA

# Maximum Ratings, Zener Element (Z1) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Forward Voltage	@ I <sub>F</sub> = 10mA	VF	0.9	V

### **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Power Dissipation	(Note 5)	PD	300	mW
Thermal Resistance, Junction to Ambient	(Note 5)	R <sub>0JA</sub>	417	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Note: 5. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.

#### Electrical Characteristics, NPN Transistor Element (Q1) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Max	Unit	Test Condition		
OFF CHARACTERISTICS (Note 6)							
Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	60		V	$I_{\rm C} = 100 \mu A, I_{\rm E} = 0$		
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	40		V	$I_{\rm C} = 1.0 {\rm mA}, I_{\rm B} = 0$		
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	6	_	V	$I_{E} = 100 \mu A, I_{C} = 0$		
Collector Cutoff Current	ICEX	_	100	nA	$V_{CE} = 35V, V_{EB(OFF)} = 0.4V$		
Base Cutoff Current	I <sub>BL</sub>	_	100	nA	$V_{CE} = 35V, V_{EB(OFF)} = 0.4V$		
ON CHARACTERISTICS (Note 6)							
		20			$I_{C} = 100 \mu A, V_{CE} = 1.0 V$		
		40	—		$I_{C} = 1.0 \text{mA}, V_{CE} = 1.0 \text{V}$		
DC Current Gain	h <sub>FE</sub>	80	—		$I_{C} = 10 \text{mA}, V_{CE} = 1.0 \text{V}$		
		100	300		$I_{C} = 150 \text{mA}, V_{CE} = 1.0 \text{V}$		
		40			$I_{C} = 500 \text{mA}, V_{CE} = 2.0 \text{V}$		
Collector-Emitter Saturation Voltage	Verser		0.40	V	I <sub>C</sub> = 150mA, I <sub>B</sub> = 15mA		
	V <sub>CE(SAT)</sub>		0.75	v	$I_{C} = 500 \text{mA}, I_{B} = 50 \text{mA}$		
Base-Emitter Saturation Voltage	Variation	0.75	0.95	V	I <sub>C</sub> = 150mA, I <sub>B</sub> = 15mA		
	V <sub>BE(SAT)</sub>	_	1.2	v	$I_{C} = 500 \text{mA}, I_{B} = 50 \text{mA}$		

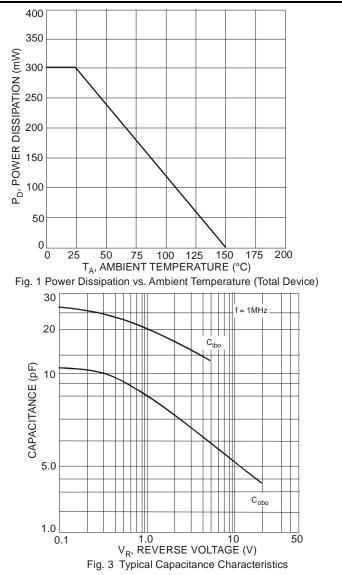
# Electrical Characteristics, Zener Element (Z1) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

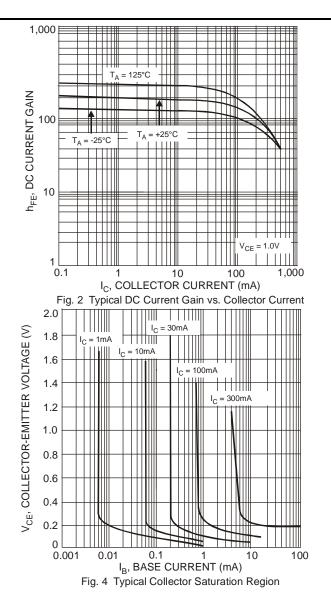
Zener Voltage Range (Note 6)			Maxir	num Zener Impedance	Maximum Reverse Leakage Current (Note 6)		
	Vz @ Izt Izt			Z <sub>ZT</sub> @ I <sub>ZT</sub>	Z <sub>ZK</sub> @ I <sub>ZK</sub> = 0.5mA	IR	@ V <sub>R</sub>
Nom (V)	Min (V)	Max (V)	mA	Ω		μA	V
5.6	5.49	5.73	5	60	200	1.0	2.5

Note: 6. Short duration pulse test used to minimize self-heating effect.



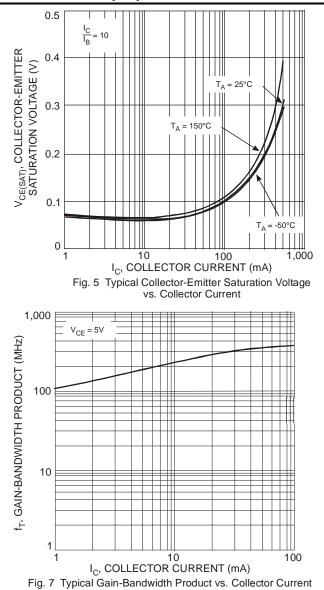
### **NPN Transistor (Q1)**

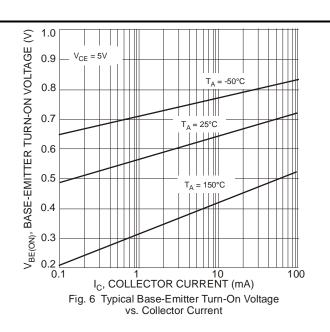






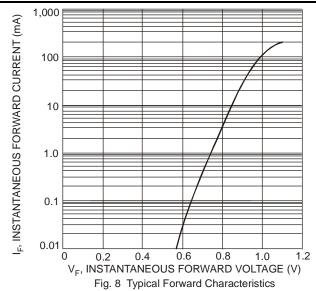
# NPN Transistor (Q1) (Continued)







# Zener (Z1)



**DVRN6056** 

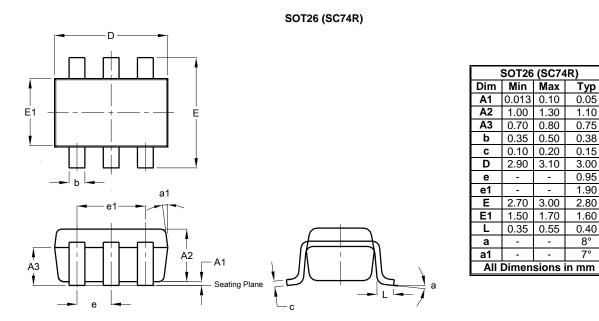


8°

7°

# **Package Outline Dimensions**

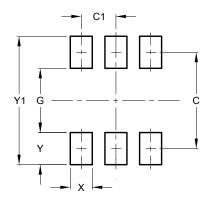
Please see http://www.diodes.com/package-outlines.html for the latest version.



### **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT26 (SC74R)



Dimensions	Value (in mm)
С	2.40
C1	0.95
G	1.60
Х	0.55
Y	0.80
Y1	3.20



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