



MMBT5401

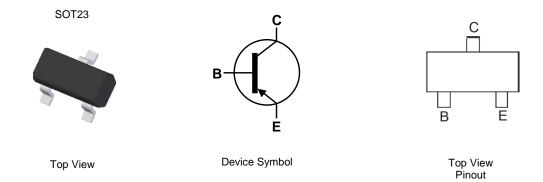
150V PNP HIGH-VOLTAGE TRANSISTOR IN SOT23

Features

- Epitaxial Planar Die Construction
- Complementary NPN Type MMBT5551
- Ideal for Low Power Amplification and Switching
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
 - https://www.diodes.com/quality/product-definitions/
- An automotive-compliant part is available under separate datasheet (MMBT5401Q)

Mechanical Data

- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 ⁽³⁾
- Weight: 0.008 grams (Approximate)



Ordering Information (Note 4)

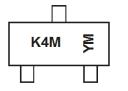
Part Number	r Package Marking Reel Size (inches)			Tape Width (mm)	Packing		
	Fackage	Marking	Reel Size (Inches)	Tape width (min)	Qty.	Carrier	
MMBT5401-7-F	SOT23	K4M	7	8	3,000	Reel	
MMBT5401-13-F	SOT23	K4M	13	8	10,000	Reel	

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



K4M = Product Type Marking Code YM = Date Code Marking Y = Year (ex: L = 2024) M = Month (ex: 3 = March)

Date Code Key

Notes:

Year	2017	-	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	E	-	L	М	Ν	Р	R	S	Т	U	V	W
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	2	4	Б	6	7	Q	0	0	N	Р



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	Vсво	-160	V
Collector-Emitter Voltage	V _{CEO}	-150	V
Emitter-Base Voltage	Vebo	-5	V
Collector Current	lc	-600	mA

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Power Dissipation	(Note 5)	D-	310	mW
	(Note 6)	pte 6) PD	350	TTIVV
Thermal Registeres, Junction to Ambient	(Note 5)	Devi	403	°C/W
Thermal Resistance, Junction to Ambient	(Note 6)	Reja	357	°C/W
Thermal Resistance, Junction to Leads (Note 7)		Rejl	350	°C/W
Operating and Storage Temperature Range	TJ,TSTG	-55 to +150	°C	

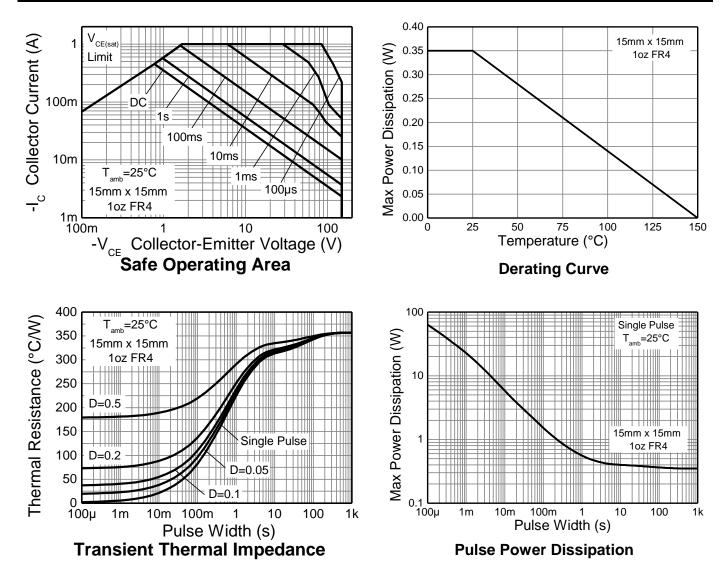
ESD Ratings (Note 8)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Notes: 5. For a device mounted on minimum recommended pad layout 1oz copper that is on a single-sided FR-4 PCB; device is measured under still air So for a device income of minimum recommended paralytic for copper that is conditions whilst operating in a steady-state.
Same as note (5), except the device is mounted on 15mm x 15mm 1oz copper.
Thermal resistance from junction to solder-point (at the end of the leads).
Refer to JEDEC specification JESD22-A114 and JESD22-A115.



Thermal Characteristics and Derating Information





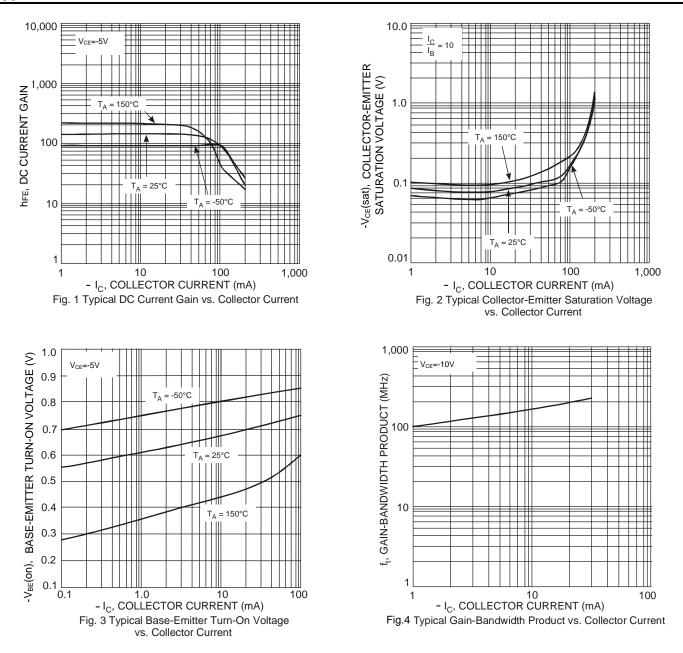
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 9)					
Collector-Base Breakdown Voltage	ВУсво	-160	_	V	$I_{C} = -100 \mu A, I_{E} = 0$
Collector-Emitter Breakdown Voltage	BVCEO	-150	_	V	$I_{C} = -1mA, I_{B} = 0$
Emitter-Base Breakdown Voltage	BVEBO	-5	_	V	$I_E = -100 \mu A$, $I_C = 0$
Collector Cutoff Current	Ісво		-50	nA	$V_{CB} = -120V, I_E = 0$
	ICBO		-50	μA	$V_{CB} = -120V, I_E = 0, T_A = +100^{\circ}C$
Emitter Cutoff Current	IEBO	_	-50	nA	$V_{EB} = -4V, I_{C} = 0$
ON CHARACTERISTICS (Note 9)		-		r	
		50	_		$I_{C} = -1mA$, $V_{CE} = -5V$
DC Current Gain	h _{FE}	60	240	—	$I_{C} = -10 \text{mA}, V_{CE} = -5 \text{V}$
		50			$I_{C} = -50 \text{mA}, V_{CE} = -5 \text{V}$
Collector Emitter Seturation Voltage	Ver	—	-0.2	V	$I_{C} = -10mA$, $I_{B} = -1mA$
Collector-Emitter Saturation Voltage	VCE(sat)		-0.5		$I_{C} = -50 \text{mA}, I_{B} = -5 \text{mA}$
Base-Emitter Saturation Voltage		—	-1	V	$I_{C} = -10mA$, $I_{B} = -1mA$
Dase-Emiller Saturation Voltage	V _{BE(sat)}				$I_{C} = -50 \text{mA}, I_{B} = -5 \text{mA}$
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	Cobo	_	6	pF	$V_{CB} = -10V, f = 1MHz, I_E = 0$
Small Signal Current Gain	hfe	40	260		$V_{CE} = -10V, I_{C} = -1mA,$
		-			f = 1kHz
Current Gain-Bandwidth Product	fτ	100	300	MHz	$V_{CE} = -10V, I_C = -10mA,$ f = 100MHz
Noise Figure	NF		8.0	dB	Vce = -5V, Ic = -200µA,
			0.0	uБ	$R_S = 10\Omega$, f = 1kHz

Note: 9. Measured under pulsed conditions. Pulse width \leq 300µs. Duty cycle \leq 2%.



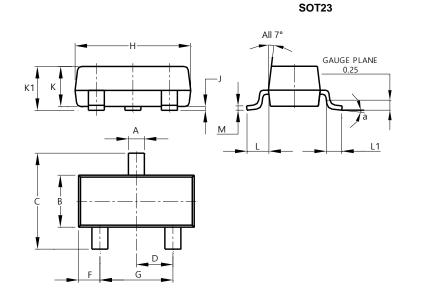
Typical Electrical Characteristics (@TA = +25°C, unless otherwise specified.)





Package Outline Dimensions

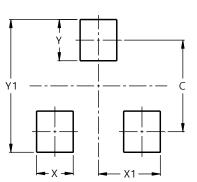
Please see http://www.diodes.com/package-outlines.html for the latest version.



	SOT23						
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
в	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
ĸ	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	0°	8°					
All	Dimens	ions in	mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23

Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.



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