



A Product Line of **Diodes Incorporated**

LITE-ON SEMICONDUCTOR

LSC10065DW

SILICON CARBIDE SCHOTTKY DIODE		REVERSE VOLTAGE – 650 Volts FORWARD CURRENT – 10 Amperes
 FEATURES Positive temperature coefficient for save operation and easy of paralleling 175°C maximum operating junction temperature Extremely fast switching not dependent on temperature Qualified according to AEC-Q101 Rev_D Lead-Free Finish; RoHS Compliant (Notes 1 & 2) Halogen and Antimony Free. "Green" Device (Note 3) APPLICATION Switch mode power supplies Power factor corrections MECHANICAL DATA Package Material: "Green" molding compound, UL flammer classification 94V-0, "Halogen-free". Moisture Sensitivity: Level 1 per J-STD-020 Terminals: Matte Tin Finish Annealed over Copper Leadfred (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (19) Weight: 0.989 grams (Approximate) Marking Code: LSC10065DW 	ability	DPAKImage: Displaying the second seco
MAXIMUM RATINGS AND ELECTRICAL CHARACTER Ratings at 25°C ambient temperature unless otherwise s		
ABSOLUTE RATINGS	peomou.	
PARAMETER	SYMBOL	VALUE UNIT

PARAMETER		SYMBOL	VALUE	UNIT
Maximum repetitive peak reverse voltage		V _{RRM}	650	V
Maximum DC blocking voltage		V _{DC}	650	V
Maximum average rectified output current	@T _c =130°C	I _(AV)	10	A
Peak forward surge current 8.3ms single half sine-v superimposed on rated load	vave	I _{FSM}	60	А
Operating junction and storage temperature range		$T_{J_i} T_{STG}$	-55 ~ +175	°C
STATIC ELECTRICAL CHARACTERIST	FICS			

PARAMETER	TEST CO	NDITIONS	SYMBOL	TYP	MAX	UNIT
Forward voltage (Note 4)	I _F =10A	Tյ=25°C Tյ=175°C	V _F	 2.05	1.70 2.25	V
Reverse leakage current	V _R =650V	TJ=25°C TJ=175°C	I _R	 32.8	250 800	μA

DYNAMIC ELECTRICAL CHARACTERISTICS

PARAM	ETER	SYMBOL	ТҮР	UNIT
Total Capacitive Charge	V _R =400V,dI/dt=250A/µS, I _F =10A	Q _C	23	nC
Typical junction capacitance (Note 5))	CJ	330	pF

THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	ТҮР	UNIT
Typical thermal resistance (Notes 6, 7)	RthJ _c	2	°C/W
Typical thermal resistance (Notes 6, 7)	RthJ∟	3	C/VV

Notes:

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.

See https://www.uoues.com/quality/lead-tree/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. 300µs pulse width, 2% duty cycle.
 5. Measured at 1.0MHz and applied voltage of 1.0V DC.

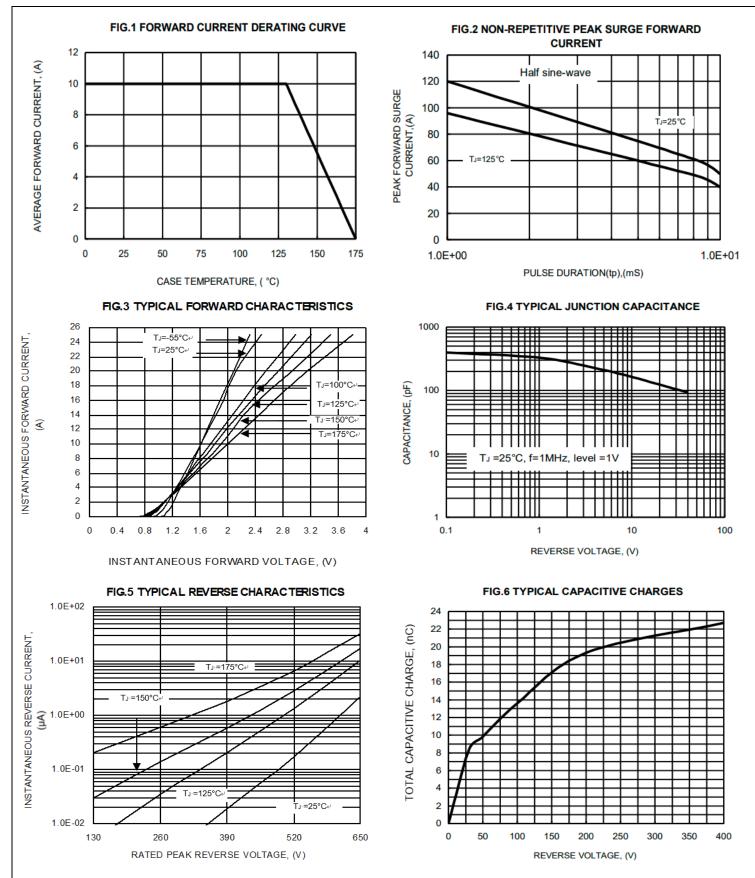
6. Thermal resistance test performed in accordance with JESD-51.

7. The unit mounted on fin-type Heatsink (75mm x 100mm x 26.8mm).



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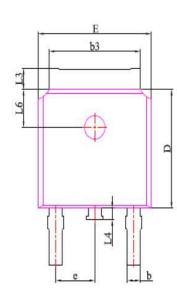
RATING AND CHARACTERISTIC CURVES LSC10065DW

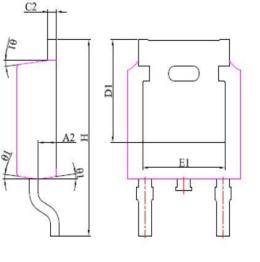


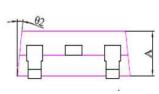


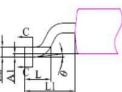
LITE-ON SEMICONDUCTOR

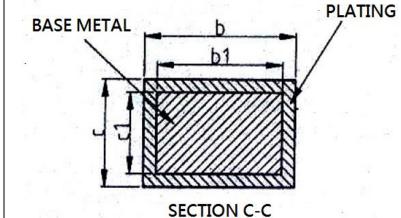
Package Dimension:











MIN	TYP	MAX
		IVIAA
2.20	2.30	2.40
0.00		0.15
0.97	1.07	1.17
0.68	0.78	0.90
0.66	0.76	0.88
5.20	5.33	5.50
0.43	0.53	0.63
0.41	0.51	0.61
0.43	0.53	0.63
5.98	6.10	6.22
	5.30 REF	
6.40	6.60	6.80
4.63	4.83	5.03
	2.286 REF	2.11
9.40	10.10	10.50
1.38	1.50	1.75
	2.90 REF	
	0.51 BSC	
0.88		1.28
(1)		1.00
1.65	1.80	1.95
		8 °
5 °	7°	9°
5°	7 °	9°
	0.97 0.68 0.66 5.20 0.43 0.41 0.43 5.98 6.40 4.63 9.40 1.38 1.65 0° 5° 5°	0.97 1.07 0.68 0.78 0.66 0.76 5.20 5.33 0.43 0.53 0.44 0.51 0.43 0.53 0.44 0.51 0.43 0.53 5.98 6.10 5.30 REF 6.40 6.60 4.63 4.83 2.286 REF 9.40 10.10 1.38 1.50 2.90 REF 0.51 BSC 0.88 1.65 1.80 0° 5° 7°



Ordering Information:

Part Number Package Qty. Carrier LSC10065DW DPAK 2500pcs Tape & Ree Information: Image: Constraint of the second secon	Qty. Carrier LSC10065DW DPAK 2500pcs Tape & Ree ng Information:
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Logo LT YMWW LSC10065DW Y = Year Code M = Assembly Code WW = Week Code Product Type Marking Code	Logo LT YMWW LSC10065DW V = Year Code WW = Assembly Code WW = Week Code Product Type Marking Code



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