



## **FB560D**

SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER	REVERSE VOLTAGE – 60 Volts FORWARD CURRENT – 5.0 Amperes		
SCHOTTKY BARRIER RECTIFIER         FEATURES         • Very low profile package         • High efficiency         • Extremely fast switching         • Negligible switching losses         • Low forward voltage drop, low power loss         • Qualified to AEC-Q101 Rev_C         • Lead-Free Finish; RoHS Compliant (Notes 1 & 2)         • Halogen and Antimony Free. "Green" Device (Note 3)         APPLICATION         • Low voltage high frequency inverters         • DC-DC converters         • Polarity protection applications         MECHANICAL DATA	F3-D           F3-D           DIM         MIN           TYP         M.           A         4.80         5.20           B         2.25         2.80         2.           C         0.90         1.00         1.           D         3.95         4.20         4.           E         1.25         1.50         1.           F         0.15         0.20         0.           G         0.75         1.00         1.	AX 60 95 10 65 40 50 075	
Package: JEDEC DO-221AC		20	
<ul> <li>Package Material: "Green" molding compound, UL flammability classification 94V-0, "Halogen-free".</li> <li>Moisture Sensitivity: Level 1 per J-STD-020</li> <li>Lead free finish, RoHS compliant</li> <li>Weight: 0.0354 grams (Approximate)</li> <li>Marking code: B560</li> </ul>	All dimension in millimet	ter	

#### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

#### **ABSOLUTE RATINGS**

PARAMETER		SYMBOL	VALUE	UNIT
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	60	V
Maximum DC blocking voltage		V <sub>DC</sub>	60	V
Maximum Average rectified output current	@T <sub>c</sub> =95°C	I <sub>(AV)</sub>	5	А
Peak forward surge current 8.3ms single half sine-wave Superimposed on rated load.		I <sub>FSM</sub>	140	А
Operating junction and Storage temperature range		$T_{J,} T_{STG}$	-55 to +150	°C

#### STATIC ELECTRICAL CHARACTERISTICS

PARAMETER	TEST CONDITIONS		SYMBOL	ТҮР	MAX	UNIT
Forward voltage (Note 4)	I <sub>F</sub> =5.0A	T <sub>J</sub> =25°C T <sub>J</sub> =125°C	V <sub>F</sub>	0.553	0.675	V
Leakage current	V <sub>R</sub> =60V	T <sub>J</sub> =25°C T <sub>J</sub> =125°C	I <sub>R</sub>	 5.15	25 5	uA mA
Typical junction capacitance (Note 5)		CJ	215		pF	

#### THERMAL CHARACTERISTICS

PARAMETER	SYMBOL	ТҮР	UNIT
Typical thermal resistance (Notes 6, 7)	RthJ <sub>c</sub>	18	°C/W
Typical inernal resistance (Notes 0, 7)	RthJ <sub>a</sub>	40	

Notes:

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. 300us pulse width, 2% duty cycle.

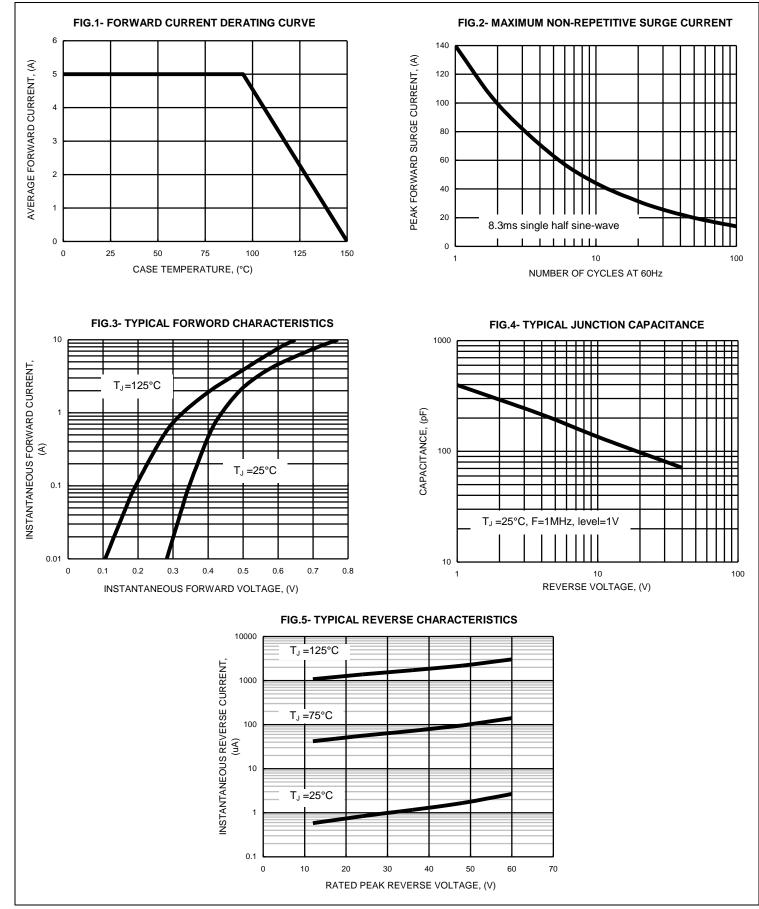
5. Measured at 1.0MHz and applied voltage of 4.0VDC.
 6. Thermal resistance test performed in accordance with JESD-51.

7. Unit mounted on glass-epoxy substrate with 1oz/ft<sup>2</sup>\_30 mm x 30 mm copper pad.

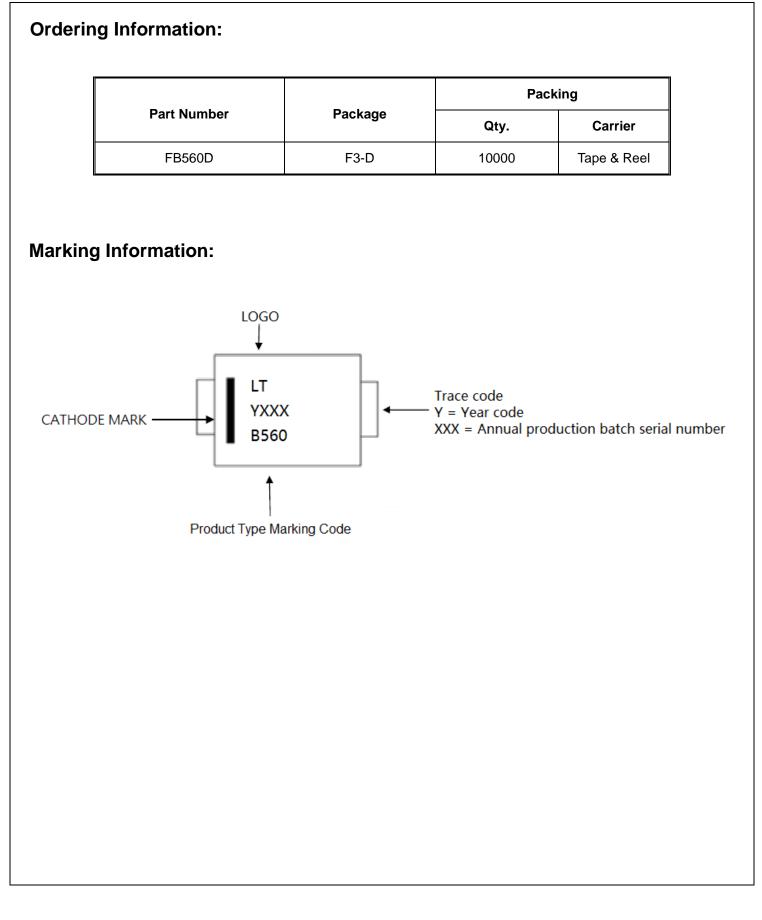


## RATING AND CHARACTERISTIC CURVES FB560D

#### LITE-ON SEMICONDUCTOR





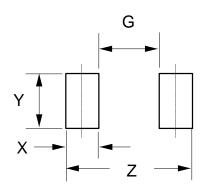




# MECHANICAL INFORMATION FB560D

#### LITE-ON SEMICONDUCTOR

### SUGGESTED PAD LAYOUT:



Dim	DO-221AC	
G	2.90	
X	2.30	
Y	2.40	
Z	7.50	
All Dimensions in Millimeters		



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